MINED BY :		FILE NO . CAS-0006801
Vincent Uh	EMERGING DISPLAY	ISSUE : AUG.20, 2009
ROVED BY:	TECHNOLOGIES CORPORATION	TOTAL PAGE: 31
David Chang		VERSION : 5
CUSTOMER	ACCEPTANCE SPEC	CIFICATIONS
MO FOR CUSTOMER'S APPROV	DEL NO.: <u>ET0500G0DH6</u> (GP) MESSRS:	
DATE :		
BY :		

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			DOC . FIRST ISSUE		
RECORD		EVISION		J	AN.16, 2009
DATE	REVISED PAGE NO.		S U M M A R Y		
FEB.17, 2009	3	4. ELECTRICAL (	CHARACTERISTICS	REMARK	
		POWER SUPPLY CURRENT FOR LED DRIVER	VCC VCC		
		PARAMETER POWER SUPPLY CURRENT FOR LED DRIVER		REMARK	
	15	ADD 11.2 THE BE OF LEDCTRL.	RIGHTNESS CONTROLLED BY BAC	CKLIGHT CUI	RRENT
MAR.09, 2009	5		T FORMAT ST HORIZONTAL DATA TIME : STI 3 TIME : STVD[6:0]+8 →27+8	HD[7:0]+88 <sup>(1)</sup> -	$\rightarrow$ 128+88 <sup>(1)</sup>
	10	7. OUTLINE DIM		MENSION	
	11	8. BLOCK DIAGR ADD FRAME C			
APR.09, 2009	10	7. OUTLINE DIM		PE	
AUG.20, 2009	3		CHARACTERISTICS .Y CURRENT∶TYP.=8→120, MAX.	=11→170	
		POWER SUPPL	Y CURRENT FOR LED DRIVER : 00, MAX.=(550)→650		
	10	7. OUTLINE DIM MARK 🖄 : MO	ENSIONS		

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		Н6	5	1
1. GENERAL SPECIFICATION				
1.1 APPLICATION NOTES PLEASE REFER TO :	FOR CONTROLLER/DRIVE	ER		
	M A X H X 8 2 5 8 M A X H X 8 6 6 2			
REQUIREMENTS, INCLU CONTAINING LEAD, ME POLYBROMINATED BIP DIPHENYL ETHERS (PE CATEGORY, POLYCHLO POLYCHLORINATED TE PARAFFINS (CP) CATEG TIN CATEGORY, ASBES FORMALDEHYDE, POLY	OMPLY WITH EDT GREEN UDING PROHIBITED MATE ERCURY, CADMIUM, HEXA PHENYLS (PBB), POLYBRO 3DE), POLYCHLORINATED ORINATED NAPHTHALENE ERPHENYLS (PCT) CATEGO GORY, TRIBUTHYL TIN CA STOS, SPECIFIC AZO COMP YVINYL CHLORIDE (PVC) A ORGANIC COMPOUNDS AN	RIALS/CO AVALENT MINATED BIPHENY E (PCN) CA DRY, CHLO TEGORY / OUNDS, AND PVC 1	MPONE CHROM LS (PCB TEGOR DRINATI TRIPHE BLENDS	IUM, ) Y, ED NYL S,
2. MECHANICAL SPECIFICAT				
(1) DIAGONALS			0.550	
(2) NUMBER OF DOTS				
(3) MODULE SIZE	(WITHOUT)		MAX.) mn	n
		,	D)	
(4) EFFECTIVE AREA			,	
(4) EFFECTIVE AREA (5) ACTIVE AREA				
(4) EFFECTIVE AREA (5) ACTIVE AREA	108W * 64.8	$\frac{311}{311} \frac{1}{1} \frac{1}{1} \frac{1}{1}$	)	
(5) ACTIVE AREA	108W * 64.8 0.045W * 0.	3H mm (LCD	)	
(5) ACTIVE AREA	0.045W * 0.	3H mm (LCD 135H mm	)	
( 5 ) ACTIVE AREA	0.045W * 0. 0.135W * 0.	8H mm (LCD 135H mm 135H mm	~	ARE
<ul> <li>(5) ACTIVE AREA</li> <li>(6) DOT SIZE</li> <li>(7) PIXEL PITCH</li> <li>(8) LCD TYPE</li> </ul>	0.045W * 0. 0.135W * 0.	8H mm (LCD 135H mm 135H mm	~	ARE
<ul> <li>(5) ACTIVE AREA</li> <li>(6) DOT SIZE</li> <li>(7) PIXEL PITCH</li> <li>(8) LCD TYPE</li> </ul>	0.045W * 0. 0.135W * 0. TFT , TRAN 262K	8H mm (LCD 135H mm 135H mm ISMISSIVE ,	~	ARE
<ul> <li>(5) ACTIVE AREA</li> <li>(6) DOT SIZE</li> <li>(7) PIXEL PITCH</li> <li>(8) LCD TYPE</li> <li>(9) COLOR</li> </ul>	0.045W * 0. 0.135W * 0. TFT , TRAN 262K 	8H mm (LCD 135H mm 135H mm ISMISSIVE , K	~	ARE
<ul> <li>(5) ACTIVE AREA</li></ul>	0.045W * 0. 0.135W * 0. TFT , TRAN 262K 6 O'CLOCH LED , COLO	8H mm (LCD 135H mm 135H mm ISMISSIVE , K OR : WHITE	~	ARE

# MODEL NO. E T 0 5 0 0 G 0 D H 6

## 3. ABSOLUTE MAXIMUM RATINGS

## 3.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS.

PARAMETER	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS	-0.3	7.0	V	
INPUT SIGNAL VOLTAGE	VIN-VSS	- 0.3	VDD+0.3	V	
STATIC ELECTRICITY				V	NOTE (1)
LED BACKLIGHT POWER DISSIPATION	PD		1008	mW	
LED BACKLIGHT FORWARD CURRENT	IF		60	mA	
LED BACKLIGHT REVERSE VOLTAGE	VR		50	V	

NOTE (1) : LCM SHOULD BE GROUNDED DURING HANDING LCM.

## 3.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS.

ITEM	OPERATING		STOF	RAGE	REMARK	
	MIN.	MAX.	MIN.	MAX.	KEMAKK	
AMBIENT TEMPERATURE	-10°C	60°C	-20°C	70°C	NOTE (1), (2)	
HUMIDITY	NOTI	$\Xi(2)$	NOTI	E(2)	WITHOUT	
	NOT	2(3)	NOT	E(3)	CONDENSATION	
VIBRATION		$2.45 \text{ m/s}^2$		11.76 m/s <sup>2</sup>	10~55Hz	
VIDKATION		(0.25 G)		(1.2G)	X,Y,Z, EACH 2HRS	
					6 m SECONDS	
SHOCK		$29.4 \text{ m/s}^2$		490 m/s <sup>2</sup>	XYZ	
SHOCK		(3G)		(50G)	DIRECTIONS	
					3 TIMES EACH	
CORROSIVE GAS	NOT ACC	EPTABLE	NOT ACC	EPTABLE		

NOTE (1) : Ta AT -20°C : 48HRS MAX.

70°C:168HRS MAX.

NOTE (2) : BACKGROUND COLOR CHANGES SLIGHTLY DEPENDING ON AMBIENT TEMPERATURE THIS PHENOMENON IS REVERSIBLE .

NOTE (3) : Ta  $\leq 40^{\circ}\text{C}$  : 90%RH MAX (96HRS MAX).

Ta > 40°C : ABSOLUTE HUMIDITY MUST BE LOWER THAN THE HUMIDITY OF 90%RH AT 40°C(96HRS MAX).

# MODEL NO. E T 0 5 0 0 G 0 D H 6

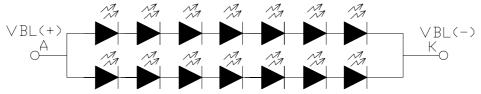
25.00

## 4. ELECTRICAL CHARACTERISTICS

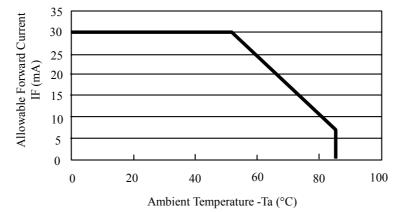
							$Ta = 25 \circ C$
PARAMETER	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS	_	3	3.3	3.6	V	
POWER SUPPLY VOLTAGE FOR LED DRIVER	VCC-VSS		2.7	3.3	3.6	V	
LOW LEVEL INPUT VOLTAGE	VIL	_	0		0.3*VDD	V	NOTE (1)
HIGH LEVEL INPUT VOLTAGE	VIH	_	0.7*VDD		VDD	V	NOTE (I)
POWER SUPPLY CURRENT	IDD	VDD-VSS =3.3V		120	170	mA	NOTE (2)
POWER SUPPLY CURRENT FOR LED DRIVER	ICC	VCC-VSS = 3.3V LED B/L=ON		500	650	mA	
POWER SUPPLY FOR LED BACKLIGHT	VBL(+)-VBL(-)	$I_F = 40 \text{mA}$		23.1		V	NOTE (3)
LED LIFE TIME			30000	40000		HRS	

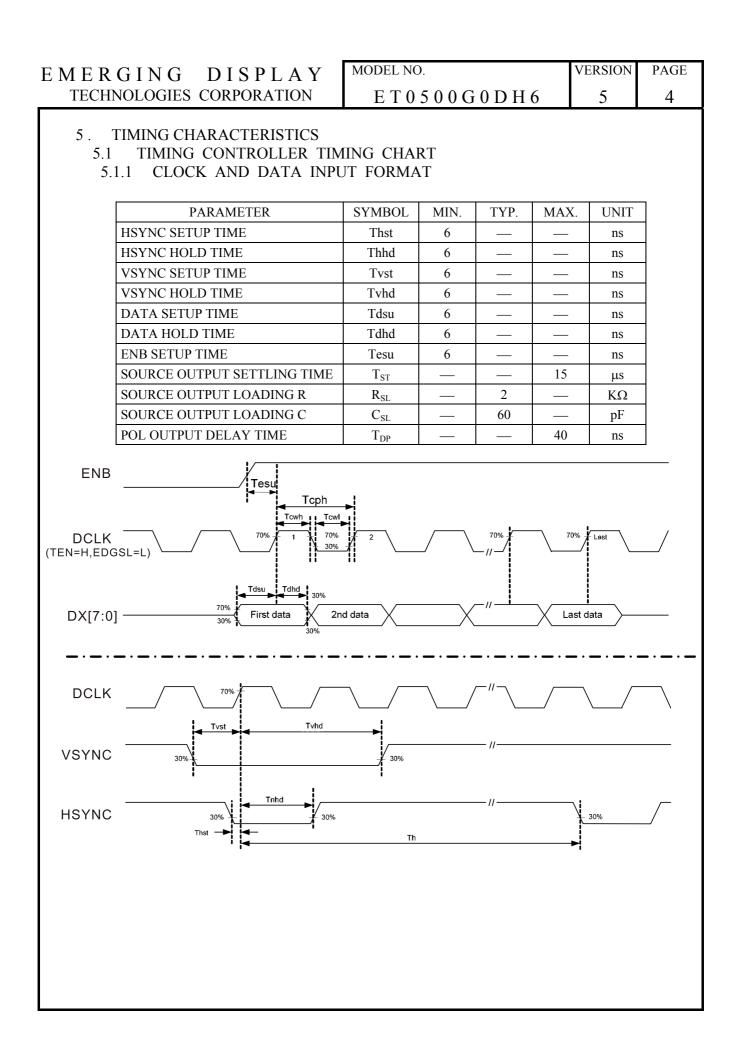
NOTE (1): APPLIED TO TERMINALS /RESET, HSYNC, VSYNC, ENB, DCLK, B5~B0, G5~G0, R5~R0. NOTE (2): THE DISPLAY PATTERN IS ALL "WHITE".

NOTE (3): INTERNAL CIRCUIT DIAGRAM OF BACKLIGHT



NOTE (4): AMBIENT TEMP. VS. ALLOWABLE FORWARD CURRENT.(PER LED)





# EMERGING DISPLAY TECHNOLOGIES CORPORATION

# MODEL NO. $E \ T \ 0 \ 5 \ 0 \ 0 \ G \ 0 \ D \ H \ 6$

#### 5.1.2 DATA INPUT FORMAT

## SYNC MODE

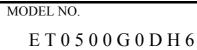
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT
DCLK FREQUENCY	F <sub>CPH</sub>	29.93	33.26	36.59	MHz
DCLK PERIOD	Т <sub>СРН</sub>	27.32	30.06	33.41	ns
DCLK PULSE DUTY	T <sub>CWH</sub>	40	50	60	%
HSYNC PERIOD	T <sub>H</sub>	950	1056	1600	T <sub>CPH</sub>
HSYNC PULSE WIDTH	$T_{WH}$	1	128	$T_{HS}$ -2	T <sub>CPH</sub>
HSYNC-FIRST HORIZONTAL DATA TIME	$T_{\rm HS}$	128+88 <sup>(1)</sup>			T <sub>CPH</sub>
HSYNC ACTIVE TIME	T <sub>HA</sub>		800	_	T <sub>CPH</sub>
VSYNC PERIOD	T <sub>v</sub>	490	525	625	$T_{\rm H}$
VSYNC PULSE WIDTH	T <sub>WV</sub>	1	2	$T_{VS}$	$T_{\rm H}$
VSYNC-ENB TIME	T <sub>VS</sub>		27+8		$T_{\rm H}$
VSYNC ACTIVE TIME	T <sub>VA</sub>		480		$T_{\mathrm{H}}$

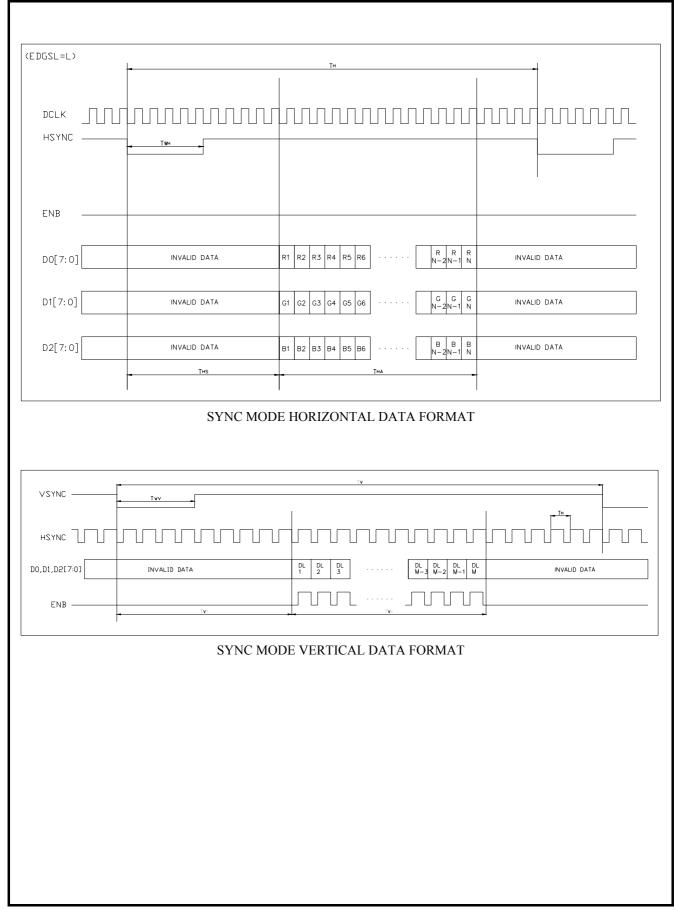
 $NOTE(1):T_{HS+}T_{HA} < T_{H}$ 

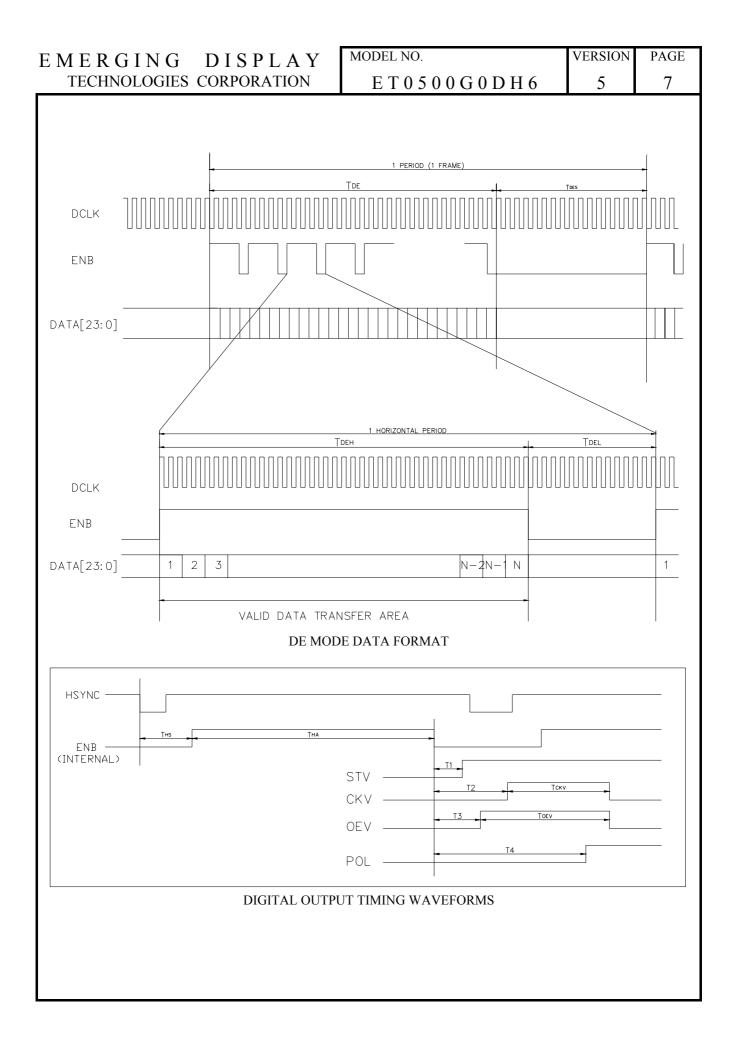
#### DE MODE

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT
DCLK FREQUENCY	F <sub>CPH</sub>	29.93	33.26	36.59	MHz
DCLK PERIOD	T <sub>CPH</sub>		30.06	_	ns
DCLK PULSE DUTY	T <sub>CWH</sub>	40	50	60	%
ENB PERIOD	$T_{DEH} + T_{DEL}$	1000	1056	1200	T <sub>CPH</sub>
ENB PULSE WIDTH	T <sub>DEH</sub>		800	_	T <sub>CPH</sub>
ENB FRAME BLANKING	T <sub>DEB</sub>	10	45	110	$T_{\rm DEH} + T_{\rm DEL}$
ENB FRAME WIDTH	$T_{DE}$		480		$T_{DEH} + T_{DEL}$

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT
OEV PULSE WIDTH	T <sub>OEV</sub>		150		T <sub>CPH</sub>
CKV PULSE WIDTH	T <sub>CKV</sub>		133		T <sub>CPH</sub>
ENB (INTERNAL)-STV TIME	T <sub>1</sub>		4		T <sub>CPH</sub>
ENB (INTERNAL)-CKV TIME	T <sub>2</sub>		40		T <sub>CPH</sub>
ENB (INTERNAL)-OEV TIME	T <sub>3</sub>		23		T <sub>CPH</sub>
ENB (INTERNAL)-POL TIME	$T_4$		157		T <sub>CPH</sub>
STV PULSE WIDTH			1		T <sub>H</sub>







# LAY MODEL NO. TION ET0500G0DH6

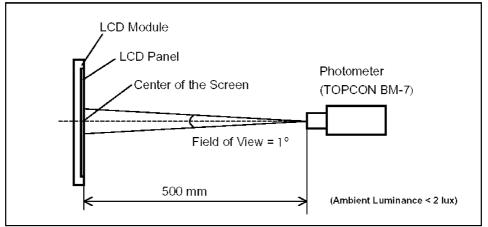
## 6. OPTICAL CHARACTERISTICS (NOTE 1) 6.1 OPTICAL CHARACTERISTICS

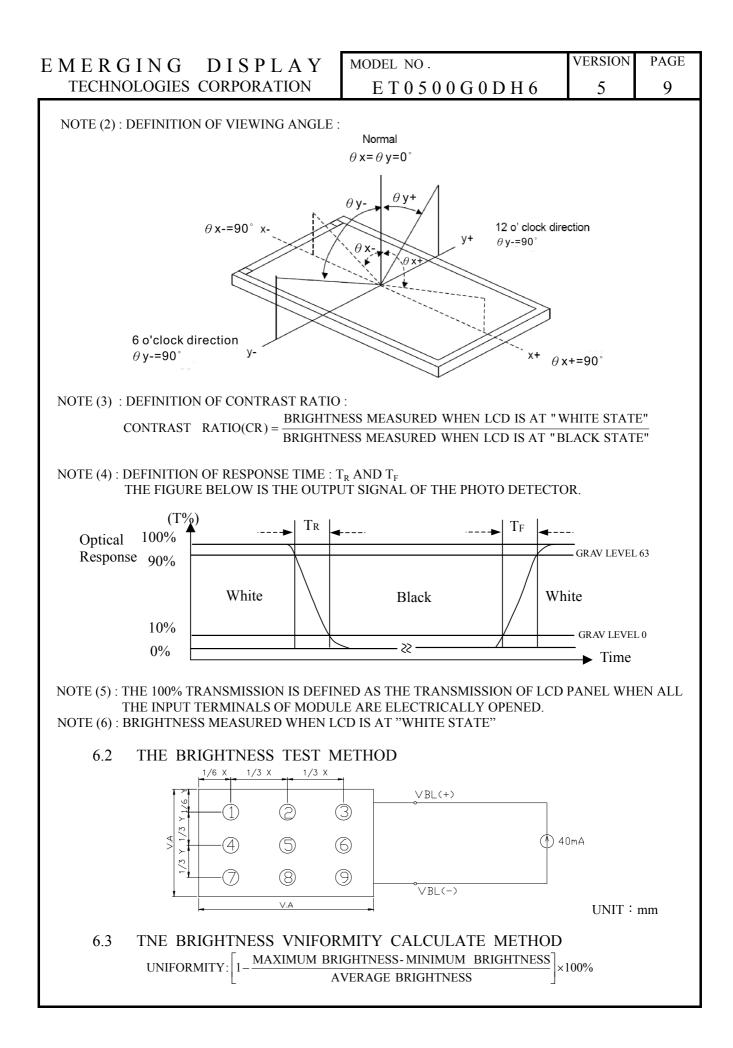
 $Ta = 25^{\circ}C$ 

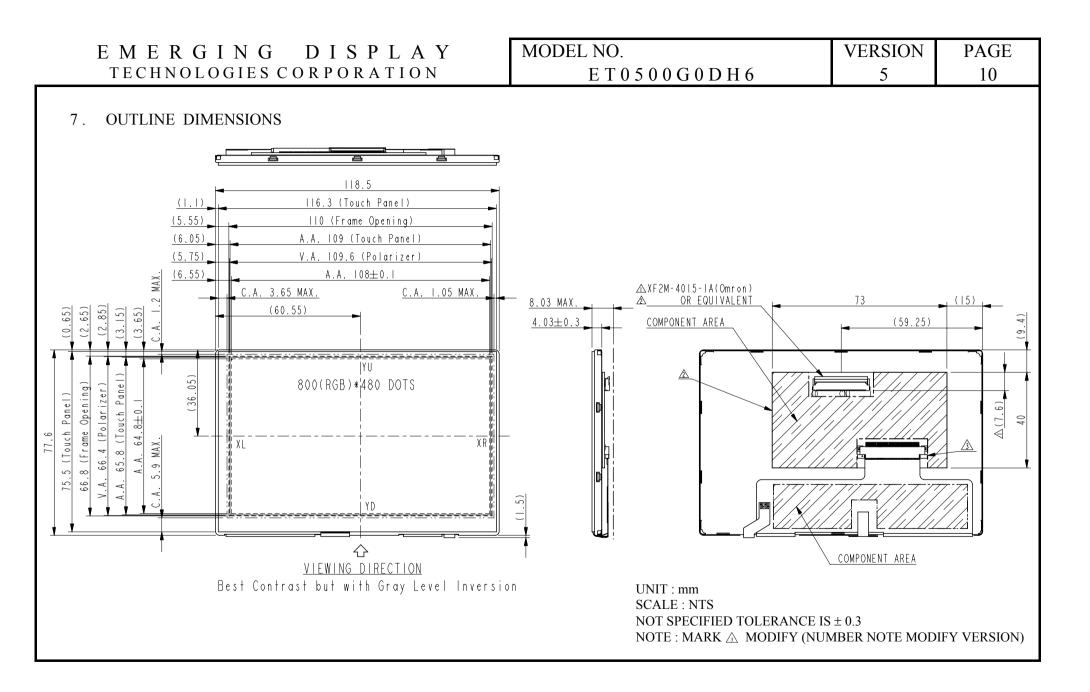
ITE	М	SYMBOL	COND	ITION	MIN.	TYP.	MAX.	UNIT	REMARK		
		$\theta_{y^{+}}$		θ <sub>x</sub> =0°		68					
VIEWING ANGL	Б	$\theta_{y}$	$CR \ge 10$	$\Theta_{\rm x} = 0$	61	68		deg.	NOTE (2)		
VIE WING ANGL	Ľ	$\theta_{x^+}$	$CK \ge 10$	θ <sub>v</sub> =0°	55	62	_	ueg.	NOTE (3)		
		$\theta_{x}$		0 <sub>y</sub> –0	58	65	_				
CONTRAST RAT	ΟI	CR	θx=0°,	θy=0°	350	450	_		NOTE (3)		
RESPONSE TIME	7	$T_R$ (rise)	$\Omega_{\rm v} = 0^{\circ}$	θy=0°		15	30	msec	NOTE (4)		
RESI ONSE TIMI	Ĺ.	$T_F(fall)$	01-0 ,	0y-0		35	50	msee	NOTE (4)		
	WHITE	Wx			0.261	0.311	0.361				
	WIIII L	Wy			0.284	0.334	0.384				
COLOD OF	RED	Rx	θx=0°,	$\theta x=0^{\circ}$ , IF = 4			0.556	0.606	0.656		
COLOR OF CIE	KED	Ry							0.314	0.364	0.414
COORDINATE	GREEN	Gx		: 55%	0.291	0.341	0.391		NOTE (5)		
COORDINATE	UKEEN	Gy			0.501	0.551	0.601				
	BLUE	Bx			0.094	0.144	0.194				
	BLUE	By			0.057	0.107	0.157				
THE BRIGHTNES OF MODULE	SS	В	θx=0°,	θx=0°, θy=0°		300		cd/m <sup>2</sup>	NOTE(6)		
THE UNIFORMIT MODULE	ΓY OF		$I_F = 4$	l0mA	70	75		%	110112(0)		

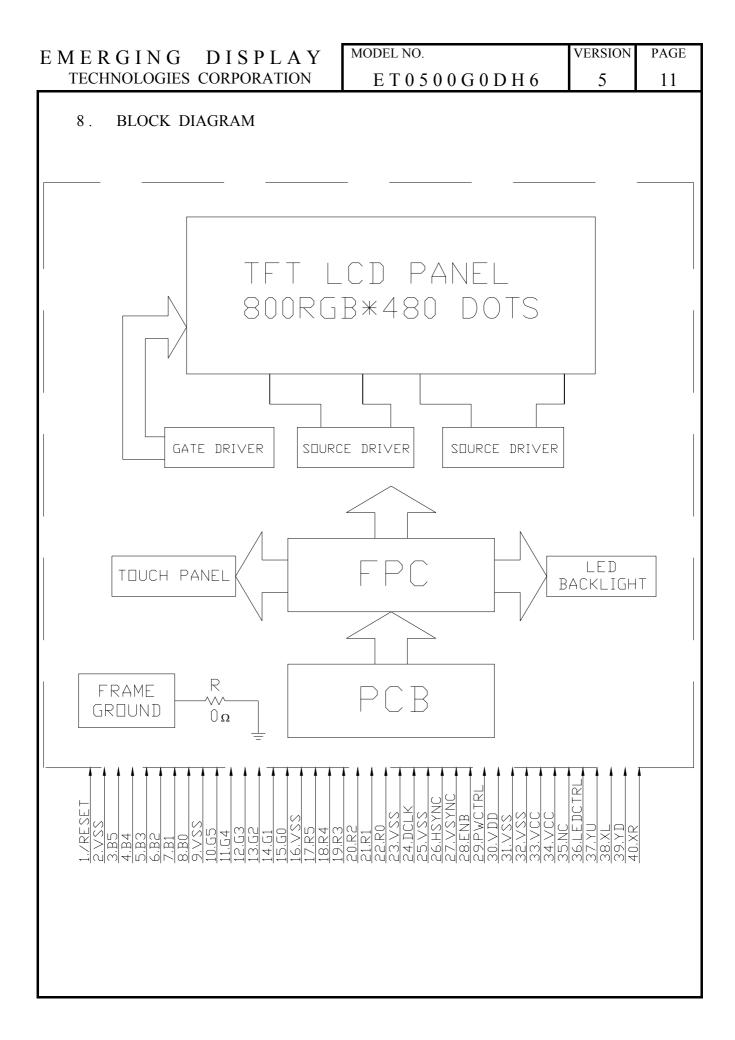
#### NOTE (1): TEST EQUIPMENT SETUP:

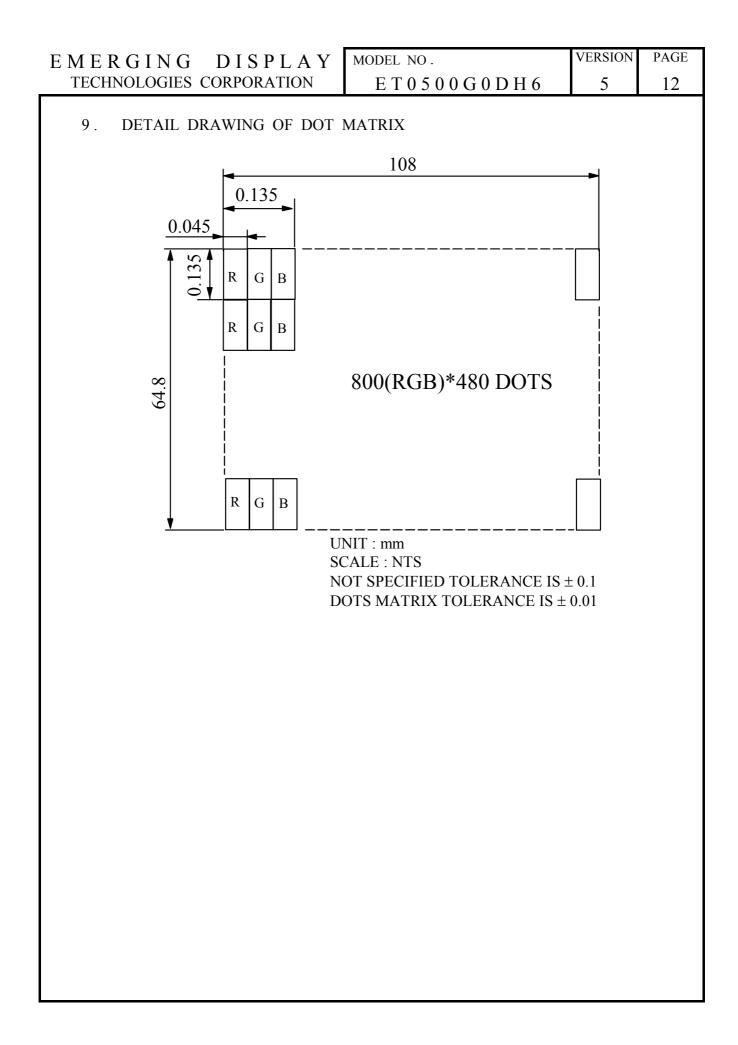
AFTER STABILIZING AND LEAVING THE PANEL ALONE AT A GIVEN TEMPERATURE FOR 30 MINUTES, THE MEASUREMENT SHOULD BE EXECUTED. MEASUREMENT SHOULD BE EXECUTED IN A STABLE, WINDLESS, AND DARK ROOM. OPTICAL SPECIFICATIONS ARE MEASURED BY TOPCON BM-7 (FAST) WITH A VIEWING ANGLE OF 1° AT A DISTANCE OF 50cm AND NORMAL DIRECTION.











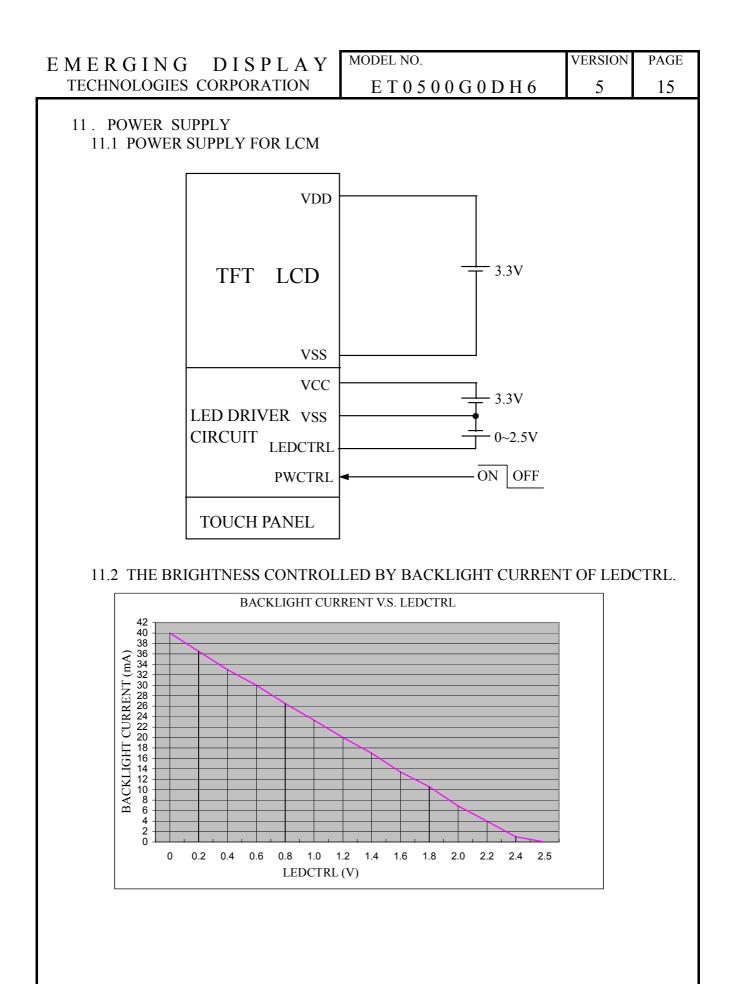
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## 10. INTERFACE SIGNALS

PIN NO	SYMBOL	I/O	FUNCTION			
1	/RESET	Ι	HARDWARE RESET			
2	VSS	Р	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)			
3	B5	Ι	BLUE DATA BIT 5			
4	B4	Ι	BLUE DATA BIT 4			
5	B3	Ι	BLUE DATA BIT 3			
6	B2	Ι	BLUE DATA BIT 2			
7	B1	Ι	BLUE DATA BIT 1			
8	B0	Ι	BLUE DATA BIT 0			
9	VSS	Р	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)			
10	G5	Ι	GREEN DATA BIT 5			
11	G4	Ι	GREEN DATA BIT 4			
12	G3	Ι	GREEN DATA BIT 3			
13	G2	Ι	GREEN DATA BIT 2			
14	G1	Ι	GREEN DATA BIT 1			
15	G0	Ι	GREEN DATA BIT 0			
16	VSS	Р	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)			
17	R5	Ι	RED DATA BIT 5			
18	R4	Ι	RED DATA BIT 4			
19	R3	Ι	RED DATA BIT 3			
20	R2	Ι	RED DATA BIT 2			
21	R1	Ι	RED DATA BIT 1			
22	R0	Ι	RED DATA BIT 0			
23	VSS	Р	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)			
24	DCLK	Ι	DOT DATA CLOCK			
25	VSS	Р	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)			
26	HSYNC	Ι	HORIZONTAL SYNC INPUT DE MODE : USE ENB AND DCLK; HSYNC AND VSYNC MUST BE PULL			
27	VSYNC	Ι	VERTICAL SYNC INPUT HIGH; SYNC MODE : USE HSYNC, VSYN			
28	ENB	Ι	DATA ENABLE INPUT AND DCLK, ENB MUST BE PULL LOW			
29	PWCTRL	Ι	PWCTRLREMARKLOGIC LEVEL H=3.3V L=0VHPOWER ONPOWER ON			
			WHEN INTERNAL LED DRIVER : JP4 1-2(DEFAULT) WHEN EXTERNAL LED DRIVER : JP4 2-3			

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PIN NO	SYMBOL	I/O		FUNCTION
30	VDD	Р	POWER SUPPLY VOLTAGE	
31	VSS	Р	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)	
32	VSS	Р	GROUND (VSS IS CONNECT CONDUCTIVE TAPE)	TED TO METAL HOUSING WITH
33	VCC	Р	POWER SUPPLY FOR LED I	DRIVER CIRCUIT
34	VCC	Р	POWER SUPPLY FOR LED I	DRIVER CIRCUIT
35	NC		NON CONNECTION (USING INTERNAL LED DRIVER) OR ANODE (USING EXTERNAL LED DRIVER) WHEN INTERNAL LED DRIVER : JP1 1-2 (DEFAULT) WHEN EXTERNAL LED DRIVER : JP1 2-3	
36	LEDCTRL	Ι	BRIGHTNESS CONTROL FOR LED BACKLIGHT ; LEDCTRL (USING INTERNAL LED DRIVER) OR CATHODE (USING EXTERNAL LED DRIVER) WHEN INTERNAL LED DRIVER : JP2 1-2 (DEFAULT) JP3 1-2 CONNECT (DEFAULT) WHEN EXTERNAL LED DRIVER : JP2 2-3 JP3 NON CONNECTION	
37	YU		TOP PANEL	
38	XL		LEFT PANEL	
39	YD		BOTTOM PANEL	TOUCH PANEL
40	XR		RIGHT PANEL	



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## 12. TOUCH PANEL SPECIFICATION 12.1 ELECTRICAL CHARACTERISTICS

 $Ta = 25^{\circ}C$ ITEM CONDITION SPEC. UNIT ≤ 1.5 % LINEARITY TRANSMISSION Min 80 **ASTM D1003** % X AXIS  $200 \sim 900$ TERMINAL RESISTANCE Ω Y AXIS  $160\sim 640$ DC25V INSULATION RESISTANCE  $\geq 20$ MΩ INPUT VOLTAGE 5(TYP) V

12.2 PRECAUTIONS IN USE OF TOUCH PANEL

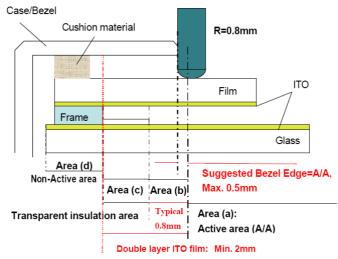
### 12.2.1 PURPOSE :

IN ORDER TO PREVENT ACCIDENTAL USE AND PERFORMANCE DETERIORATION, PLEASE KEEP THE FOLLOWING PRECAUTIONS AND INHIBITED POINTS.

12.2.2 ITEM AND ILLUSTRATION :

(1) STRUCTURE, AREA DEFINITION

THE STRUCTURE AND THE PERFORMANCE GUARANTEED AREA OF THIS TOUCH PANEL ARE DEFINED BELOW :



#### Single layer ITO film: Min. 3mm

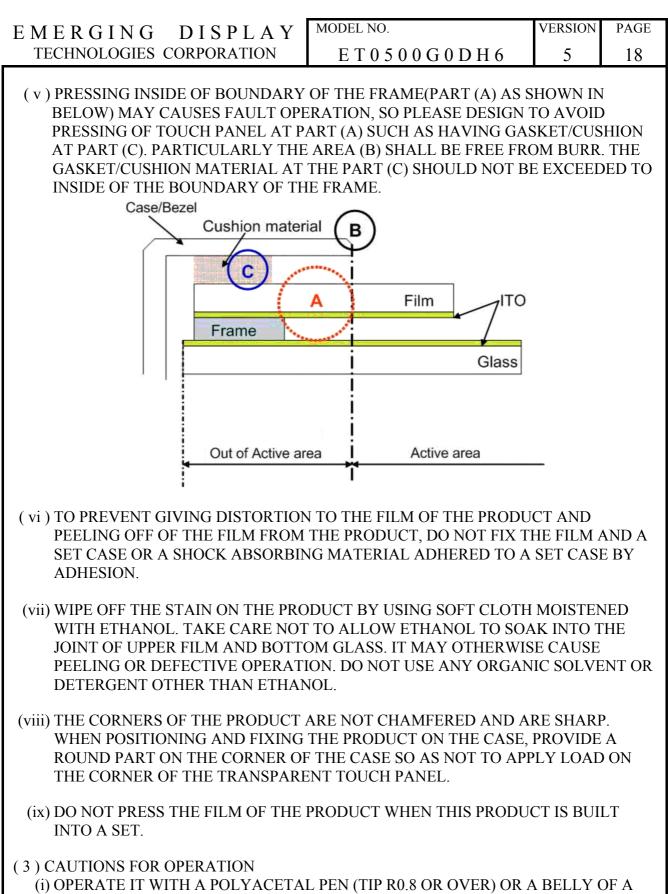
THE ABOVE FIGURE IS OUR DESIGN RULE OF TOUCH PANEL. IF IT CANNOT MEET YOUR REQUIREMENT, PLEASE CONTACT WITH OUR ENGINEERS FOR FURTHER DISCUSSION.

ABOVE FIGURE ILLUSTRATES THE RECOMMENDED BEZEL AND CUSHION DESIGN. IN ORDER TO PREVENT UNUSUAL PERFORMANCE DEGRADATION AND MALFUNCTION OF A TOUCH PANEL, PLEASE CARRY OUT THE SET CASE DESIGNING AND A TOUCH PANEL ASSEMBLING METHOD AFTER SURELY CONSIDERING THE DEFINITION OF EACH AREA ILLUSTRATED IN ABOVE FIGURE.

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AREA(a) : ACTIVE AREA		DETECT		
	RANTEED THE POSITION DATA		ABLE	
	DRCE AND OTHER OPERATIONS			
	D TO PLACE THE OPERATION I			
	CTIVE AREA. DUE TO STRUCT	· ·		
ACTIVE AREA IS LESS DUR	ABLE AT THE EDGE OR CLOSE	IO THE I	EDGE.	
AREA(b) : OPERATION NON-GUARANT	TEED AREA			
THIS AREA DOES NOT GUARANTEE A TOUCH PANEL OPERATION AND				
ITS FUNCTION. WHEN THIS AREA IS PRESSED, TOUCH PANEL SHOWS				
DEGRADATION OF ITS PERFORMANCE AND DURABILITY SUCH AS A PEN				
SLIDING DURABILITY BECC	OMES ABOUT ONE-TENTH COM	IPARED W	VITH	
THE ACTIVE AREA (AREA-(A	A) AS GUARANTEED AREA) AN	ID ITS		
OPERATION FORCE REQUIR	ES ABOUT DOUBLE. ABOUT 0.	5 MM OU	TSIDE	
FROM A BOUNDARY OF TH	E ACTIVE AREA CORRESPOND	S TO THIS	S AREA.	
AREA(c) : PRESSING PROHIBITION AR				
	S PRESSING, BECAUSE AN EXC			
	NT ELECTRODE (ITO) AND A S	ERIOUS D	DAMAGE	
IS GIVEN TO A TOUCH PAN	EL FUNCTION BY PRESSING.			

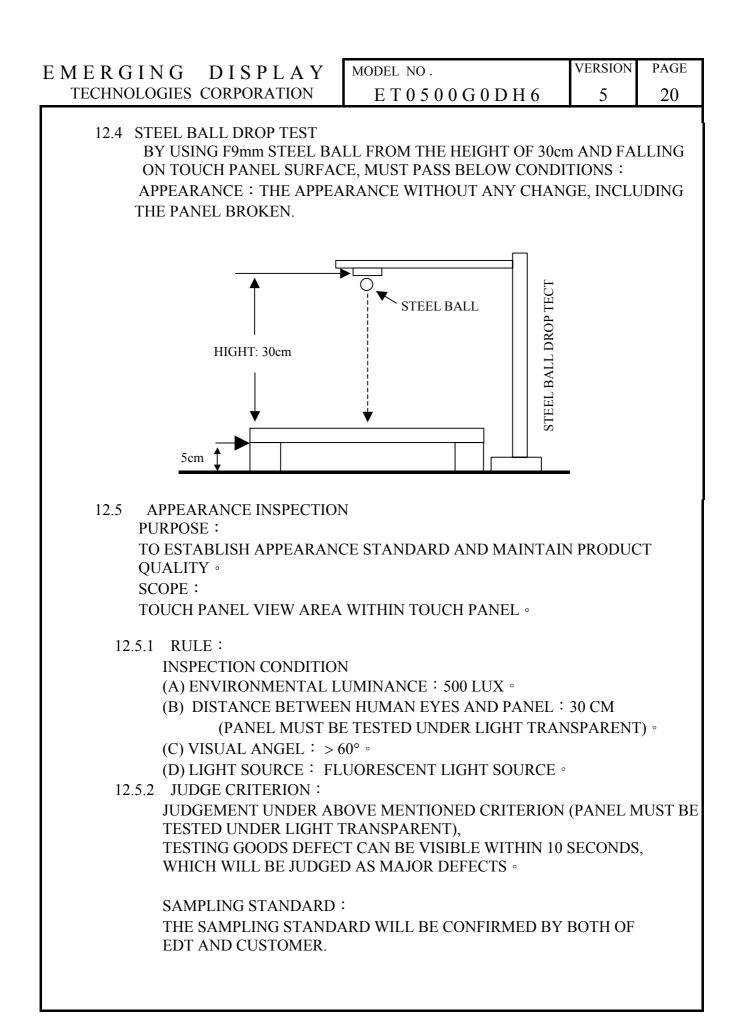
- AREA(d) : NON-ACTIVE AREA THE AREA DOES NOT ACTIVATE EVEN IF PRESSED.
- (2) CAUTIONS FOR INSTALLING AND ASSEMBLING (i) DO NOT GIVE EXCESSIVE STRAIN TO THE PRODUCT.
  - ( ii ) FLEXIBLE PATTERN CABLE IS CONNECTED TO THE BODY BY THERMAL PRESSURE METHOD. SO, DO NOT APPLY EXCESSIVE FORCES TO THE FLEXIBLE PATTERN. DO NOT ADD AN EXCESSIVE FORCE TO A FPC (FLEX TAIL) THAT MAKES PEELING OFF OF THE FPC FROM THE PRODUCT. DO NOT FIX, ADHERE OR MOUNT ANY ADDITIONAL GOODS ON THE FPC SUCH AS ADDITIONAL FILM/PLATE ON THE FPC, BECAUSE SUCH ADDITIONAL GOODS WILL APPLY A STRESS AT THE FPC BONDING AREA. IT MAY AFFECT THE CONDUCTIVITY OF FPC WITH TOUCH PANEL.
  - ( iii ) IN ORDER NOT TO APPLY LOAD ON THE DISPLAY, PROVIDE A CLEARANCE OF AT LEAST 0.3MM BETWEEN THE PRODUCT AND DISPLAY.

(iv) WE RECOMMEND THE DESIGN OF A CASE OR BEZEL SHOULD COVERS THE BOUNDARY OF THE ACTIVE AREA INSIDE IN ORDER TO PREVENT AN OPERATION AT OUTSIDE OF THE ACTIVE AREA WHICH CAN NOT GUARANTEE THE FUNCTION OR DURABILITY.
BEZEL'S EDGE PART MAY GUIDE THE PEN SLIDING ON THE SAME POSITION REPEATEDLY. IF THE BEZEL IS PLACED OUTSIDE OF THE ACTIVE AREA, IT MAY CAUSE THE DAMAGE OF THE ITO FILM.



(i) OPERATE IT WITH A POLYACETAL PEN (TIP R0.8 OR OVER) OR A BELLY OF A FINGER WITHOUT APPLYING EXCESSIVE LOAD. NEVER USE ANY MECHANICAL PENCILS, BALL POINT PENS AND HARD FINGERTIPS WHO'S TIP IS HARD FOR INPUT, OTHERWISE MALFUNCTIONS MAY RESULT.

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(ii) THE INPUT POSITION MAY BE FLU USE. IT IS DESIRABLE TO PROVID A CIRCUIT AND SOFTWARE.			
(iii) OPERATION AT THE OUT OF ACTI CAUSES A SERIOUS DAMAGE OF OPERATE AT THE OUT OF ACTIVE	A TRANSPARENT ELECTRODE		IT
(iv) IN CASE OF CLEANING THE PART SET, USE A SOFT CLOTH WITH A I CLEAN WITH A THI NG OTHER TH EDGES LIKE A FINGER NAIL ETC. TRANSPARENT CONDUCTIVE FIL TO YOUR LAST CUSTOMERS.	FINGER BERRY OR A COTTON IAN THE FINGER SUCH AS HAI ON THE CLOTH, BECAUSE IT (	BUD. DO I RD OR SH. CAUSE	NOT ARP
12.3 DURABILITY			
12.3.1 STYLUS HITTING : ONE MILLION TIMES OR OVER NO DAMAGE ON FILM SURFAG PEN : R8 mm SILICON RUBBER LOAD : 250g FREQUENCY : 180 times/min MEASUREMENT POSITION: 1 POINT OF TOUCH PANEL AC REPEATED : OVER 1,000,000 T	CE R CTIVE AREA		
12.3.2 PEN TOUCH SLIDING DURAB 100,000 TIMES OR OVER WRITING WITH R0.8mm PLAST IN ACTIVE AREA. SPEED IS 60mm/sec.	SILITY : TIC STYLUS PEN; WRITING FO	RCE 150g	



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ITEM	INSPECTION METHOD	CRITERIA
	$D \le 0.15 mm$	IGNORED
SPOTS AND DOTS	0.15mm < D ≤ 0.3mm	3 OR LESS (DISTANCE 5mm OVER)
	D > 0.3mm	NG
	$W \leq 0.05 mm$	IGNORED
SCRATCH	$0.05mm < W \le 0.1mm, L \le 5.0mm$	3 OR LESS (DISTANCE 5mm OVER)
	W > 0.1mm	NG
	$W \leq 0.05 mm$	IGNORED
LINEAR FOREIGN MATTER	$0.05mm < W \le 0.1mm, L \le 5.0mm$	3 OR LESS (DISTANCE 5mm OVER)
	W > 0.1mm	NG
GENERAL CRACK	$-Y$ X $\leq$ 3mm, Y $\leq$ 2mm, Z $\leq$ t	IGNORED
CORNER CRACK	$X \le 3$ mm, $Y \le 3$ mm, $Z \le t$	IGNORED
BAD CRACK	ALL SHALL BE REJECTED. BY NAKED EYES.	NG
	$\varnothing \le 0.2$ mm	IGNORED
	$0.2 \text{mm} < \emptyset \le 0.4 \text{mm}$	3 OR LESS (DISTANCE 5mm OVER)
FISH EYE	$0.4$ mm $< \emptyset \le 0.5$ mm	1 OR LESS (DISTANCE 5mm OVER)
	Ø > 0.5mm	NG
NEWTON RING	D ≤ 8mm	1 OR LESS
	8mm < D	NG

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			ТО
ONDITION : ± O LCD PANE FION : ±45° O LCD PANE	5° L SURFACE) L SURFACE)		
TIONS :			
T C INSPECTION		1X	
CTIONAL PECTION	300~500 Lux		
ION SHALL H DNS TO JUDO DARD : DN, SINGLE S	BE MADE ACCORD GE THE ACCEPTAB AMPLING		HE
	E T 0 5	E T 0 5 0 0 G 0 D H 6 S TO BE APPLIED TO THE LCD DISPLAY TECHNOLOGIES COR ANCE : 35cm±5cm DNDITION : ±5° O LCD PANEL SURFACE) TION : ±45° O LCD PANEL SURFACE) TION : ±45° O LCD PANEL SURFACE) $\frac{5}{90}$ $\frac{1}{90}$ $\frac{1}{90}$ $\frac{1}{300}$ $\frac{1}{30}$	ET0500G0DH65STO BE APPLIED TO THE LCD MODULE DISPLAY TECHNOLOGIES CORP.(E.D.T.)ANCE : $35cm\pm5cm$ ONDITION : $\pm 5^{\circ}$ O LCD PANEL SURFACE) TON : $\pm 45^{\circ}$ O LCD PANEL SURFACE) $1000 : \pm 45^{\circ}$ Operation Condition $30cm-40cm$ $45 : \pm 0^{\circ}C-25^{\circ}C}$ $(5\pm 20\% RH)SUBJECTION MORE THAN 600LuxTIONAL 300~500 LuxERY LOT FOR EACH MODELION SHALL BE MADE ACCORDING TO TDNS TO JUDGE THE ACCEPTABILITYDARD :DN, SINGLE SAMPLINGCT : AQL 0.65$

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## 13.3 INSPECTION STANDARDS

## 13.3.1 VISUAL DEFECTS CLASSIFICATION

TYPE OF DEFECT	INSPECTION ITEM	DEFECT FEATURE	AQL
	1.DISPLAY ON	• DEFECT TO MISS SPECIFIED DISPLAY FUNCTION, FOR ALL AND SPECIFIED DOTS EX: DISCONNECTION, SHORT CIRCUIT ETC	
MAJOR DEFECT	2.BACKLIGHT	<ul> <li>NO LIGHT</li> <li>FLICKERING AND OTHER ABNORMAL ILLUMINATION</li> </ul>	- 0.65
	3.DIMENSIONS	• SUBJECT TO INDIVIDUAL ACCEPTANCE SPECIFICATIONS	
	1.DISPLAY ZONE	<ul> <li>BLACK/WHITE SPOT</li> <li>BUBBLES ON POLARIZER</li> <li>NEWTON RING</li> <li>BLACK/WHITE LINE</li> <li>SCRATCH</li> <li>CONTAMINATION</li> <li>LEVER COLOR SPREED</li> </ul>	
MINOR DEFECT	2.BEZEL ZONE	• STAINS • SCRATCHES • FOREIGN MATTER	1.0
	3.SOLDERING	<ul> <li>INSUFFICIENT SOLDER</li> <li>SOLDERED IN INCORRECT POSITION</li> <li>CONVEX SOLDERING SPOT</li> <li>SOLDER BALLS</li> <li>SOLDER SCRAPS</li> </ul>	
	4.DISPLAY ON (ALL ON)	• LIGHT LINE	1

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## 13.3.2 MODULE DEFECTS CALSSIFICATION

NO.	ITEM			TERIA	
1.	DISPLAY ON INSPECTION	<ul><li>(1)INCORRECT PA</li><li>(2)MISSING SEGM</li><li>(3)DIM SEGMENT</li><li>(4)OPERATING VC</li></ul>	IENT	D SPEC	
2.	OVERALL DIMENSIONS	(1)OVERALL DIME	ENSION BEYOND	) SPEC	
3.	DOT DEFECT	AND BLUE SCR (2) IT BRIGHT DOT DARK DOT TOAL BRIGHT A NOTE : 1. THE DEFINITION THE SIZE OF A D REGARDED AS C 2. BRIGHT DOT : DOTS APPEAR B PANEL IS DISPLA 3. DARK DOT : DOTS APPEAR D	REENS. TEMS AND DARK DOTS N OF DOT : DEFECTIVE DOT ( DNE DEFECTUVE BRIGHT AND UNC AYING UNDER B DARK AND UNCH	OVER 1/2 OF WHOLE	NT E DOT IS N WHICH LCD WHICH LCD
4.	FOREIGN BLACK/WHITE/ BRIGHT LINE/ SCRATCH OF VIEWING AREA	LENGTH : L $L \le 0.3$ $0.3 < L \le 2.5$ $2.5 < L$ WIDTH : W mm, LI	WIDTH : W $W \le 0.05$ $0.05 < W \le 0.1$ $0.1 < W$	PERMISSIBLE NO. IGNORE 4 NONE	
5.	FOREIGN MATTER \ BLACK SPOTS \ WHITE SPOTS \ DENT (INCLUDING LIGHT LEAKAGE DUE TO POLARIZING PLATES PINHOLES, ETC.)	AVERAGE DIAM $D \le 0$ $0.15 < D$ $0.5 <$ NOTE : DIAMETH $a$	$\frac{0.15}{D \le 0.5}$	NUMBER OF PIECES IGNORE 4 NONE	

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NO.	ITEM		CRITERIA	
			AVERAGE DIAMETER (mm) : D	NUMBER OF PIECES PERMITTED
			D ≤ 0.25	LGNORE
		BUBBLE ON THE POLARIZER	$0.25 < D \le 0.5$	N ≤ 5
		I OLARIZER	0.5 < D	NOTE
		SURFACE STATUS	D < 0.1 mm	IGNORE
		Source Shiros	$0.1 < D \leq 0.3 mm$	$N \leq 3$
		CF FAIL / SPOT	D < 0.1 mm	IGNORE
			$0.1 < D \leq 0.3 mm$	$N \leq 3$
6. BUBBLES OF POLARIZER /DIRT/CF FAIL /SURFACE STAINS		ON ACTIV BUBBLE APPEARS (2)THE EXT OBSERV (3)THE DEF AS FOLL	ER BUBBLE IS DEFINED VE DISPLAY AREA. THE I SHALL BE IGNORED IF T ON THE OUTSIDE OF AC RANEOUS SUBSTANCE I ED WHEN THE MODULE INITION OF AVERAGE D OWING. E DIAMETER (D)=( $a+b$ )/2	DEFECT OF POLARIZER HE POLARIZER BUBBLE CTIVE DISPLAY AREA. IS DEFINED AS IT CAN B IS POWER ON. IAMETER, D IS DEFINED
7.	LINE DEFECT ON DISPLAY	OBVIOUS VERTICAI	L OR HORIZONTAL LINE	DEFECT IS NOT ALLOW
8.	MURA ON DISPLAY	IT'S OK IF MURA IS	SLIGHT VISIBLE THROU	NG 6% ND FILTER
9.	UNEVEN COLOR SPREAD, COLORATION	(1)TO BE DETERMINED BASED UPON THE STANDARD SAMPLE.		
	BEZEL		HAVE RUST, BE DEFORM	
10.	APPEARANCE		F OTHER CONTAMINAT	
			MPLY WITH JOB SPECIFI	
11	РСВ	THE SEAL AREA ( THAN THREE PLA (2)NO OXIDATION O (3)PARTS ON PCB M CHARACTERISTIC THERE SHOULD B PARTS. (4)THE JUMPER ON CHARACTERISTIC (5)IF SOLDER GETS	OR CONTAMINATION PCI UST BE THE SAME AS OF C CHART. BE NO WRONG PARTS, M THE PCB SHOULD CONFO	E SHOULD BE NO MORE 3 TERMINALS. N THE PRODUCTION ISSING PARTS OR EXCES ORM TO THE PRODUCT ED PAD, ZEBRA PAD OR

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NO		
NO.	ITEM	CRITERIA
		(b)CHIP COMPONENT COMPONENT IS OFF CENTER, AND MORE THAN 50% OF THE LEADS IS OFF THE PAD OUTLINE
12.	SOLDERING	
		<ul> <li>(4)NO UNMELTED SOLDER PASTE MAY BE PRESENT ON THE PCB.</li> <li>(5)NO COLD SOLDER JOINTS, MISSING SOLDER CONNECTIONS, OXIDATION OR ICICLE.</li> <li>(6)NO RESIDUE OR SOLDER BALLS ON PCB.</li> <li>(7)NO SUPPER CONCURSION CONTRACTOR OF THE CONTRACT OF THE PCB.</li> </ul>
		(7)NO SHORT CIRCUITS IN COMPONENTS ON PCB. (1)NO LIGHT
13.	BACKLIGHT	<ul> <li>(2)FLICKERING AND OTHER ABNORMAL ILLUMINATION</li> <li>(3)SPOTS OR SCRATCHES THAT APPEAR WHEN LIT MUST BE JUDGED USING LCD SPOT, LINES AND CONTAMINATION STANDARDS.</li> <li>(4)BACKLIGHT DOESN'T LIGHT OR COLOR IS WRONG.</li> </ul>
14.	GENERAL APPEARANCE	<ul> <li>(1)NO OXIDATION, CONTAMINATION, CURVES OR, BENDS ON INTERFACE PIN (OLB) OF TCP.</li> <li>(2)NO CRACKS ON INTERFACE PIN (OLB) OF TCP.</li> <li>(3)NO CONTAMINATION, SOLDER RESIDUE OR SOLDER BALLS ON PRODUCT.</li> <li>(4)THE IC ON THE TCP MAY NOT BE DAMAGED, CIRCUITS.</li> <li>(5)THE UPPERMOST EDGE OF THE PROTECTIVE STRIP ON THE INTERFACE PIN MUST BE PRESENT OR LOOK AS IF IT CAUSE THE INTERFACE PIN TO SEVER.</li> <li>(6)THE RESIDUAL ROSIN OR TIN OIL OF SOLDERING (COMPONENT OR CHIP COMPONENT) IS NOT BURNED INTO BROWN OR BLACK COLOR.</li> <li>(7)SEALANT ON TOP OF THE ITO CIRCUIT HAS NOT HARDENED.</li> <li>(8)PIN TYPE MUST MATCH TYPE IN SPECIFICATION SHEET.</li> <li>(9)LCD PIN LOOSE OR MISSING PINS.</li> <li>(10)PRODUCT PACKAGING MUST THE SAME AS SPECIFIED ON PACKAGING SPECIFICATION SHEET.</li> <li>(11)PRODUCT DIMENSION AND STRUCTURE MUST CONFORM TO PRODUCT SPECIFICATION SHEET.</li> <li>(12)THE APPEARANCE OF HEAT SEAL SHOULD NOT ADMIT ANY DIRT AND BREAK.</li> </ul>

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NO.	ITEM	CRITERIA		
		THE LCD WITH EXTENSIVE CRACK IS NOT ACCEPTABLE		
		GENERAL GLASS CHIP:	a b c	
		b	$\leq t/2$ < VIEWING AREA $\leq 1/8X$	
			$t/2 > , \le 2t \qquad \le W/2 \qquad \le 1/8X$	
			*W=DISTANCE BETWEEN	
			SEALANT AREA AND LCD	
			PANEL EDGE	
			X = LCD SIDE LENGTH	
			t = GLASS THICKNESS	
		W, C		
		a a		
		X		
		₹b I		
		a		
		CORNER PART:	abc $\leq t/2$ $<$ VIEWING AREA $\leq 1/8X$	
		\ h	$\frac{\leq V2}{\langle VIEWINGAREA} \leq 1/8X$	
			*W=DISTANCE BETWEEN	
15.	CRACKED GLASS			
13.	CRACKED GLASS		SEALANT AREA AND LCD PANEL EDGE	
		a		
			X = LCD SIDE LENGTH	
			t = GLASS THICKNESS	
		CHIP ON ELECTRODE PAD	a b c	
		a	$\leq t$ $\leq 0.5$ mm $\leq 1/8X$	
			* X=LCD SIDE WIDTH	
		the second secon	t =GLASS THICKNESS	
		C C		
			$\begin{array}{ c c c c c } \hline a & b & c \\ \hline \leq t & \leq 1/8X & \leq L \\ \hline \end{array}$	
			*X=LCD SIDE WIDTH	
			t = GLASS THICKNESS	
			L=ELECTRODE PAD LENGTH	
			©IF GLASS CHIPPING THE ITO	
			TERMINAL, OVER 2/3 OF THE ITO MU	
			REMAIN AND BE, INSPECTED	
			ACCORDING TO ELECTRODE	
		D D	TERMINAL SPECIFICATIONS	
			©IF THE PRODUCT WILL BE HEAT	
			SEALED BY THE CUSTOMER,	
			THE ALIGNMENT MARK MUST NOT	
			BE DEMAGED	
	<u> </u>			

## 13.4 RELIABILITY TEST

## 13.4.1 STANDARD SPECIFICATIONS FOR RELIABILITY OF LCD MODULE

NO	ITEM	DESCRIPTION	
1	HIGH TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +60°C FOR 240 HRS	
2	LOW TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -10°C FOR 240 HRS	
3	HIGH TEMPERATURE STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +70°C FOR 240 HRS	
4	LOW TEMPERATURE STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -20°C FOR 240 HRS	
5	HIGH TEMP / HUMIDITY TEST STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT 40°C , 90% RH 240 HRS	
6	THERMAL SHOCK (NOT OPERATED)	THE SAMPLE SHOULD BE ALLOWED TO STAND THE FOLLOWING 10 CYCLES OF OPERATION: -20°C FOR 30 MINUTES ~ +70°C FOR 30 MINUTES	
7	ESD (ELECTROSTATIC DISCHARGE) (NOT OPERATED)	AIR DISCHARGE ± 12KV CONTACT DISCHARGE ± 8KV	

NOTE (1) : THE TEST SAMPLES HAVE RECOVERY TIME FOR 2 HOURS AT ROOM TEMPERATURE BEFORE THE FUNCTION CHECK. IN THE STANDARD CONDITIONS, THERE IS NO DISPLAY FUNCTION NG ISSUE OCCURRED.

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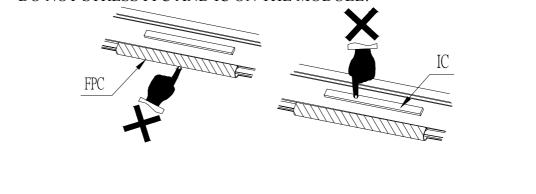
## 13.5 TESTING CONDITIONS AND INSPECTION CRITERIA

FOR THE FINAL TEST THE TESTING SAMPLE MUST BE STORED AT ROOM TEMPERATURE FOR 24 HOURS, AFTER THE TESTS LISTED IN TABLE 13.5, STANDARD SPECIFICATIONS FOR RELIABILITY HAVE BEEN EXECUTED IN ORDER TO ENSURE STABILITY.

NO	ITEM	TEST MODEL	INSPECTION CRITERIA
1	CURRENT CONSUMPTION	REFER TO SPECIFICATION	THE CURRENT CONSUMPTION SHOULD CONFORM TO THE PRODUCT SPECIFICATION.
2	CONTRAST	REFER TO SPECIFICATION	AFTER THE TESTS HAVE BEEN EXECUTED, THE CONTRAST MUST BE LARGER THAN HALF OF ITS INITIAL VALUE PRIOR TO THE TESTS.
3	APPEARANCE	VISUAL INSPECTION	DEFECT FREE

## 13.6 OPERATION

- 13.6.1 DO NOT CONNECT OR DISCONNECT MODULES TO OR FROM THE MAIN SYSTEM WHILE POWER IS BEING SUPPLIED .
- 13.6.2 USE THE MODULE WITHIN SPECIFIED TEMPERATURE ; LOWER TEMPERATURE CAUSES THE RETARDATION OF BLINKING SPEED OF THE DISPLAY ; HIGHER TEMPERATURE MAKES OVERALL DISPLAY DISCOLOR . WHEN THE TEMPERATURE RETURNS TO NORMALITY, THE DISPLAY WILL OPERATE NORMALLY .
- 13.6.3 ADJUST THE LC DRIVING VOLTAGE TO OBTAIN THE OPTIMUM CONTRAST .
- 13.6.4 POWER ON SEQUENCE INPUT SIGNALS SHOULD NOT BE SUPPLIED TO LCD MODULE BEFORE POWER SUPPLY VOLTAGE IS APPLIED AND REACHES THE SPECIFIED VALUE. IF ABOVE SEQUENCE IS NOT FOLLOWED, CMOS LSIS OF LCD MODULES MAY BE DAMAGED DUE TO LATCH - UP PROBLEM.
- 13.6.5 NOT ALLOWED TO INFLICT ANY EXTERNAL STRESS AND TO CAUSE ANY MECHANICAL INTERFERENCE ON THE BENDING AREA OF FPC DURING THE TAIL BENDING BACKWARDS! DO NOT STRESS FPC AND IC ON THE MODULE!



## 13.7 NOTICE

- 13.7.1 USE A GROUNDED SOLDERING IRON WHEN SOLDERING CONNECTOR I/O TERMINALS . FOR SOLDERING OR REPAIRING, TAKE PRECAUTION AGAINST THE TEMPERATURE OF THE SOLDERING IRON AND THE SOLDERING TIME TO PREVENT PEELING OFF THE THROUGH-HOLE-PAD .
- 13.7.2 DO NOT DISASSEMBLE . EDT SHALL NOT BE HELD RESPONSIBLE IF THE MODULE IS DISASSEMBLED AND UPON THE REASSEMBLY THE MODULE FAILED .
- 13.7.3 DO NOT CHARGE STATIC ELECTRICITY , AS THE CIRCUIT OF THIS MODULE CONTAINS CMOS LSIS. A WORKMAN'S BODY SHOULD ALWAYS BE STATIC-PROTECTED BY USE OF AN ESD STRAP. WORKING CLOTHES FOR SUCH PERSONNEL SHOULD BE OF STATIC -PROTECTED MATERIAL .
- 13.7.4 ALWAYS GROUND THE ELECTRICALLY-POWERED DRIVER BEFORE USING IT TO INSTALL THE LCD MODULE. WHILE CLEANING THE WORK STATION BY VACUUM CLEANER, DO NOT BRING THE SUCKING MOUTH NEAR THE MODULE ; STATIC ELECTRICITY OF THE ELECTRICALLY-POWERED DRIVER OR THE VACUUM CLEANER MAY DESTROY THE MODULE .
- 13.7.5 DON'T GIVE EXTERNAL SHOCK.
- 13.7.6 DON'T APPLY EXCESSIVE FORCE ON THE SURFACE.
- 13.7.7 LIQUID IN LCD IS HAZARDOUS SUBSTANCE. MUST NOT LICK AND SWALLOW. WHEN THE LIQUID IS ATTACH TO YOUR, SKIN, CLOTH ETC. WASH IT OUT THOROUGHLY AND IMMEDIATELY.
- 13.7.8 DON'T OPERATE IT ABOVE THE ABSOLUTE MAXIMUM RATING.
- 13.7.9 STORAGE IN A CLEAN ENVIRONMENT, FREE FROM DUST, ACTIVE GAS, AND SOLVENT.
- 13.7.10 STORE WITHOUT ANY PHYSICAL LOAD.
- 13.7.11 REWIRING: NO MORE THAN 3 TIMES.