

IR-directFET\_MN-Outline\_Names\_RevA\_30Jun2011



IR-directFET\_MP-Outline\_Names\_RevA\_30Jun2011



IR-directFET\_MT-Outline\_Names\_RevA\_30Jun2011



IR-directFET\_MX-Outline\_Names\_RevA\_30Jun2011



IR-directFET\_MZ-Outline\_Names\_RevA\_30Jun2011

IR-directFET\_SH-Outline\_Names\_RevA\_30Jun2011



IR-directFET\_SJ-Outline\_Names\_RevA\_30Jun2011



IR-directFET\_SQ-Outline\_Names\_RevA\_30Jun2011



IR-directFET\_ST-Outline\_Names\_RevA\_30Jun2011



IR-directFET\_S1-Outline\_Names\_RevA\_30Jun2011



IR-directFET\_L8-Outline\_Names\_RevA\_30Jun2011

100 mm

90

80

70

60

50

40

30

20

10

5

0

mm

0

5

10

20

30

40

50

60

70

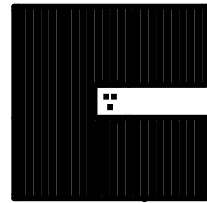
80

90

100mm

1:1 Print! Check dimension by controlling the gauge!

T



IR-directFET\_ST-Outline\_Names\_SquareHeatsink\_RevA\_30Jun2011  
Experimental footprint with copper plane

Heatsink

Footprints of International Rectifiers  
directFet Transistor series,

- SH-Outline
- SJ-Outline
- SQ-Outline
- ST-Outline
- S1-Outline

- MN-Outline
- NP-Outline
- MT-Outline
- MX-Outline
- MZ-Outline
- L8-Outline

"directFET" is a trademark of International Rectifier.  
Those housings are proprietary, so be careful of using them.

Those footprints are untested yet!  
So be nice and make a report to [bernd.wiebus@gmx.de](mailto:bernd.wiebus@gmx.de)  
and tell about your experiences!

# KiCAD Footprints of some International Rectifier directFED Transistors from the file IR-directFET\_Packages\_RevA\_30Jun2011.mod , Author: Bernd Wiebus / Uedem / Germany / 02Jul2011



**GNU-GPL NO WARRANTY!**

File: IR-directFET_Packages_RevA_30Jun2011.brd	
Sheet: 1/1	
Title:	
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Rev:	1/1