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main.c

2 *          ATMEL Microcontroller Software Support  -  ROUSSET  -
33
34 /*
35 * This example is based on the Application Note 6296A
36 * "Getting Started with AT91SAM7X Microcontrollers"
37 * by Atmel. Take a look at www.Atmel.com/products/AT91
38 *
39 * I took out all stuff not needed for a minimal startup
40 * The Debug Unit is quite usefull, so I decided not to delete it
41 */
42
43 #include "AT91SAM7X256.h"
44
45 /* TC_CMR: Timer Counter Channel Mode Register Bits Definition
46 #define TC_CLKS_MCK2          0x0
47 #define TC_EEVT_XC0          0x400
48 #define TC_CPCTRG            0x4000
49 #define TC_WAVE              0x8000
50 #define TC_ACPA_TOGGLE_OUTPUT 0x30000
51 #define TC_ACPC_TOGGLE_OUTPUT 0xC0000
52 #define TC_ASWTRG_SET_OUTPUT 0x400000
53 #define TC_BCPB_TOGGLE_OUTPUT 0x3000000
54 #define TC_BCPC_TOGGLE_OUTPUT 0xC000000
55 #define TC_BSWTRG_SET_OUTPUT 0x40000000
56
57 /* TC_CCR: Timer Counter Control Register Bits Definition
58 #define TC_CLKEN              0x1
59 #define TC_CLKDIS            0x2
60 #define TC_SWTRG             0x4
61
62 #define LED_PIN (1<<0)
63
64 // switch on LED
65
66 void ConfigureLed(){
67     AT91C_BASE_PMC->PMC_PCER = (1 << AT91C_ID_PIOB);    // enable periph clock for PIO
controller A
68     AT91C_BASE_PIOB->PIO_PER = LED_PIN;    // PIO control enable
69     AT91C_BASE_PIOB->PIO_OER = LED_PIN;    // make pin output
70     AT91C_BASE_PIOB->PIO_PPUDR = LED_PIN;  // disable pull-up
71 }
72 void LedOn() {
73     AT91C_BASE_PIOB->PIO_CODR = LED_PIN;
74 }
75 }
76
77 // switch off LED
78 void LedOff() {
79     AT91C_BASE_PIOB->PIO_SODR = LED_PIN;
80 }
81
82
83 __attribute__ ((section (".data")))
84 int main() {
85
86 AT91C_BASE_PMC->PMC_PCER = (1 << AT91C_ID_PIOB);
87 ConfigurePWM();
88
89 //// ===== PWM mittels Timer/Counter =====
90 //PIO Controller B enable
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91 AT91C_BASE_PMC->PMC_PCER = (1 << AT91C_ID_PIOB); // enable periph clock for PIO
    controller A
92
93 //AT91C_BASE_PIOB->PIO_PDR = AT91C_PA15_TIOA1; // PIO control disable
94 //AT91C_BASE_PIOB->PIO_PPUDR = AT91C_PA15_TIOA1; // disable pull-up
95 //AT91C_BASE_PIOB->PIO_PDR = AT91C_PA15_TIOA1; // let SPI peripheral control the output
96 //AT91C_BASE_PIOB->PIO_BSR = AT91C_PA15_TIOA1; // set pin to be driven by peripheral B
    (PWM0)
97
98 AT91C_BASE_PIOB->PIO_PER = AT91C_PA15_TIOA1; // PIO control enable
99 AT91C_BASE_PIOB->PIO_OER = AT91C_PA15_TIOA1; // make pin output
100 AT91C_BASE_PIOB->PIO_PPUDR = AT91C_PA15_TIOA1; // disable pull-up
101
102
103 //
104 AT91C_BASE_PMC->PMC_PCER = (1 << AT91C_ID_TC1); //enable periph clock for timer counter 1
105 AT91C_BASE_TC1->TC_CCR=TC_CLKDIS; //Disables the timer clock, stopping the
    counting.
106 //
107 //Set Channel Mode Register
108 AT91C_BASE_TC1->TC_CMR =
109     TC_ASWTRG_SET_OUTPUT | /* ASWTRG : software trigger set TIOA */
110     TC_ACPC_TOGGLE_OUTPUT | /* ACPC : Register C compare toggle TIOA */
111     TC_ACPA_TOGGLE_OUTPUT | /* ACPA : Register A compare toggle TIOA */
112     TC_WAVE | /* WAVE : Waveform mode */
113     TC_CPCTRG | /* CPCTRG : Register C compare trigger enable */
114     TC_EEVT_XC0 | /* EEVT : XC0 as external event (TIOB=output) */
115     TC_CLKS_MCK2 ; // TCCLKS : MCK / 2 */
116
117 //
118 ///* Compare registers initialization
119 AT91C_BASE_TC1->TC_RC = 0x5DC0 ; // 1kHz PWM generation */
120 AT91C_BASE_TC1->TC_RA = 0x1C20 ; // 50% duty cycle on TIOA1 */
121
122 AT91C_BASE_TC1->TC_CCR = TC_CLKEN | TC_SWTRG; /* Enable the Clock counter */
123
124 //ConfigureLed();
125 //
126 //LedOn();
127
128 return 0;
129 }
130

```