

6. INTERFACE SPECIFICATIONS

6.1 INPUT SIGNAL TIMING SPECIFICATIONS

Signal	Parameter	Symbol	MIN.	TYP.	MAX.	Unit	Remarks
CK	Period	t_{CLK}	42	40	31	ns	
	Frequency	f_{CLK}	24	25	32.5	MHz	
	Low Level Width	t_{WCL}	6	-	-	ns	
	High Level Width	t_{WCH}	6	-	-	ns	
	Rise, Fall Time	t_{CLKr}, t_{CLKf}	-	-	3	ns	
	Duty ⁽¹⁾	-	0.45	0.50	0.55	-	
ENAB (Data Enable)	Setup Time	t_{DES}	5	-	-	ns	
	Hold Time	t_{DEH}	10	-	-	ns	
	Rise, Fall Time	t_{DEr}, t_{DEf}	-	-	16	ns	
	Horizontal Period	t_{HP}	788	800	890	t_{CLK}	
	Horizontal Valid	t_{HV}	640	640	640	t_{CLK}	
	Horizontal Blank	t_{HBK}	148	160	250	t_{CLK}	
	Vertical Period	t_{VP}	516	525	561	t_{HP}	
	Vertical Valid	t_{VW}	480	480	480	t_{HP}	
	Vertical Blank	t_{VBK}	36	45	81	t_{HP}	
	Vertical Frequency	f_v	50	60	80	Hz	
Data R,G,B	Setup Time	t_{DS}	5	-	-	ns	
	Hold Time	t_{DH}	10	-	-	ns	
	Rise, Fall Time	t_{Dr}, t_{Df}	-	-	3	ns	

Note: (1) t_{CLKL} / t_{CLK} .

6.1.1 DE AND RGB INPUT DATA TIMING WAVEFORM

