```
 1 #daemon configuration
 2 telnet_port 4444
 3 gdb_port 3333
 4
 5 #interface
 6 interface ft2232
 7 ft2232_device_desc "Olimex OpenOCD JTAG ARM-USB-TINY-H"
 8 ft2232_layout "olimex-jtag"
 9 ft2232_vid_pid 0x15ba 0x002a
10
11
12 #use combined on interfaces or targets that can't set TRST/SRST separately
13 reset_config srst_only srst_pulls_trst
14
15
16 if { [info exists CHIPNAME] } {
17    set  _CHIPNAME $CHIPNAME
18 } else {
19    set  _CHIPNAME sam7x256
20 }
21
22 if { [info exists ENDIAN] } {
23    set  _ENDIAN $ENDIAN
24 } else {
25    set  _ENDIAN little
26 }
27
28 if { [info exists CPUTAPID ] } {
29    set _CPUTAPID $CPUTAPID
30 } else {
31    set _CPUTAPID 0x3f0f0f0f
32
33 }
34
35 #use combined on interfaces or targets that cannot set TRST/SRST separately
36 reset_config trst_and_srst
37
38 # set speed
39 jtag_khz 1000
40
41
42 #jtag scan chain
43 jtag newtap $_CHIPNAME cpu -irlen 4 -ircapture 0x1 -irmask 0xf -expected-id
   $_CPUTAPID
44
45 set _TARGETNAME $_CHIPNAME.cpu
46 target create $_TARGETNAME arm7tdmi -endian $_ENDIAN -chain-position $_TARGETNAME
   -variant arm7tdmi
47
48 $_TARGETNAME configure -event reset-init {
49     # disable watchdog
50     mww 0xfffffd44 0x00008000
51     # enable user reset
52     mww 0xfffffd08 0xa5000001
53     # CKGR_MOR : enable the main oscillator
54     mww 0xfffffc20 0x00000601
55     sleep 10
56     # CKGR_PLLR: 96.1097 MHz
57     mww 0xfffffc2c 0x00481c0e
58     sleep 10
59     # PMC_MCKR : MCK = PLL / 2 ~= 48 MHz
60     mww 0xfffffc30 0x00000007
61     sleep 10
62     # MC_FMR: flash mode (FWS=1,FMCN=60)
```

```
63      mww 0xffffff60 0x003c0100
64      sleep 100
65 }
66
67      proc program_device () {
68      #halt the processor
69      halt
70      wait_halt
71
72      #write file to flash memory
73      arm7_9 dcc_downloads enable
74      sleep 10
75      poll
76      flash probe 0
77      flash write_image erase unlock "at91sam7x_getting_started_flash.bin" 0x00100000
78      sleep 10
79
80      #start execution of the program just downloaded
81      reset run
82      sleep 10
83
84
85      #exit OpenOCD
86      shutdown
87
88 }
89
90
91 $_TARGETNAME configure -work-area-phys 0x00200000 -work-area-size 0x4000
   -work-area-backup 0
92
93 #flash bank <driver> <base_addr> <size> <chip_width> <bus_width> <target_number>
   [<target_name> <banks> <sectors_per_bank> <pages_per_sector> <page_size>
   <num_nvmbits> <ext_freq_khz>]
94 set _FLASHNAME $_CHIPNAME.flash
95 flash bank $_FLASHNAME at91sam7 0 0 0 0  $_TARGETNAME 0 0 0 0 0 0 0 18432
96
97
98 init
99 reset init
100 at91sam7 gpnvm 0 2 set
101 program_device ()
102
103
104 # For more information about the configuration files, take a look at:
105 # openocd.texi
106
107
108
```