

Eagle Direction Parameter [Eagle Handbuch]

NC	Not connected
In	Input
Out	Output
IO	Input/output
OC	Open Collector or Open Drain
Hiz	High impedance output
Pas	Passive (resistors, etc.)
Pwr	Power pin (power supply input)
Sup	Power supply output for ground and supply symbols

MAX1496 <http://datasheets.maximintegrated.com/en/ds/MAX1447-MAX1498.pdf>

Pin No.	Pin Name	Eagle Direction	Function
1	VNEG	Sup ?	-2.5V Charge-Pump Voltage Output. Connect a 0.1µF capacitor to GND.
2	REF-	Pwr/In ?	Negative Reference Voltage Input. For internal-reference operation, connect REF- to GND. For external-reference operation, bypass REF- to GND with a 0.1µF capacitor and set VREF- from -2.2V to +2.2V, provided VREF+ > VREF-.
3	REF+	Pwr/In ?	Positive Reference Voltage Input. For internal-reference operation, connect a 4.7µF capacitor from REF+ to GND. For external-reference operation, bypass REF+ to GND with a 0.1µF capacitor and set VREF+ from -2.2V to +2.2V, provided VREF+ > VREF-.
4	AIN+	In	Positive Analog Input. Positive side of fully differential analog input. Bypass AIN+ to GND with a 0.1µF or greater capacitor.
5	AIN-	In	Negative Analog Input. Negative side of fully differential analog input. Bypass AIN- to GND with a 0.1µF or greater capacitor.
6	ISET	?	Segment Current Controller. Connect to ground through a resistor to set the segment current. See Table 5 for current selection.
7	GND	Pwr	Ground
8	VDD	Pwr	Analog and Digital Circuit Supply Voltage. Connect VDD to a +2.7V to +5.25V power supply. Bypass VDD to GND with a 0.1µF capacitor and a 4.7µF capacitor.
9	INTREF	In	Internal-Reference Logic Input. Connect to GND to select external-reference mode. Connect to DVDD for the MAX1447/MAX1498 and VDD for the MAX1496 to select the internal reference mode.
10	RANGE	In	Range Logic Input. RANGE controls the fully differential analog input range. Connect to GND for the ±2V input range. Connect to DVDD (MAX1447/MAX1498) or VDD (MAX1496) for the ±200mV input range.
11	DPSET1	In	Decimal-Point Logic-Input 1. Controls the decimal point of the LED. See the Decimal-Point Control section.
12	DPSET2	In	Decimal-Point Logic-Input 2. Controls the decimal point of the LED. See the Decimal-Point Control section.
13	PEAK	In	Peak Logic Input. Connect to DVDD (MAX1447/MAX1498) or VDD (MAX1496) to display the highest ADC value on the LED. Connect to GND to disable the peak function.
14	HOLD	In	Hold Logic Input. Connect to DVDD (MAX1447/MAX1498) or VDD (MAX1496) to hold the current ADC value on the LED. Connect to GND to update the LED at a rate of 2.5Hz and disable the hold function. For the MAX1447, only placing the device into hold mode initiates an offset mismatch calibration. Assert HOLD high for a minimum of 2s to ensure the completion of offset mismatch calibration.
15	DIG0	Hiz	Digit 0 Driver
16	DIG1	Hiz	Digit 1 Driver
17	GLED	Pwr	Ground for LED Display Digit Driver
18	DIG2	Hiz	Digit 2 Driver
19	DIG3	Hiz	Digit 3 Driver
20	SEGA	Hiz	Segment A Driver
21	SEGB	Hiz	Segment B Driver
22	SEGC	Hiz	Segment C Driver
23	SEGD	Hiz	Segment D Driver
24	SEGE	Hiz	Segment E Driver
25	VLED	Pwr	LED Display Segment Driver Supply. Connect to a +2.7V to +5.25V supply. Bypass with a 0.1µF capacitor to GLED.
26	SEGF	Hiz	Segment F Driver
27	SEGG	Hiz	Segment G Driver
28	SEGDP	Hiz	Segment DP Driver