

	Standard:	Ist:
Low	62	E2
High	99	D8
Extended	07	FD
Lock	3F	FF

```
C:\Users\JakobsenD.G02\Desktop\Ordner\FFP\Projekte\arduino-1.0.1-windows\arduino-1.0.1\hardware\tools\avr\bin\avrdude -
CC:\Users\JakobsenD.G02\Desktop\Ordner\FFP\Projekte\arduino-1.0.1-windows\arduino-1.0.1\hardware\tools\avr\etc\avrdude.conf -v -v -v -v -patmega2560 -cstk500v2 -P\\.\COM5 -e -
Ulock:w:0x3F:m -Uefuse:w:0xFD:m -Uhfuse:w:0xD8:m -Ulfuse:w:0xFF:m
```

```
avrdude: Version 5.11, compiled on Sep  2 2011 at 19:38:36
Copyright (c) 2000-2005 Brian Dean, http://www.bdmicro.com/
Copyright (c) 2007-2009 Joerg Wunsch
```

```
System wide configuration file is
"C:\Users\JakobsenD.G02\Desktop\Ordner\FFP\Projekte\arduino-1.0.1-windows\arduino-1.0.1\hardware\tools\avr\etc\avrdude.conf"
```

```
Using Port          : \\.\COM5
Using Programmer    : stk500v2
avrdude: Send: . [1b] . [01] . [00] . [01] . [0e] . [01] . [14]
avrdude: Recv: . [1b]
avrdude: Recv: . [01]
avrdude: Recv: . [00]
avrdude: Recv: . [0b]
avrdude: Recv: . [0e]
avrdude: Recv: . [01]
avrdude: Recv: . [00]
avrdude: Recv: . [08]
avrdude: Recv: S [53]
avrdude: Recv: T [54]
avrdude: Recv: K [4b]
avrdude: Recv: 5 [35]
avrdude: Recv: 0 [30]
avrdude: Recv: 0 [30]
avrdude: Recv: _ [5f]
avrdude: Recv: 2 [32]
avrdude: Recv: . [02]
avrdude: stk500v2_getsync(): found STK500 programmer
```

```
AVR Part          : ATMEGA2560
Chip Erase delay  : 9000 us
PAGEL             : PD7
BS2               : PA0
RESET disposition : dedicated
RETRY pulse       : SCK
serial program mode : yes
parallel program mode : yes
Timeout           : 200
StabDelay         : 100
CmdexeDelay       : 25
SyncLoops         : 32
ByteDelay         : 0
PollIndex         : 3
PollValue         : 0x53
Memory Detail     :
```

				Block Poll			Page				Polled		
Memory	Type	Mode	Delay	Size	Indx	Paged	Size	Size	#Pages	MinW	MaxW	ReadBack	Polled

ee	eprom	65	10	8	0	no	4096	8	0	9000	9000	0x00	0x00

				Block Poll			Page				Polled		
Memory	Type	Mode	Delay	Size	Indx	Paged	Size	Size	#Pages	MinW	MaxW	ReadBack	Polled

flash		65	10	256	0	yes	262144	256	1024	4500	4500	0x00	0x00

Memory	Type	Mode	Delay	Block Size	Poll Indx	Paged	Size	Page Size	#Pages	MinW	MaxW	Polled ReadBack
lfuse		0	0	0	0	no	1	0	0	9000	9000	0x00 0x00
hfuse		0	0	0	0	no	1	0	0	9000	9000	0x00 0x00
efuse		0	0	0	0	no	1	0	0	9000	9000	0x00 0x00
lock		0	0	0	0	no	1	0	0	9000	9000	0x00 0x00
calibration		0	0	0	0	no	1	0	0	0	0	0x00 0x00
signature		0	0	0	0	no	3	0	0	0	0	0x00 0x00

Programmer Type : STK500V2

Description : Atmel STK500 Version 2.x firmware

Programmer Model: STK500

```

avrdude: Send: . [1b] . [02] . [00] . [02] . [0e] . [03] . [90] . [86]
avrdude: Recv: . [1b]
avrdude: Recv: . [02]
avrdude: Recv: . [00]
avrdude: Recv: . [03]
avrdude: Recv: . [0e]
avrdude: Recv: . [03]
avrdude: Recv: . [00]
avrdude: Recv: . [03]
avrdude: Recv: . [14]
avrdude: Send: . [1b] . [03] . [00] . [02] . [0e] . [03] . [91] . [86]
avrdude: Recv: . [1b]
avrdude: Recv: . [03]
avrdude: Recv: . [00]
avrdude: Recv: . [03]
avrdude: Recv: . [0e]
avrdude: Recv: . [03]
avrdude: Recv: . [00]
avrdude: Recv: . [02]
avrdude: Recv: . [14]
avrdude: Send: . [1b] . [04] . [00] . [02] . [0e] . [03] . [92] . [82]
avrdude: Recv: . [1b]
avrdude: Recv: . [04]
avrdude: Recv: . [00]
avrdude: Recv: . [03]
avrdude: Recv: . [0e]
avrdude: Recv: . [03]
avrdude: Recv: . [00]
avrdude: Recv: . [0a]
avrdude: Recv: . [1b]

```

Hardware Version: 3

Firmware Version Master : 2.10

```

avrdude: Send: . [1b] . [05] . [00] . [02] . [0e] . [03] . [9a] . [8b]
avrdude: Recv: . [1b]
avrdude: Recv: . [05]
avrdude: Recv: . [00]
avrdude: Recv: . [03]
avrdude: Recv: . [0e]
avrdude: Recv: . [03]

```

```

avrdude: Recv: . [00]
avrdude: Recv: . [00]
avrdude: Recv: . [10]
      Topcard          : Unknown
avrdude: Send: . [1b] . [06] . [00] . [02] . [0e] . [03] . [94] . [86]
avrdude: Recv: . [1b]
avrdude: Recv: . [06]
avrdude: Recv: . [00]
avrdude: Recv: . [03]
avrdude: Recv: . [0e]
avrdude: Recv: . [03]
avrdude: Recv: . [00]
avrdude: Recv: . [00]
avrdude: Recv: . [13]
      Vtarget          : 0.0 V
avrdude: Send: . [1b] . [07] . [00] . [02] . [0e] . [03] . [98] . [8b]
avrdude: Recv: . [1b]
avrdude: Recv: . [07]
avrdude: Recv: . [00]
avrdude: Recv: . [03]
avrdude: Recv: . [0e]
avrdude: Recv: . [03]
avrdude: Recv: . [00]
avrdude: Recv: . [03]
avrdude: Recv: . [11]
avrdude: Send: . [1b] . [08] . [00] . [02] . [0e] . [03] . [95] . [89]
avrdude: Recv: . [1b]
avrdude: Recv: . [08]
avrdude: Recv: . [00]
avrdude: Recv: . [03]
avrdude: Recv: . [0e]
avrdude: Recv: . [03]
avrdude: Recv: . [00]
avrdude: Recv: . [00]
avrdude: Recv: . [1d]
avrdude: Send: . [1b] . [09] . [00] . [02] . [0e] . [03] . [96] . [8b]
avrdude: Recv: . [1b]
avrdude: Recv: . [09]
avrdude: Recv: . [00]
avrdude: Recv: . [03]
avrdude: Recv: . [0e]
avrdude: Recv: . [03]
avrdude: Recv: . [00]
avrdude: Recv: . [00]
avrdude: Recv: . [1c]
avrdude: Send: . [1b] . [0a] . [00] . [02] . [0e] . [03] . [97] . [89]
avrdude: Recv: . [1b]
avrdude: Recv: . [0a]
avrdude: Recv: . [00]
avrdude: Recv: . [03]
avrdude: Recv: . [0e]
avrdude: Recv: . [03]
avrdude: Recv: . [00]
avrdude: Recv: . [00]
avrdude: Recv: . [1f]
      SCK period       : 3.3 us
      Varef            : 0.0 V
      Oscillator       : Off

avrdude: Send: . [1b] . [0b] . [00] . [03] . [0e] . [02] . [9e] . [01] . [80]
avrdude: Recv: . [1b]
avrdude: Recv: . [0b]
avrdude: Recv: . [00]
avrdude: Recv: . [02]
avrdude: Recv: . [0e]
avrdude: Recv: . [02]
avrdude: Recv: . [00]

```

```
avrdude: Recv: . [1e]
avrdude: Send: . [1b] . [0c] . [00] . [0c] . [0e] . [10] . [c8] d [64] . [19] [20] . [00] S
[53] . [03] . [ac] S [53] . [00] . [00] ? [3f]
avrdude: Recv: . [1b]
avrdude: stk500v2_ReceiveMessage(): timeout
avrdude: Send: . [1b] . [0c] . [00] . [01] . [0e] . [01] . [19]
avrdude: Recv: . [0c]
avrdude: Recv: . [00]
avrdude: Recv: . [02]
avrdude: Recv: . [0e]
avrdude: Recv: . [10]
avrdude: Recv: . [c0]
avrdude: Recv: . [cb]
avrdude: Recv: . [1b]
avrdude: Recv: . [0c]
avrdude: Recv: . [00]
avrdude: Recv: . [0b]
avrdude: Recv: . [0e]
avrdude: Recv: . [01]
avrdude: Recv: . [00]
avrdude: Recv: . [08]
avrdude: Recv: S [53]
avrdude: Recv: T [54]
avrdude: Recv: K [4b]
avrdude: Recv: 5 [35]
avrdude: Recv: 0 [30]
avrdude: Recv: 0 [30]
avrdude: Recv: _ [5f]
avrdude: Recv: 2 [32]
avrdude: Recv: . [0f]
avrdude: stk500v2_getsync(): found STK500 programmer
avrdude: AVR device initialized and ready to accept instructions
```

```
Reading | avrdude: Send: . [1b] . [0d] . [00] . [08] . [0e] . [1d] . [04] . [04] . [00] 0 [30]
. [00] . [00] . [00] = [3d]
avrdude: Recv: . [1b]
avrdude: Recv: . [0d]
avrdude: Recv: . [00]
avrdude: Recv: . [07]
avrdude: Recv: . [0e]
avrdude: Recv: . [1d]
avrdude: Recv: . [00]
avrdude: Recv: . [00]
avrdude: Recv: . [00]
avrdude: Recv: . [00]
avrdude: Recv: . [00]
avrdude: Recv: . [00]
avrdude: Recv: . [02]
avrdude: Send: . [1b] . [0e] . [00] . [08] . [0e] . [1d] . [04] . [04] . [00] 0 [30] . [00] .
[01] . [00] ? [3f]
avrdude: Recv: . [1b]
avrdude: Recv: . [0e]
avrdude: Recv: . [00]
avrdude: Recv: . [07]
avrdude: Recv: . [0e]
avrdude: Recv: . [1d]
avrdude: Recv: . [00]
avrdude: Recv: . [00]
avrdude: Recv: . [00]
avrdude: Recv: . [00]
avrdude: Recv: . [00]
avrdude: Recv: . [00]
avrdude: Recv: . [01]
#####avrdude: Send: . [1b] . [0f] . [00] . [08] . [0e] . [1d] . [04] . [04] . [00]
0 [30] . [00] . [02] . [00] = [3d]
avrdude: Recv: . [1b]
avrdude: Recv: . [0f]
```

```
avrdude: Recv: . [00]
avrdude: Recv: . [07]
avrdude: Recv: . [0e]
avrdude: Recv: . [1d]
avrdude: Recv: . [00]
avrdude: Recv: . [00]
avrdude: Recv: . [00]
avrdude: Recv: . [00]
avrdude: Recv: . [00]
avrdude: Recv: . [00]
avrdude: Recv: . [00]
avrdude: Recv: . [00]
avrdude: Recv: . [00]
```

```
##### | 100% 0.98s
```

```
avrdude: Device signature = 0x000000
avrdude: Yikes! Invalid device signature.
Double check connections and try again, or use -F to override
this check.
```

```
avrdude: Send: . [1b] . [10] . [00] . [03] . [0e] . [11] . [01] . [01] . [17]
avrdude: Recv: . [1b]
avrdude: Recv: . [10]
avrdude: Recv: . [00]
avrdude: Recv: . [02]
avrdude: Recv: . [0e]
avrdude: Recv: . [11]
avrdude: Recv: . [00]
avrdude: Recv: . [16]
```

```
avrdude done. Thank you.
```