

GRUNDIG
electronic**OSCILLOSCOPE MO 53**

S.Nr. 9.40011-1101

40011-942.11

Datum/Date
04.84Deutsch
English**Schaltbild/Circuit diagram**

INHALT Contents	Baustein Modul	Schaltbild Circuit diagram	Nr./No.
Blockschaltbild Block diagram		40011-921.00	1
Gesamtschaltbild Full connection diagram		40011-906.00 Bl.1	2
Leiterplatte unten Circuit panel lower		40011-906.00 Bl. 2	3
Leiterplatte unten Printed circuit board lower	40011-700.00		4
Leiterplatte oben Circuit panel upper		40011-906.00 Bl. 3	5
Leiterplatte oben Printed circuit board upper	40011-710.00		6
Bedienplatte Front panel	40011-720.00	40011-906.00 Bl. 4	7
Endstufenplatte Final amplifier panel	40011-730.00	40011-906-00 Bl. 5	8
Lämpchenplatte †) Printed circuit board lamp	40011-740.00		

†) Im Gesamtschaltbild enthalten
Contained in full connection diagram

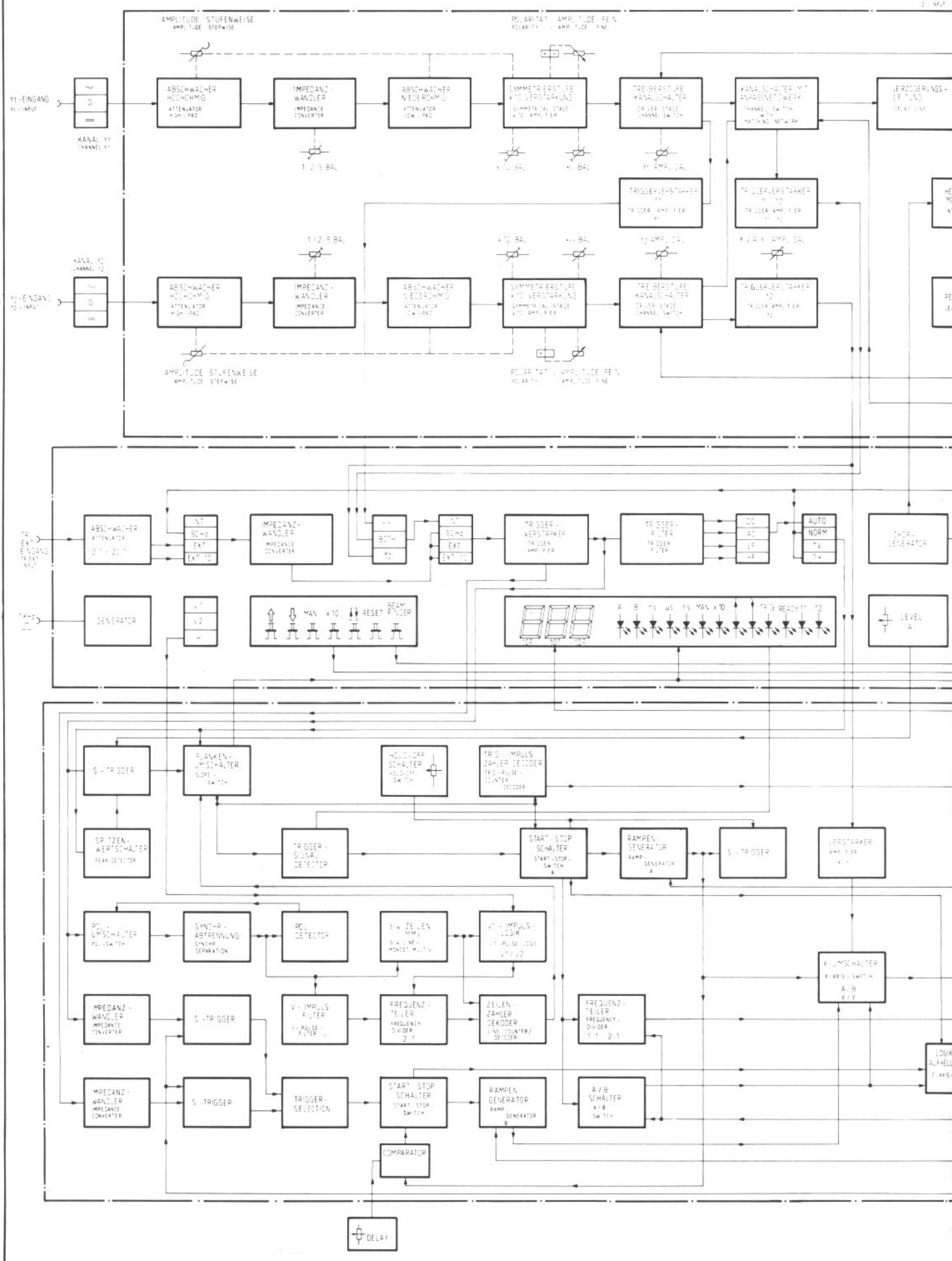
Änderungen vorbehalten/Alterations reserved

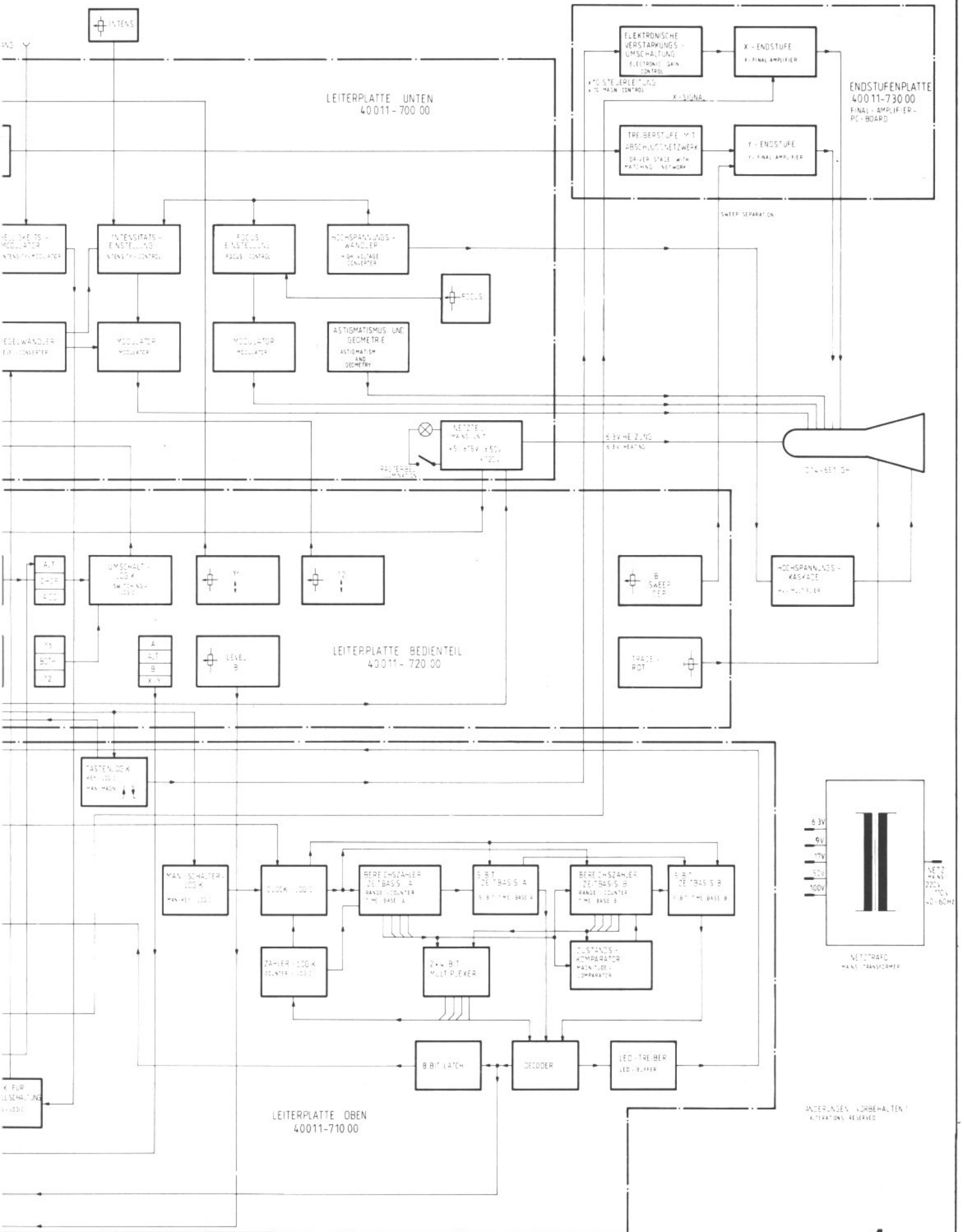
Toute communication ou reproduction de ce document, toute exploitation ou communication de son contenu sont interdites, sauf autorisation expresse. Tout manquement à cette règle est illicite et expose son auteur au versement de dommages et intérêts. Tous nos droits sont réservés pour le cas de la délivrance d'un brevet ou celui de l'enregistrement d'un modèle d'utilité.

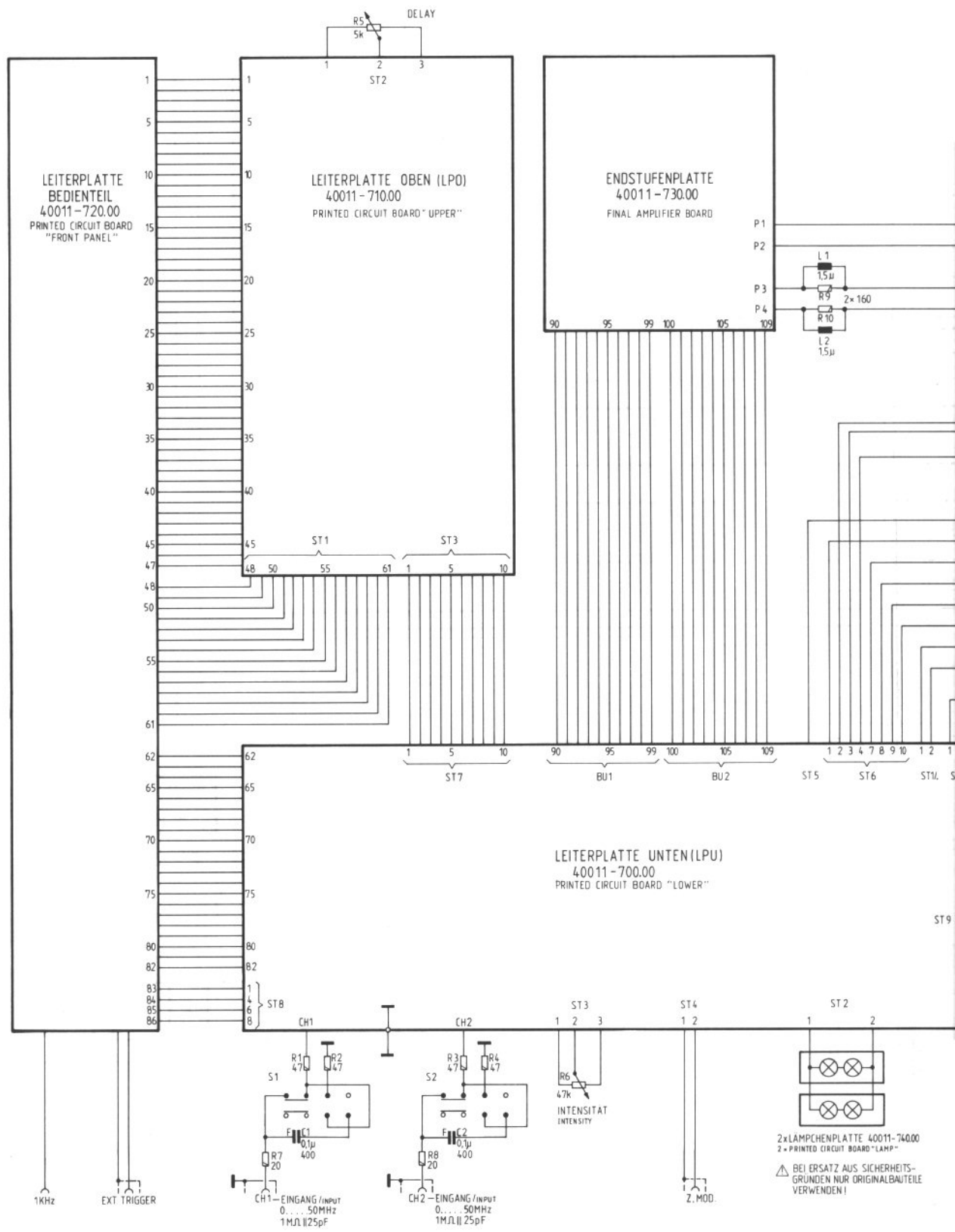
Sin nuestra expresa autorización, queda terminantemente prohibida la reproducción total o parcial de este documento, así como su uso indebido y/o su exhibición o comunicación a terceros. De las infracciones se exigirá el correspondiente resarcimiento de daños y perjuicios. Se reservan todos los derechos para el caso de la concesión de patente de invención o el registro de Model Industrial.

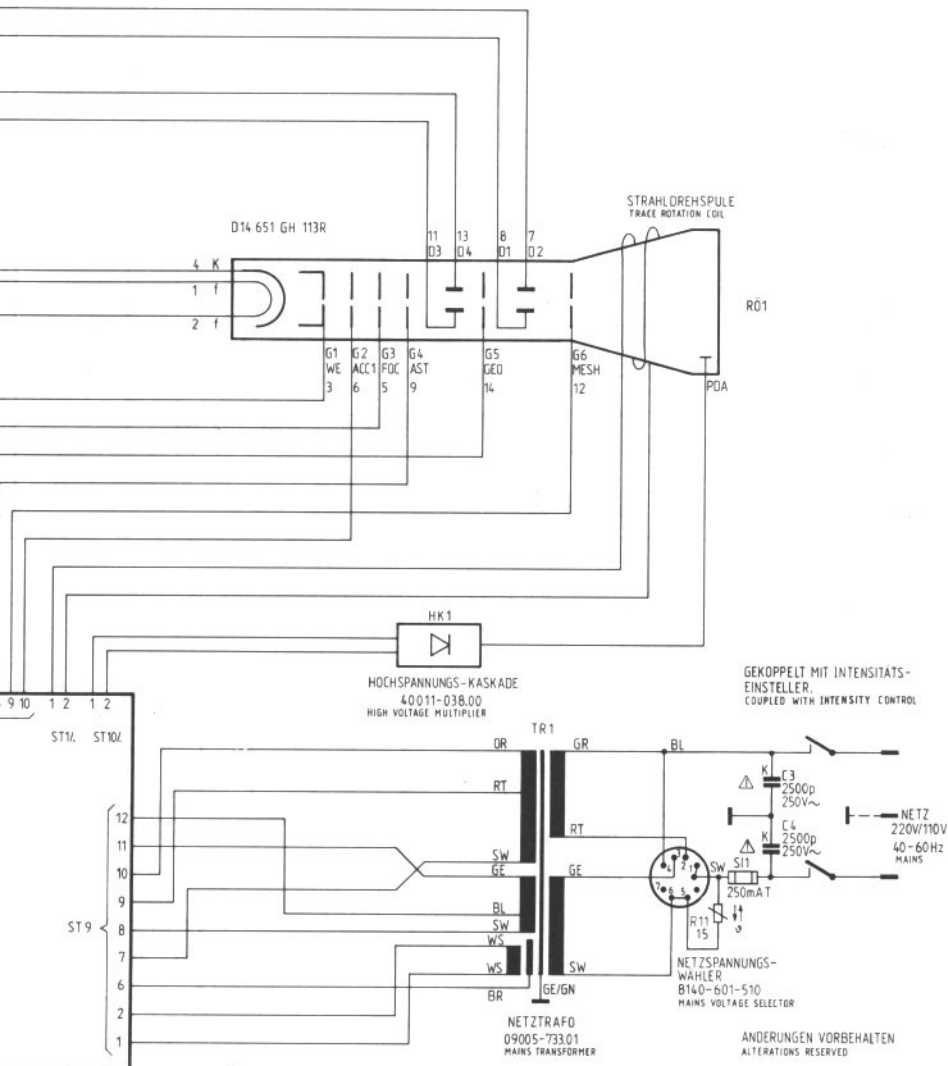
Weitergabe sowie Vervielfältigung dieser Unterlagen, Verwertung und Mitteilung ihres Inhalts nicht gestattet, soweit nicht ausdrücklich zugestanden. Zuwiderhandlungen verpflichten zu Schadenersatz. Alle Rechte für den Fall der Patenterteilung oder GM-Eintragung vorbehalten.

Copying of this document, and giving it to others and the use or communication of the contents thereof, are forbidden without express authority. Offenders are liable to the payment of damages. All rights are reserved in the event of the grant of a patent or the registration of a utility model or design.

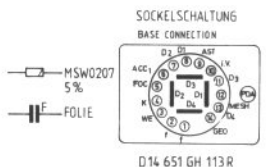


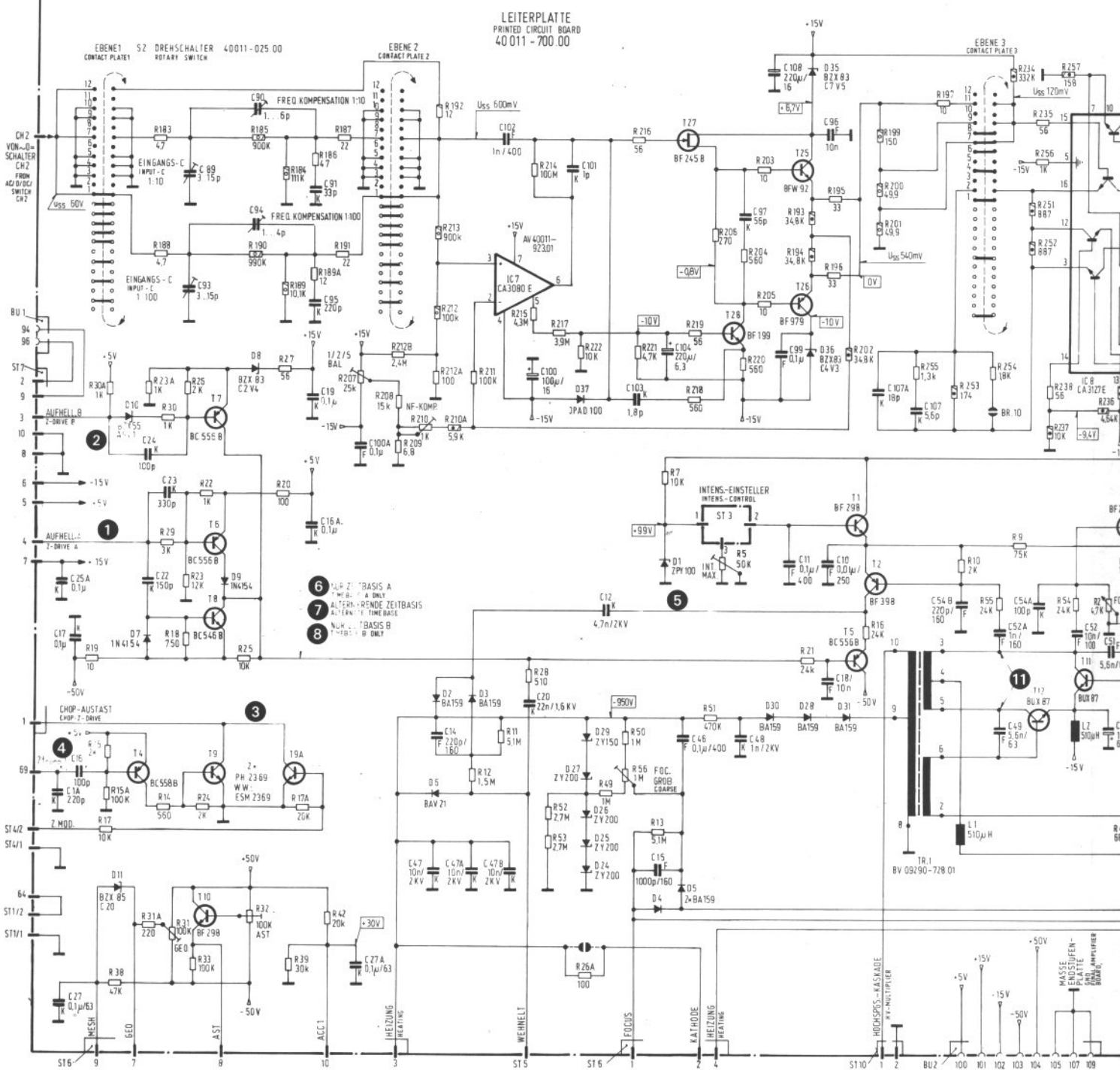
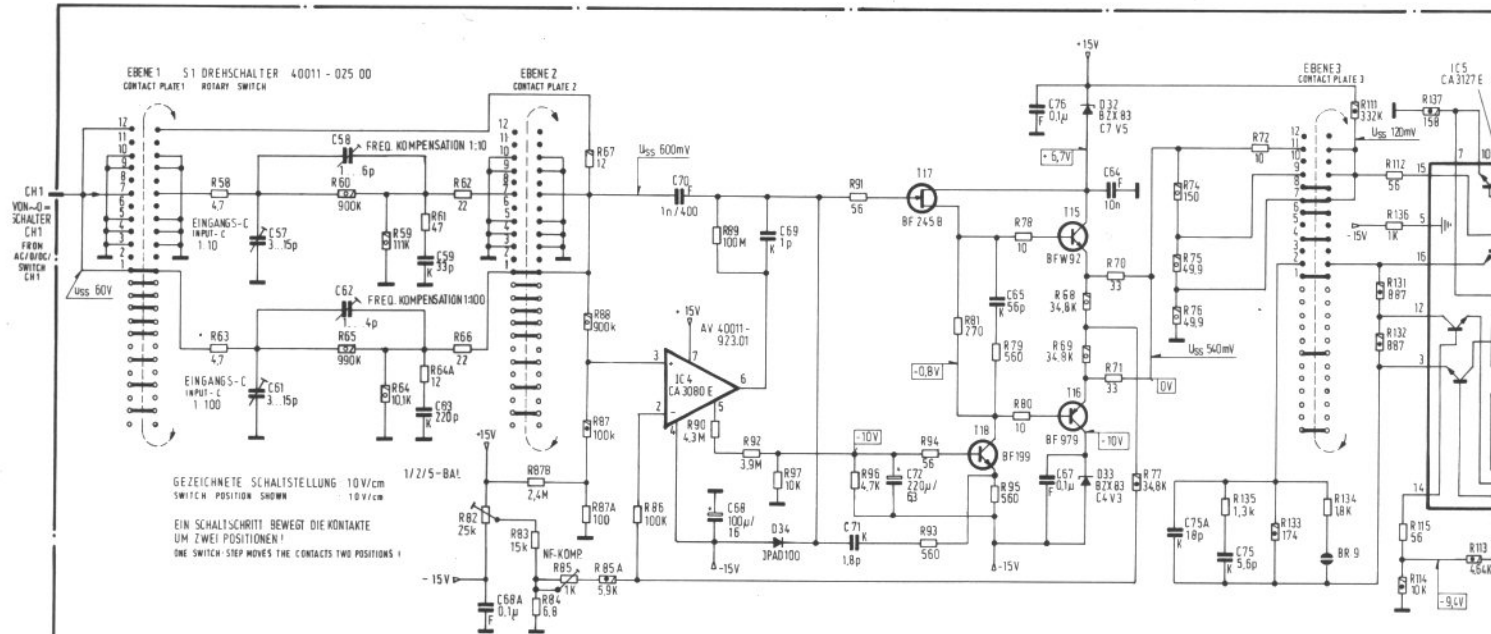


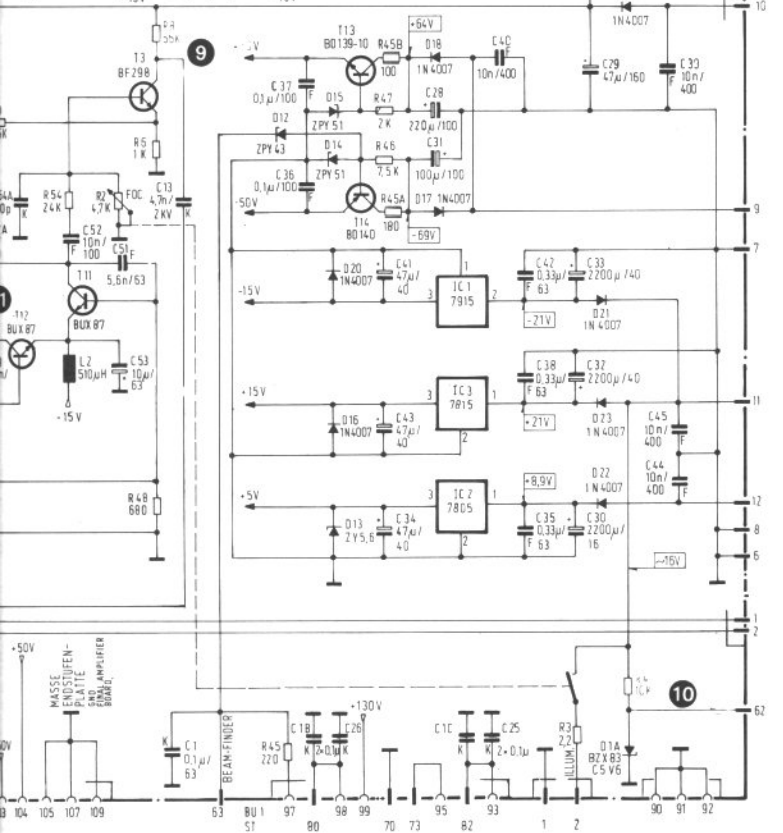
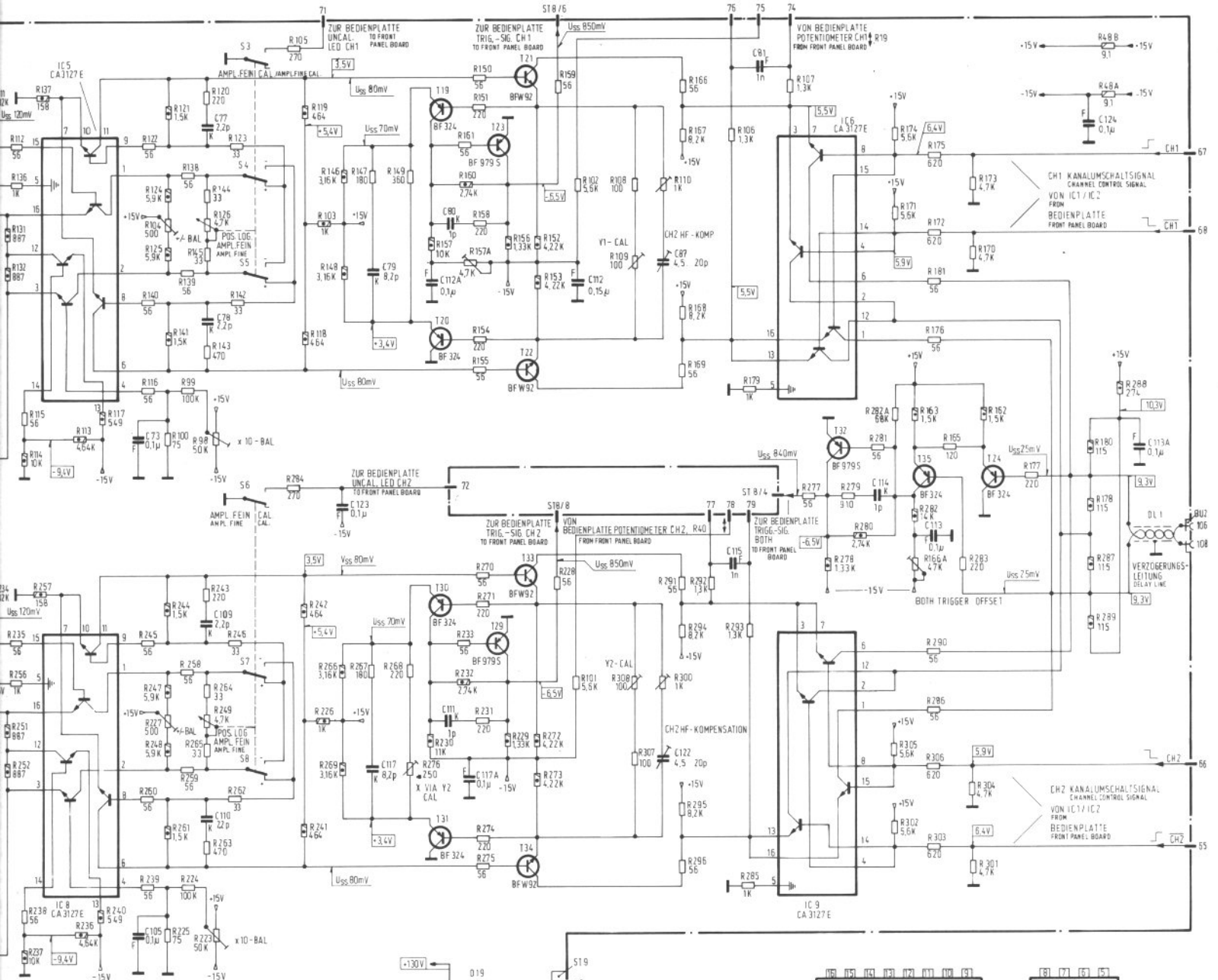




1-740.00
IP
THEITS-
BAUTEILE







ANSCHLUSSNUMMERN 62-82 ZUR BEDIENPL. 62-82
PIN CONNECTION 62-82 TO FRONT PANEL BOARD 62-82

ANSCHLUSSNUMMERN 90-109 ZUR ENDSUFENPL. 90-109
PIN CONNECTION 90-109 TO FINAL AMPLIFIER BOARD 90-109

STECKER:
 1 STRAHLDREHPULVE / TRACE ROT COIL
 2 LAMPHENPLATTE / LAMP BOARD
 3 INTENS. EINSTELLER / INTENS CONTROL
 4 Z-MOD-BUEHSE / Z-MOD-JACK
 5 WEHNELTZYLINDER / WEHNELT
 6 BILDROHRE / CRT
 7 LEITERPLATTE OBEN / UPPER BOARD
 8 BEDIENPLATTE / FRONT PANEL BOARD
 9 NETZTRAFO / MAINS TRANSFORMER
 10 HOCHSPG. KASKADE / HV MULTIPLIER

MESSBEDINGUNGEN BETRIEBSART CH1, SIGNALPEGEL Δ 50mm AUSLENKUNG
MEASURING CONDITIONS MODE CH1 SIGNAL LEVEL Δ 50mm DEFLECTION
 STRAHLLAGE MITTE MIT 1kHz SINUS
 BEAM CENTERED WITH SINUS 1kHz

ÄNDERUNGEN VORBEHALTEN
ALTERATIONS RESERVED

LEGENDE

- Z 0207
- B 0207, SCHWER ENTLAMMBAR
OFFICIELL INFLAMMABLE
- MSW 0207, 5%
- MSW 0207, 1%
- MSW 0207, 0.5%
- ELEKTROLYT
ELECTROLYTIC
- F FOLIE
FILM
- K KERAMIK
KERAMIC

IC 8 CA3127E **IC 9 CA3127E**

GLEICHSPANNUNG DC-VOLTAGE **WECHSELSPANNUNG** AC-VOLTAGE



BC 546 **BC 556** **BC 558** **BF 298** **BF 398** **BF 324** **PH 2369**
BF 979 S **BF W 92**

E 1 A **E 2 A** **E 3 B**
MC 7805 **MC 7815** **BO 139** **BO 140** **BUX 87**

Meßbedingungen zu den Oszillogrammen Leiterplatte „UNTEN“

Measuring conditions to the oscillograms “BOTTOM” Circuit Board

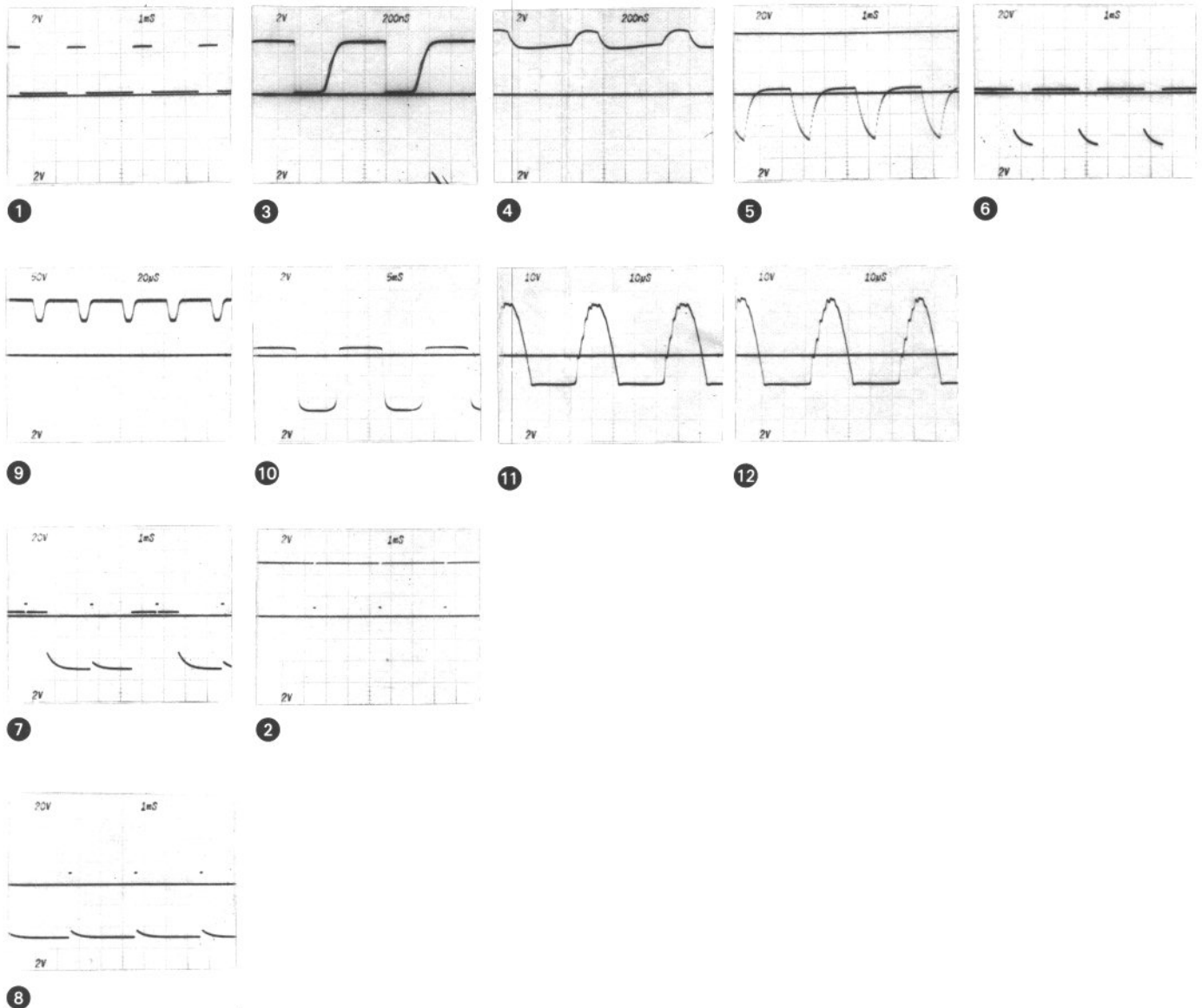
Sinussignal an CH 1, ca. 1 kHz, 6 cm Auslenkung
Sine-wave signal to CH 1, 1 kHz approx., deflection 6 cm

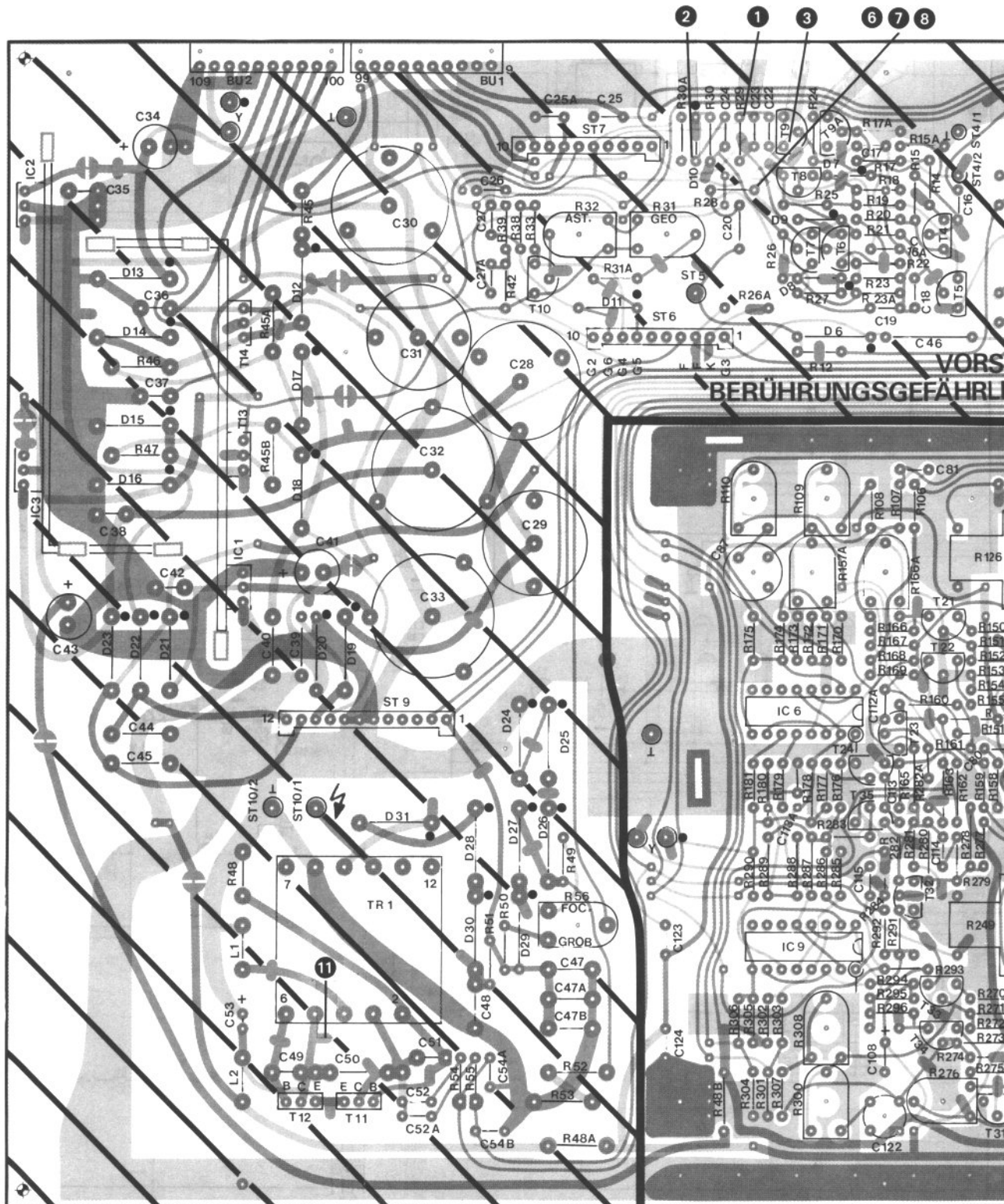
Triggerflanke : 
Trigger edge : 
A-Trigger : AUTO
DC
CH 1
INT

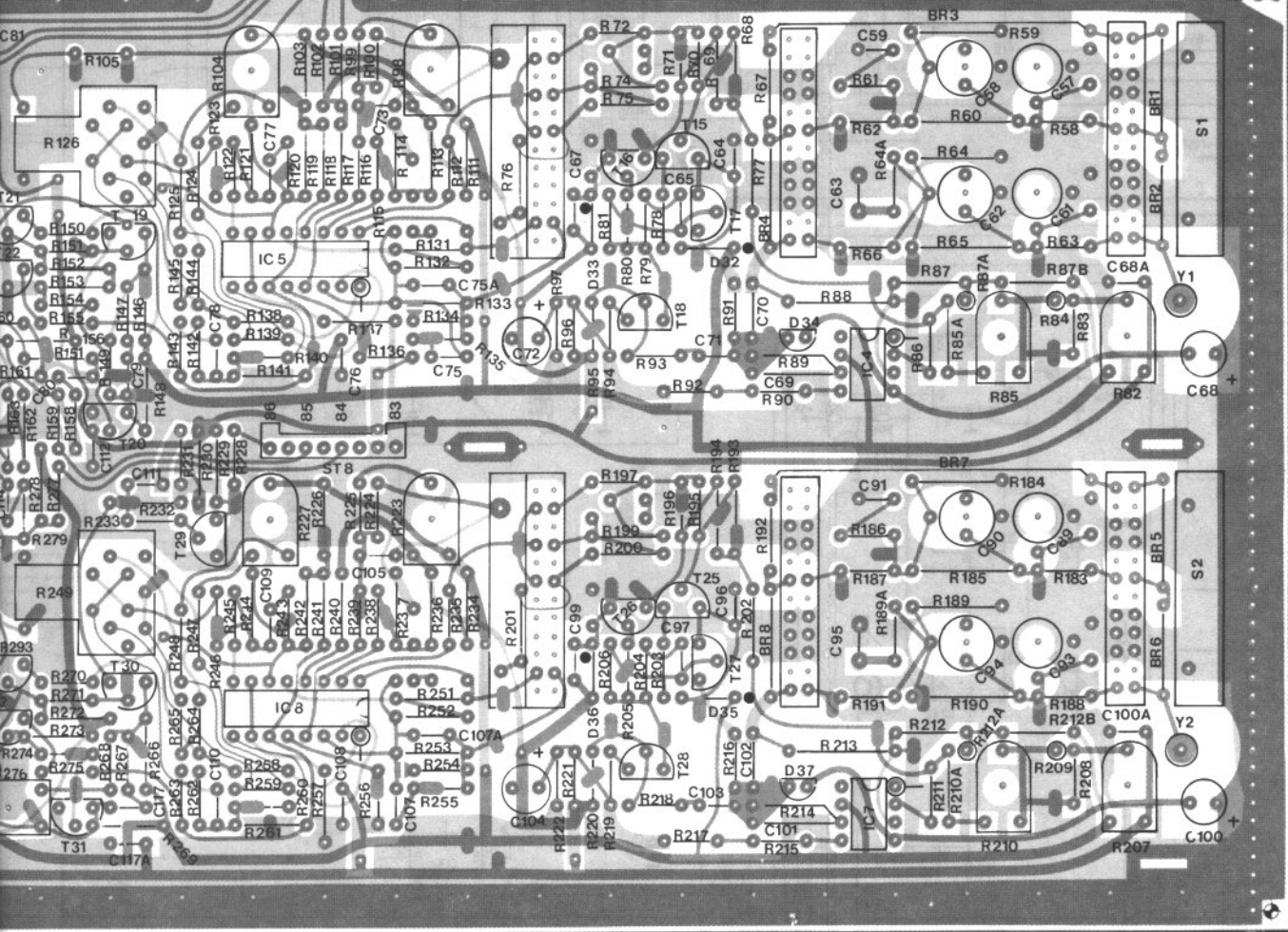
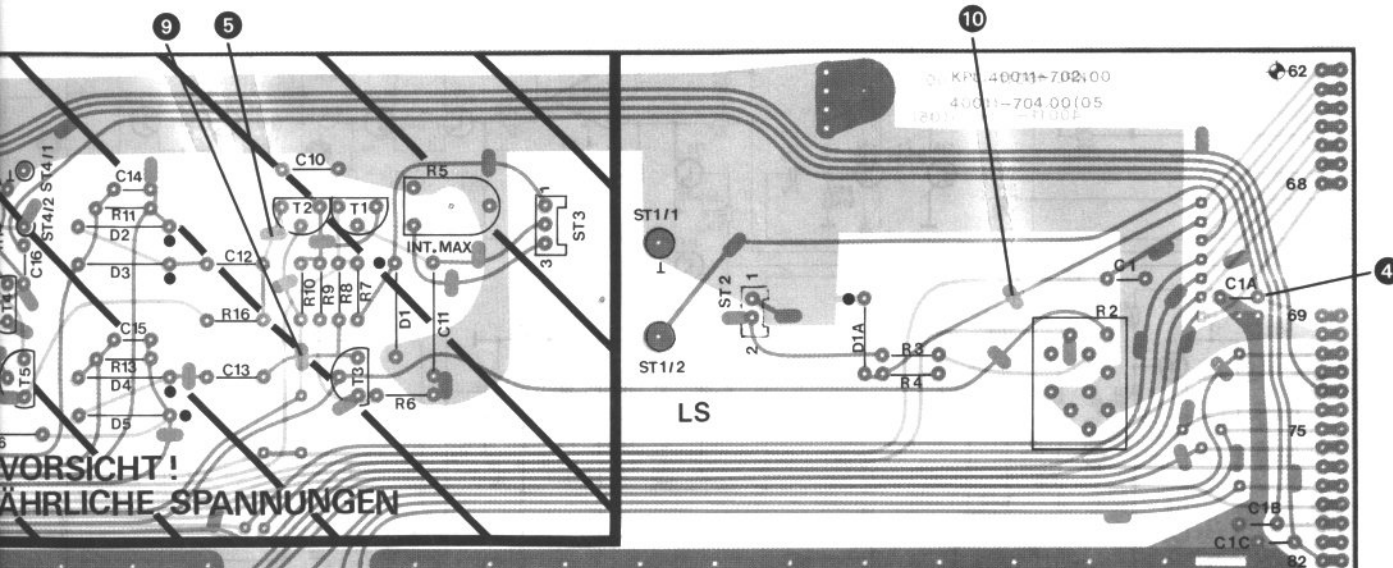
VERTICAL MODE: CH 1 bei/at ①...⑪ außer/excepted ③, ④
BOTH, CHOP bei/at ③, ④

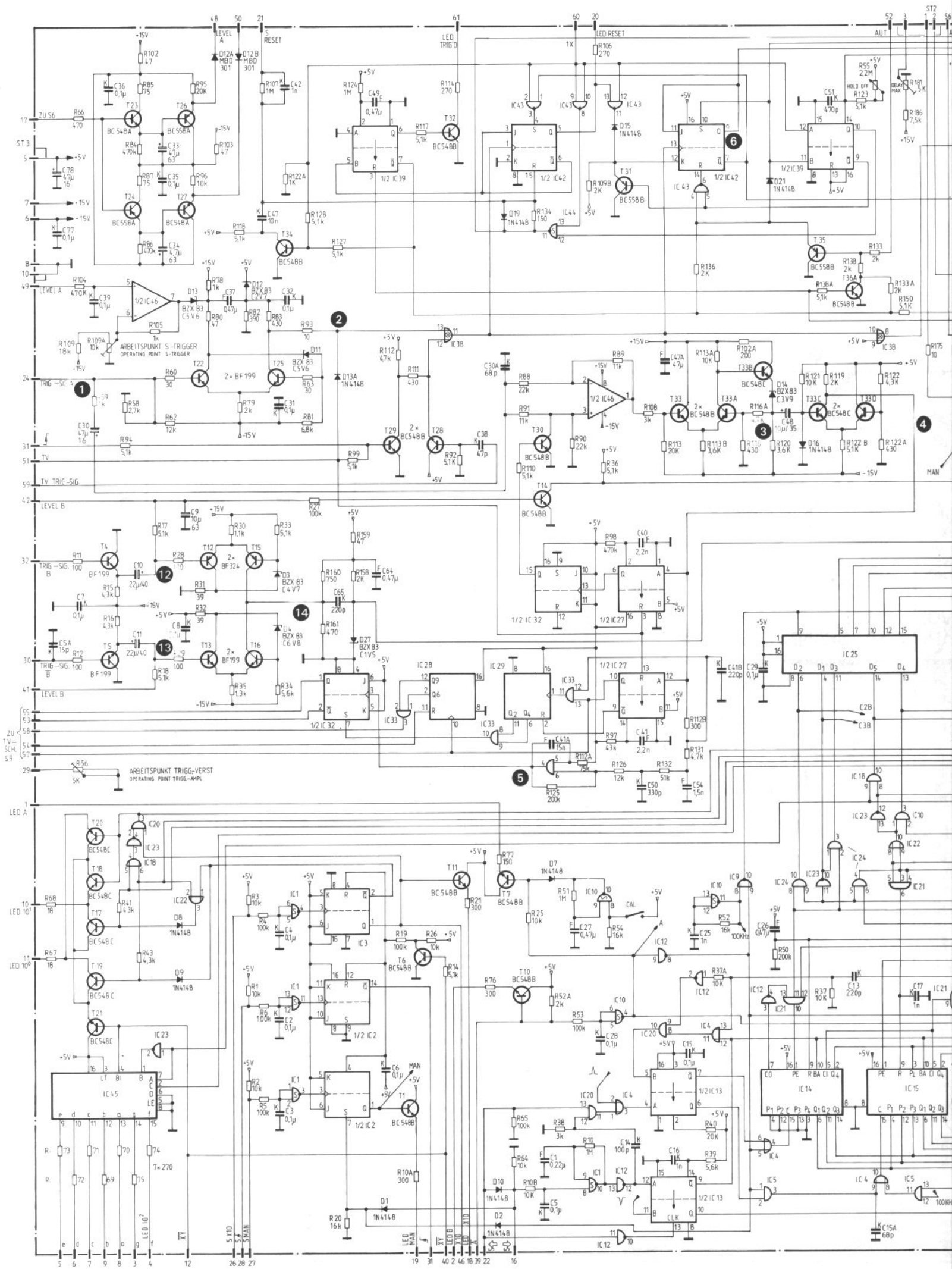
TIME BASE : nur/only A : 200 μ s/cm bei/at ①, ③...⑥, ⑨...⑫
ALT A : 200 μ s/cm } bei/at ⑦, ②
B : 10 μ s/cm }
nur/only B : 10 μ s/cm bei/at ⑧

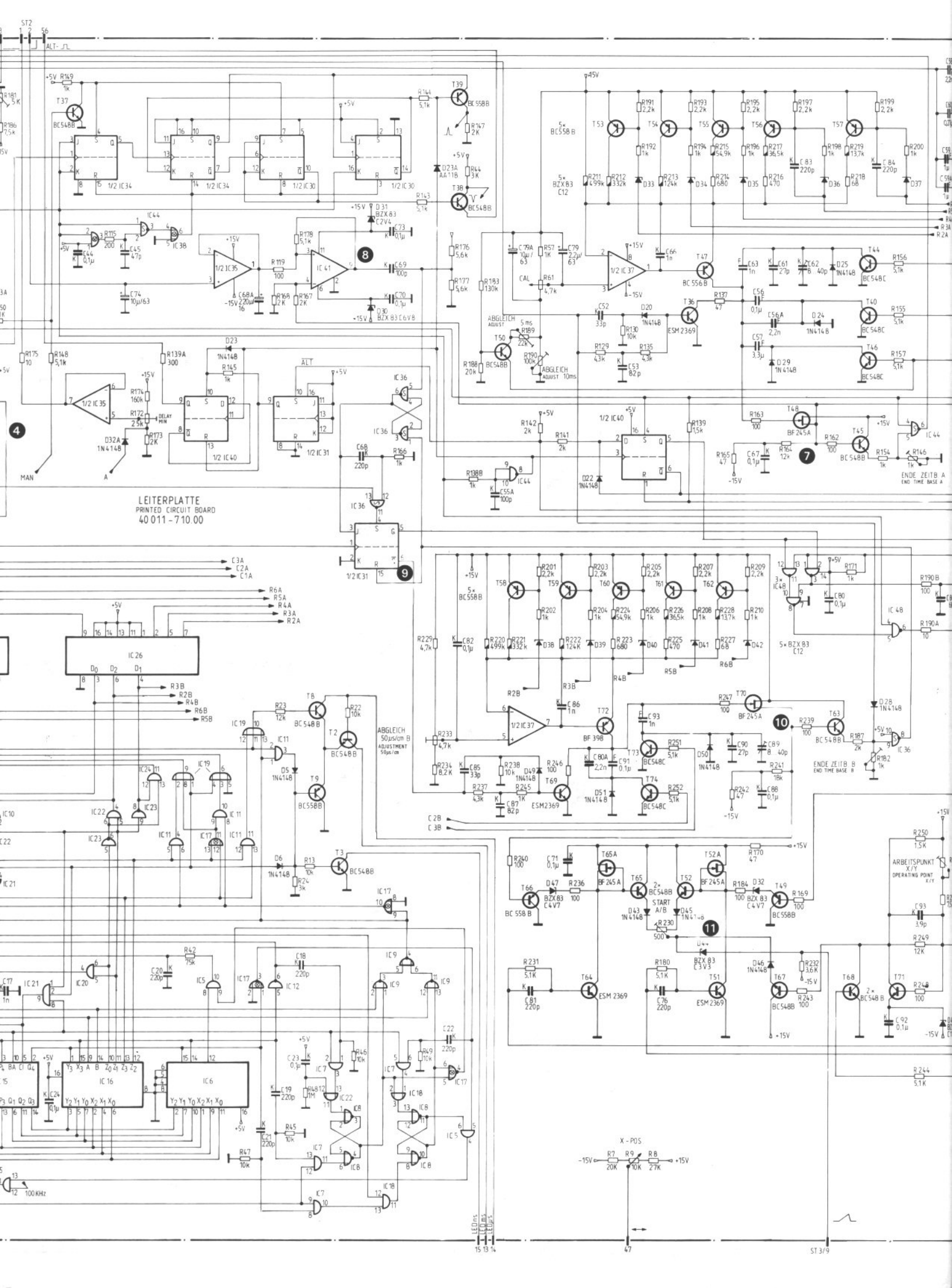
B-Trigger : START AFTER DELAY











LEITERPLATTE
PRINTED CIRCUIT BOARD
40 011 - 710.00

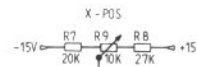
4

9

10

7

11



LEDs
LEDs

Meßbedingungen zu den Oszillogrammen Leiterplatte „OBEN“

Measuring conditions to the oscillograms “TOP” Circuit Board

TV-Signal an CH 1, Auslenkung 5 cm

TV-Signal to CH 1, deflection 5 cm

Der zweite Kanal bildet die Null-Linie

The second channel represents the zero line

Triggerflanke :

Trigger edge :

A-Trigger : Auto bei/at **1, 2, 6...11**
TV, H bei/at **3, 4** und/and **5**

DC

CH 1

INT

TIME BASE : ALT

A : 10 μ s/cm

B : 2 μ /cm

LEVEL POT. : linker Anschlag/anticlockwise limit position

DELAY-POT. : Mittelstellung/mid-position

TIME BASE B : START AFTER DELAY bei/at **1...11**

TIME BASE B getriggert auf Flanke bei/at **12...14**

TIME BASE B triggered to edge bei/at **12...14**

Oszillogramm **15** : Sinussignal 100 kHz $U_{ss} \approx 3V$

CH 1 : Signal an/to **1** CH 3 : Signal an/to **9**

CH 2 : Signal an/to **6** CH 4 : Signal an/to **11**

TYPE DER IC

TYPES OF IC

- 1 HEF 4093 BP
- 2 HEF 4027 BP
- 3 HEF 4027 BP
- 4 HEF 4071 BP
- 5 HEF 4081 BP
- 6 HEF 4585 BP
- 7 HEF 4081 BP
- 8 HEF 4001 BP
- 9 HEF 4001 BP
- 10 HEF 4093 BP
- 11 HEF 4081 BP
- 12 HEF 4069 UBP
- 13 HEF 4528 BP
- 14 HEF 4516 BP
- 15 HEF 4516 BP
- 16 HEF 4519 BP
- 17 HEF 4077 BP
- 18 HEF 4071 BP
- 19 HEF 4075 BP
- 20 HEF 4081 BP
- 21 HEF 4073 BP
- 22 HEF 4071 BP
- 23 HEF 4069 BP
- 24 HEF 4081 BP
- 25 HEF 40174 BP
- 26 HEF 40174 BP
- 27 HEF 4528 BP
- 28 HEF 4040 BP
- 29 HEF 4024 BP
- 30 74 LS 76
- 31 SN 74 LS 112N
- 32 HEF 4027 BP
- 33 HEF 4081 BP
- 34 SN 74 S 112N
- 35 TL 082 CP
- 36 74 LS 132
- 37 TL 082 CP
- 38 SN 74 S 86N
- 39 HEF 4528 BP
- 40 74 LS 74
- 41 MC 1770
- 42 74 S 112N
- 43 74 LS 00
- 44 74 LS 132
- 45 HEF 4511 BP
- 46 TL 082 CP
- 47 TL 082 CP
- 48 SN 74 LS00N

ANSCHLUSSNUMMERN 1...61 ZUR BEDIENTAFEL 1...5
PIN CONNECTION: 1...61 TO FRONT PANEL BOARD 1...51

ST 3: ZUR LEITERPLATTE UNTEN
TO PRINTED CIRCUIT BOARD LOWER

ST 2: ZUM DELAY-POT
TO DELAY-POT.



- BF 199
- BC 548 A/B/C
- BC 558 A/B/C
- BC 556 B
- BF 324
- BF 398
- ESM 2369

MSW 0207 1%

Z 0207

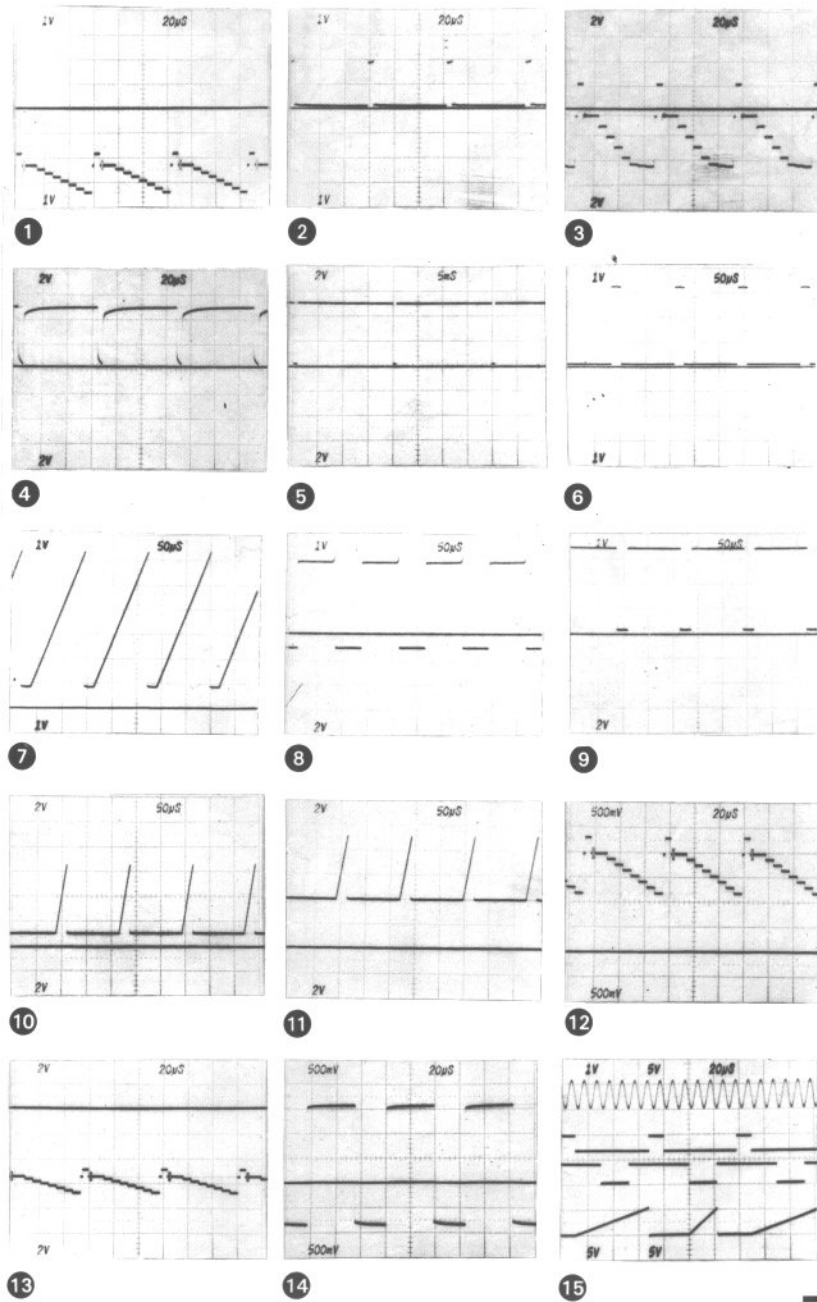
FOLIE
FOLIE

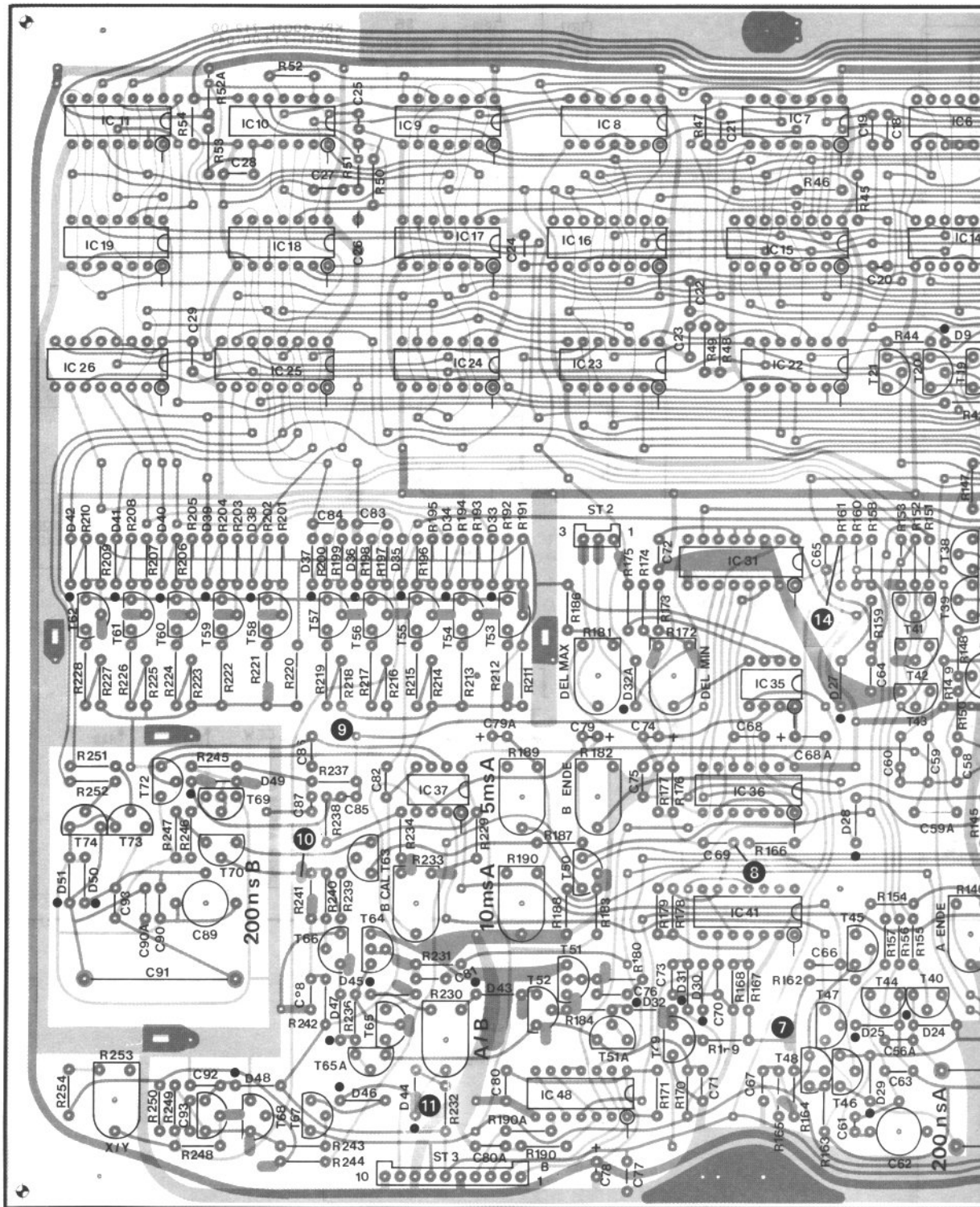
KERAMIK
CERAMIC

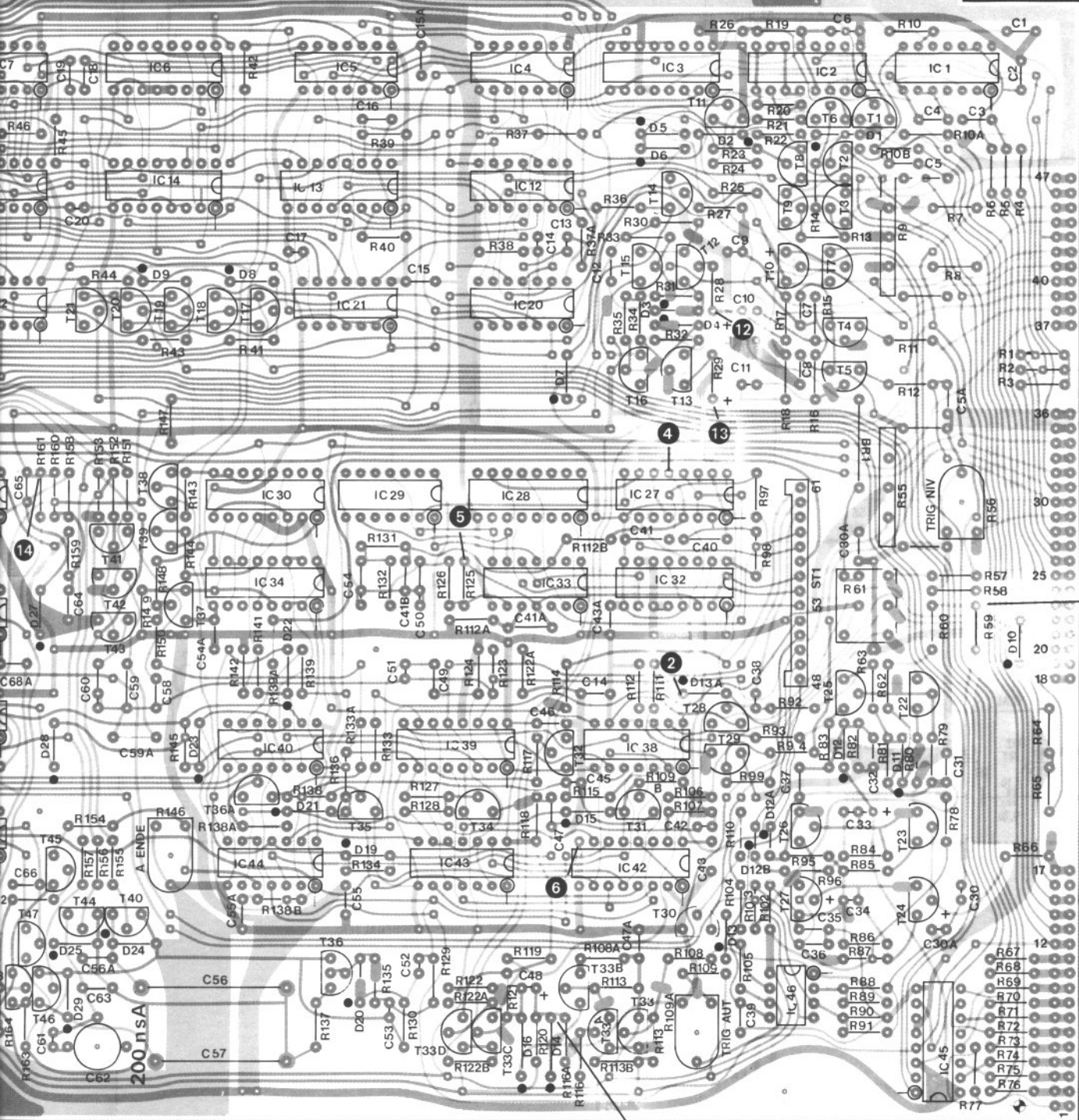
ELEKTROLYT
ELECTROLYTIC

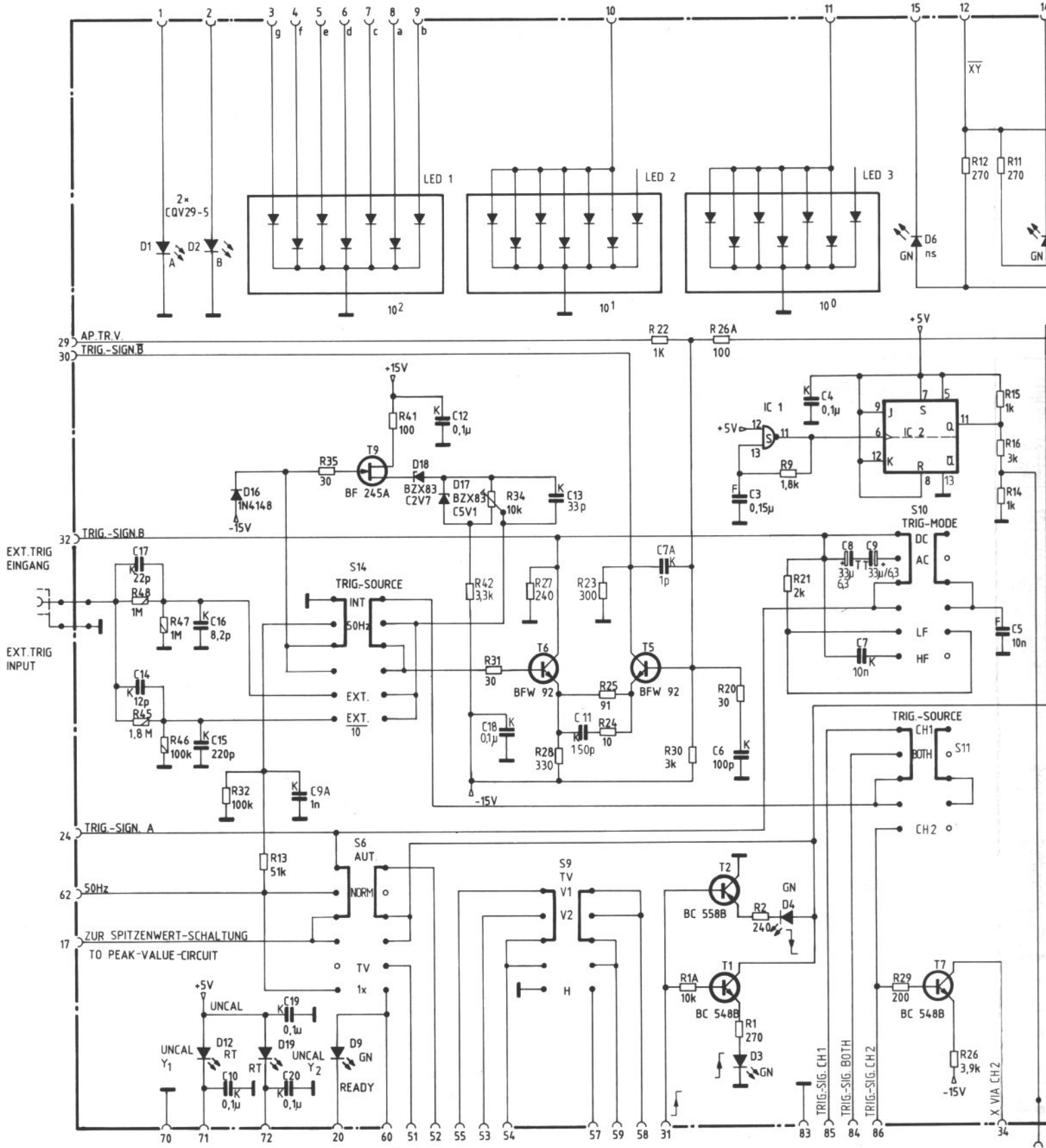
TANTAL-ELEKTROLYT
TANTAL-ELECTROLYTIC

ÄNDERUNGEN VORBEHALTEN!
ALTERATIONS RESERVED!

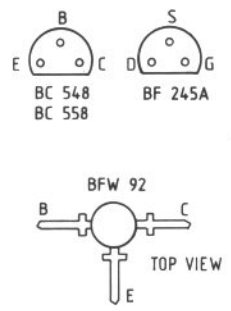






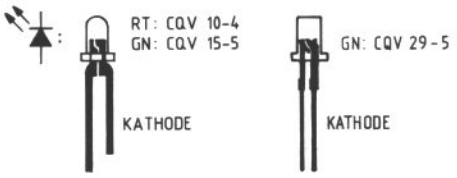


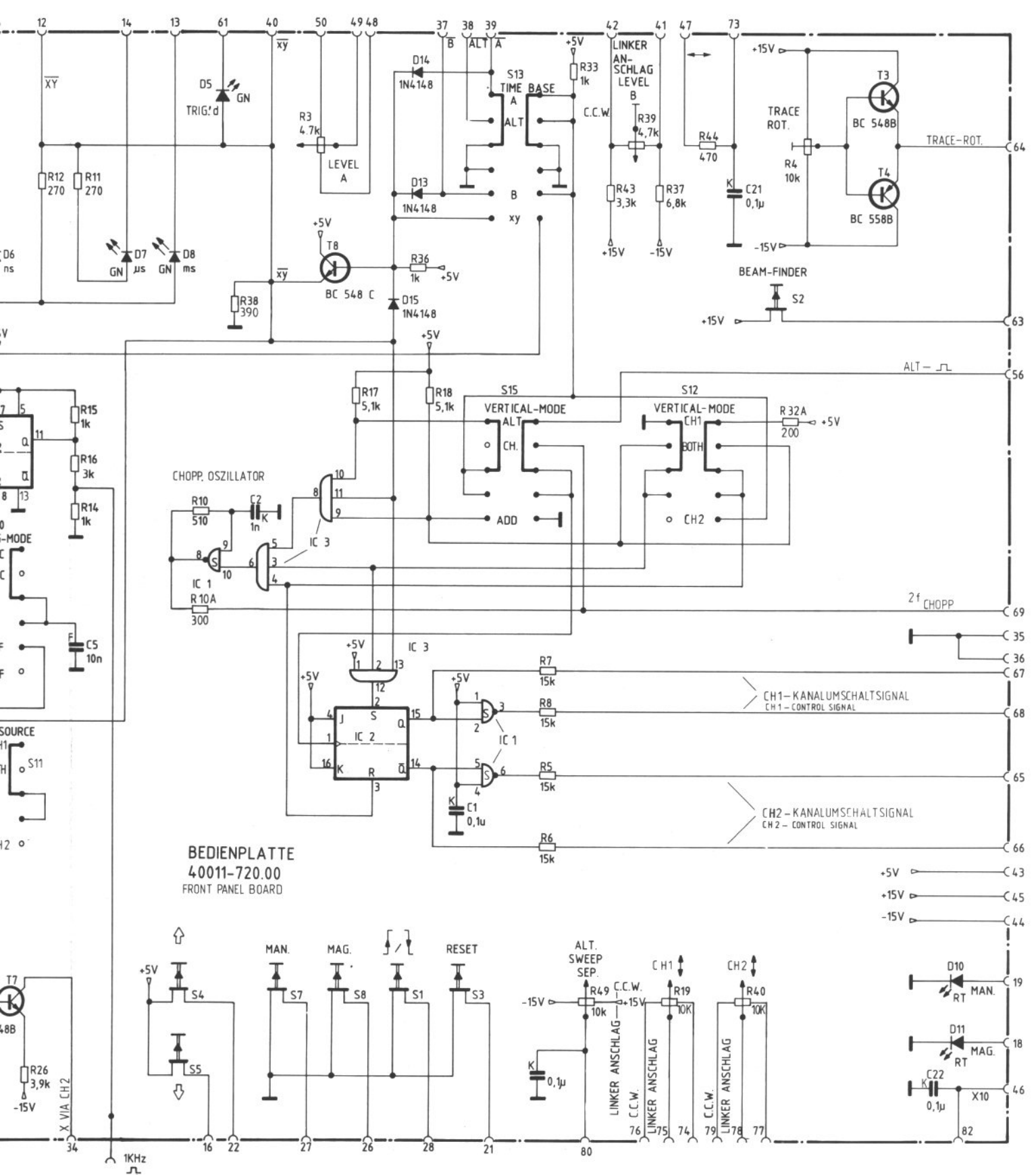
- Z 0207
- MSW 0207
- TANTAL
- FOLIE
- KERAMIK

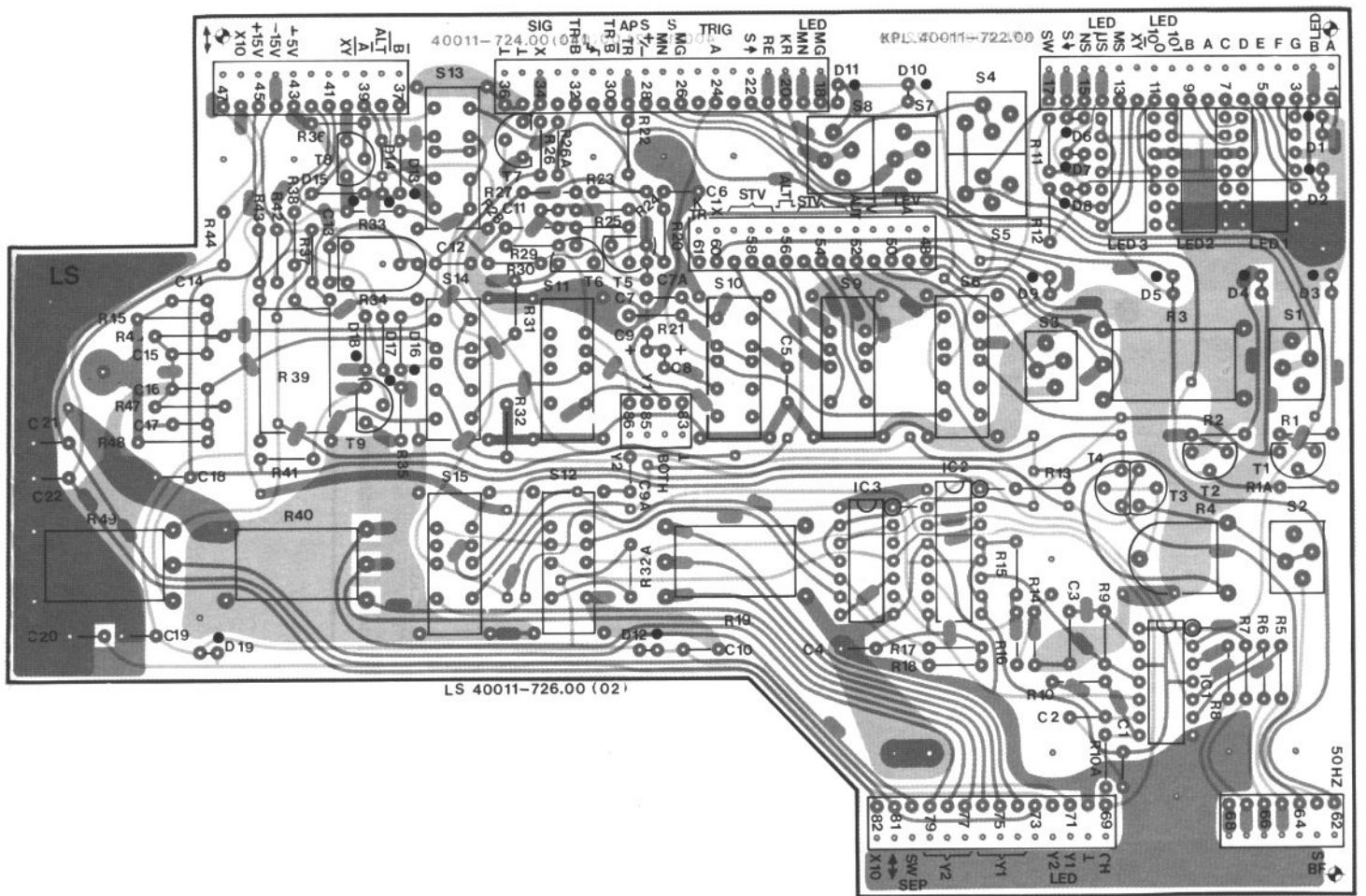


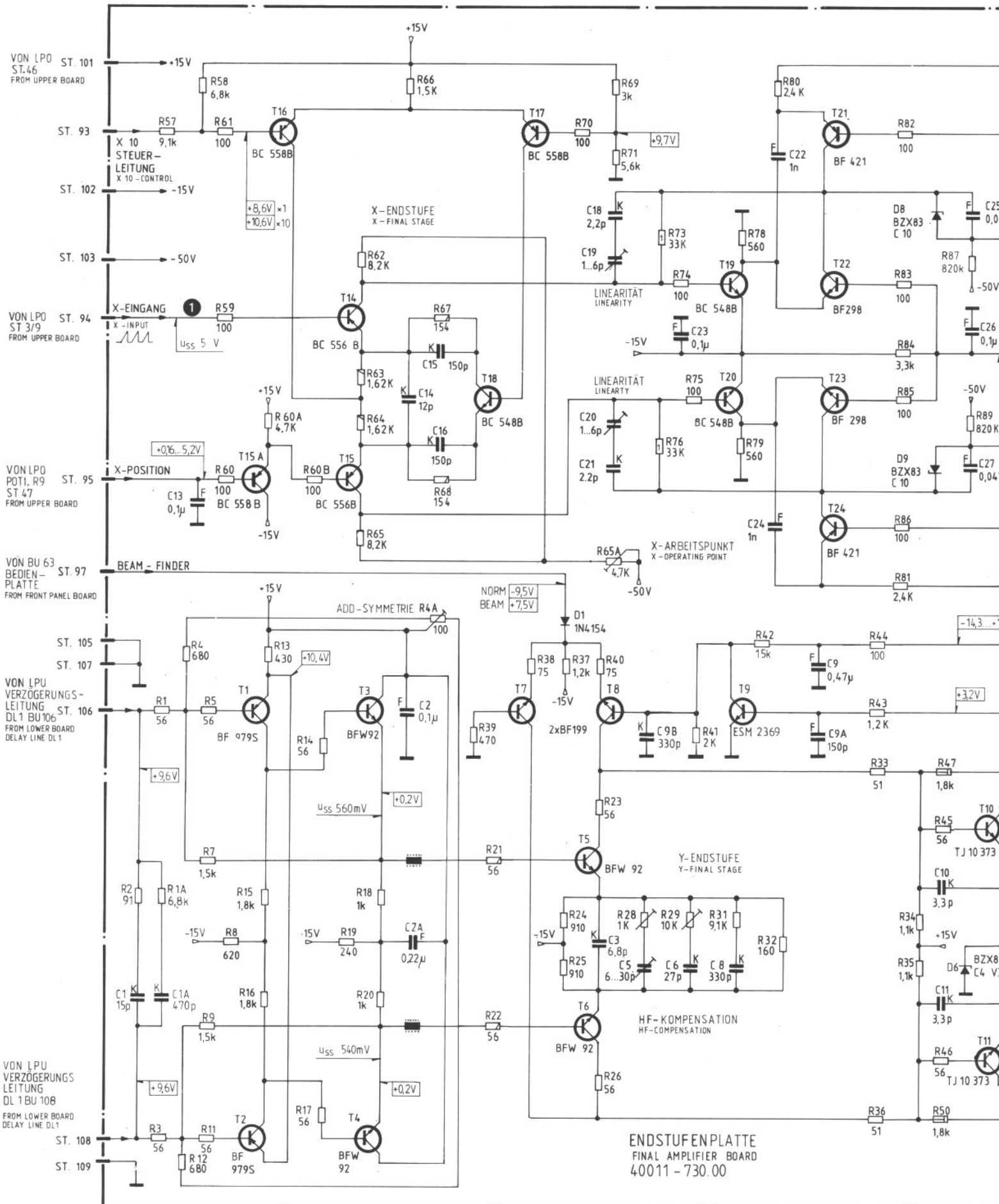
TYPE DER IC: 1 74 LS132
 2 74 LS76
 3 74 LS11

ÄNDERUNGEN VORBEHALTEN
 ALTERATIONS RESERVED






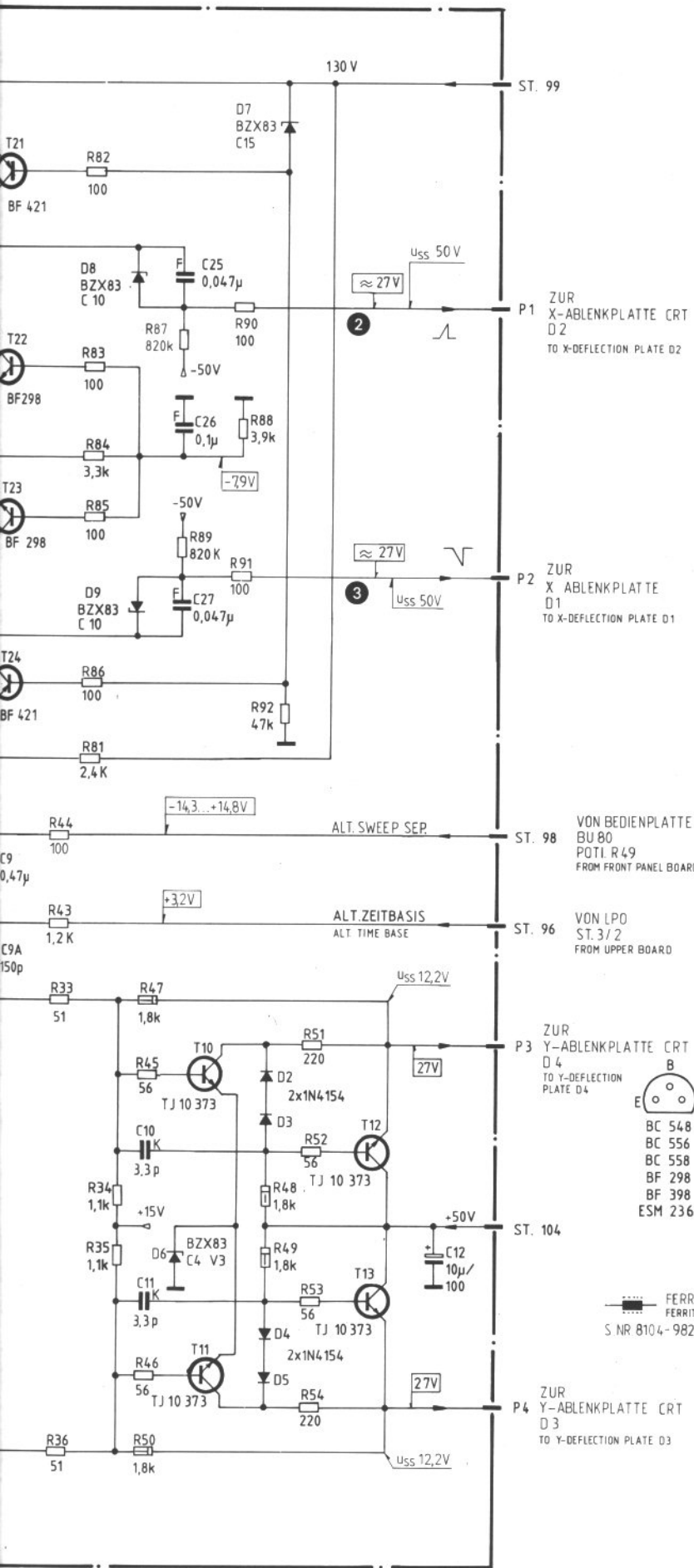




Meßbedingungen „Endstufenplatte“

Sinussignal an CH 1, ca. 1 kHz, 6 cm Auslenkung

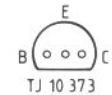
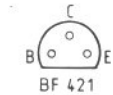
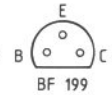
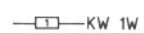
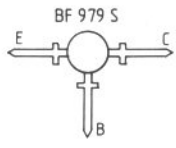
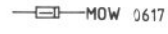
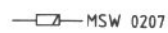
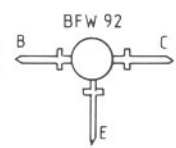
- Triggerflanke : 
 A-Trigger : AUTO
 DC
 CH 1
 INT
 TIME BASE : A 200 μ s/cm



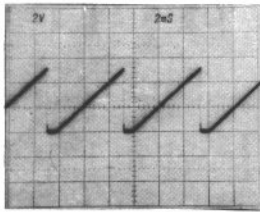
ANSCHLUSSNR. ST.90...109 ZUR LEITERPLATEN BU 90...109
 PIN CONNECTIONS ST.90...109 TO PRINTED CIRCUIT BOARD "LOWER" BU 90...109

MESSBEDINGUNGEN: BETRIEBSART CH1, STRAHLAGE MITTE,
 SIGNALPEGEL \approx 60mm AUSLENKUNG
 MIT 1KHZ SINUS

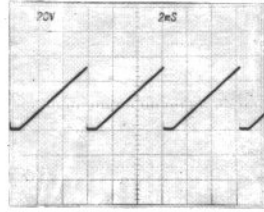
MEASURING CONDITIONS: MODE CH1, BEAM CENTERED
 SIGNAL LEVEL \approx 60mm DEFLECTION
 WITH SINUS 1KHZ



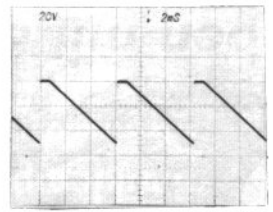
ÄNDERUNGEN VORBEHALTEN!
 ALTERATIONS RESERVED!



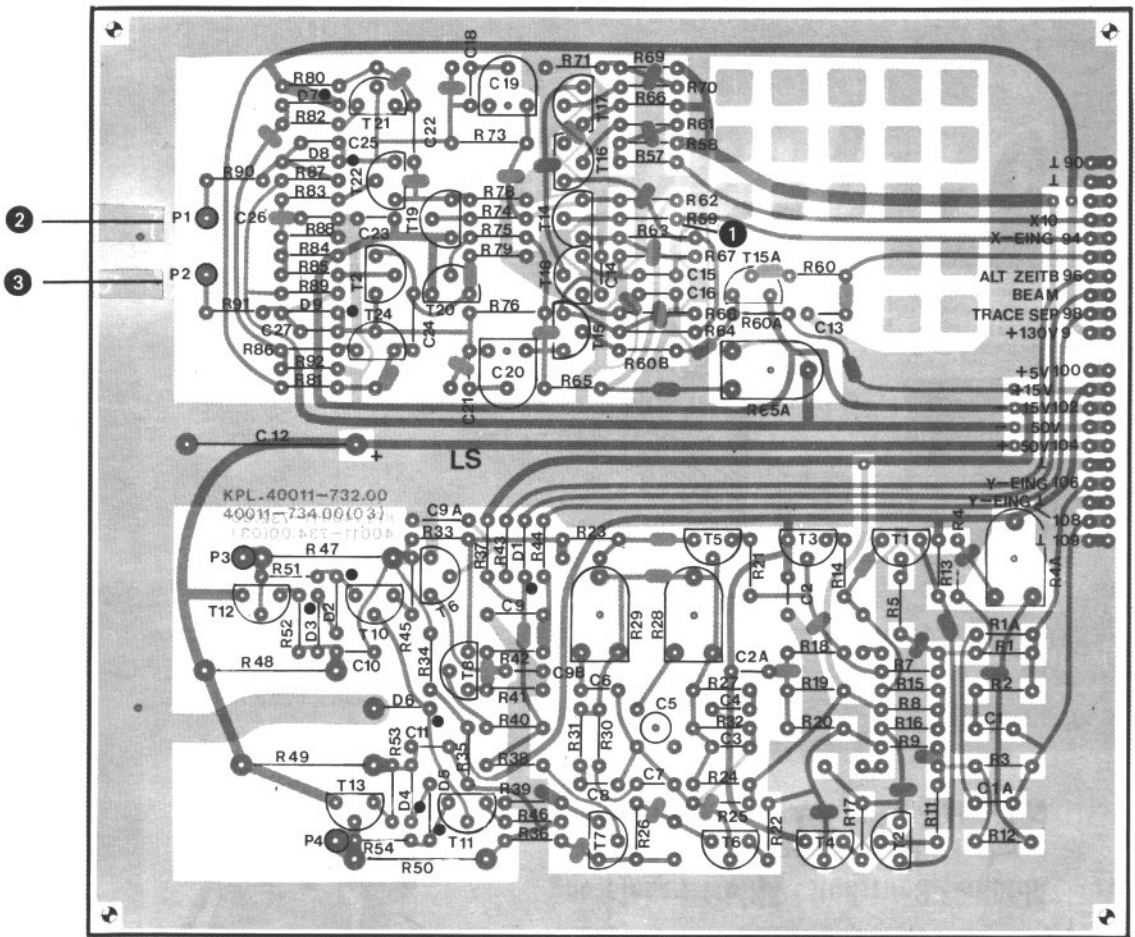
1



2



3



LS 40011-736.00(02)