

MUX5:0	Single Ended Input	Positive Differential Input	Negative Differential Input	Gain
000000	ADC0	N/A	N/A	N/A
000001	ADC1			
000010	ADC2			
000011	ADC3			
000100	ADC4			
000101	ADC5			
000110	ADC6			
000111	ADC7			

Table 26-4. Input Channel Selections (Continued)

MUX5:0	Single Ended Input	Positive Differential Input	Negative Differential Input	Gain
001000 ⁽¹⁾	N/A	ADC0	ADC0	10x
001001 ⁽¹⁾		ADC1	ADC0	10x
001010 ⁽¹⁾		ADC0	ADC0	200x
001011 ⁽¹⁾		ADC1	ADC0	200x
001100 ⁽¹⁾		ADC2	ADC2	10x
001101 ⁽¹⁾		ADC3	ADC2	10x
001110 ⁽¹⁾		ADC2	ADC2	200x
001111 ⁽¹⁾		ADC3	ADC2	200x
010000		ADC0	ADC1	1x
010001		ADC1	ADC1	1x
010010		ADC2	ADC1	1x
010011		ADC3	ADC1	1x
010100		ADC4	ADC1	1x
010101		ADC5	ADC1	1x
010110		ADC6	ADC1	1x
010111		ADC7	ADC1	1x
011000	ADC0	ADC2	1x	
011001	ADC1	ADC2	1x	
011010	N/A	ADC2	ADC2	1x
011011		ADC3	ADC2	1x
011100		ADC4	ADC2	1x
011101		ADC5	ADC2	1x
011110	1.1V (V _{AG})	N/A		
011111	0V (GND)	N/A		
100000	ADC8	N/A	N/A	N/A
100001	ADC9			
100010	ADC10			
100011	ADC11			
100100	ADC12			
100101	ADC13			
100110	ADC14			
100111	ADC15			

Table 26-4. Input Channel Selections (Continued)

MUX5:0	Single Ended Input	Positive Differential Input	Negative Differential Input	Gain
001000 ⁽¹⁾	N/A	ADC0	ADC0	10x
001001 ⁽¹⁾		ADC1	ADC0	10x
001010 ⁽¹⁾		ADC0	ADC0	200x
001011 ⁽¹⁾		ADC1	ADC0	200x
001100 ⁽¹⁾		ADC2	ADC2	10x
001101 ⁽¹⁾		ADC3	ADC2	10x
001110 ⁽¹⁾		ADC2	ADC2	200x
001111 ⁽¹⁾		ADC3	ADC2	200x
010000		ADC0	ADC1	1x
010001		ADC1	ADC1	1x
010010		ADC2	ADC1	1x
010011		ADC3	ADC1	1x
010100		ADC4	ADC1	1x
010101		ADC5	ADC1	1x
010110		ADC6	ADC1	1x
010111		ADC7	ADC1	1x
011000	ADC0	ADC2	1x	
011001	ADC1	ADC2	1x	
011010	N/A	ADC2	ADC2	1x
011011		ADC3	ADC2	1x
011100		ADC4	ADC2	1x
011101		ADC5	ADC2	1x
011110	1.1V (V _{AG})	N/A		
011111	0V (GND)			
100000	ADC8	N/A		
100001	ADC9			
100010	ADC10			
100011	ADC11			
100100	ADC12			
100101	ADC13			
100110	ADC14			
100111	ADC15			

Quelle: Datenblatt Atmega2560