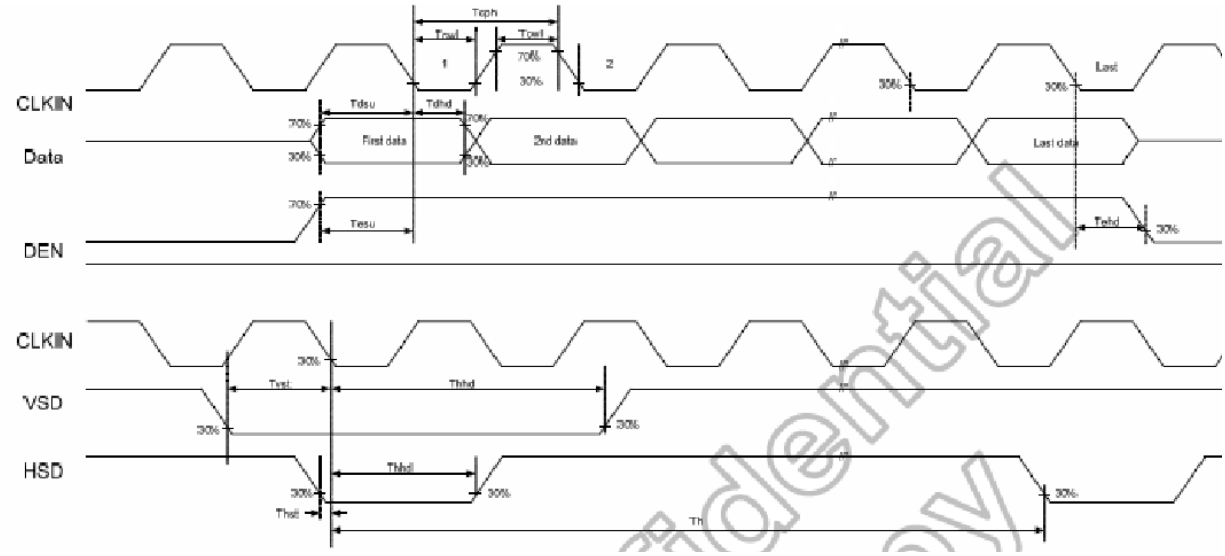
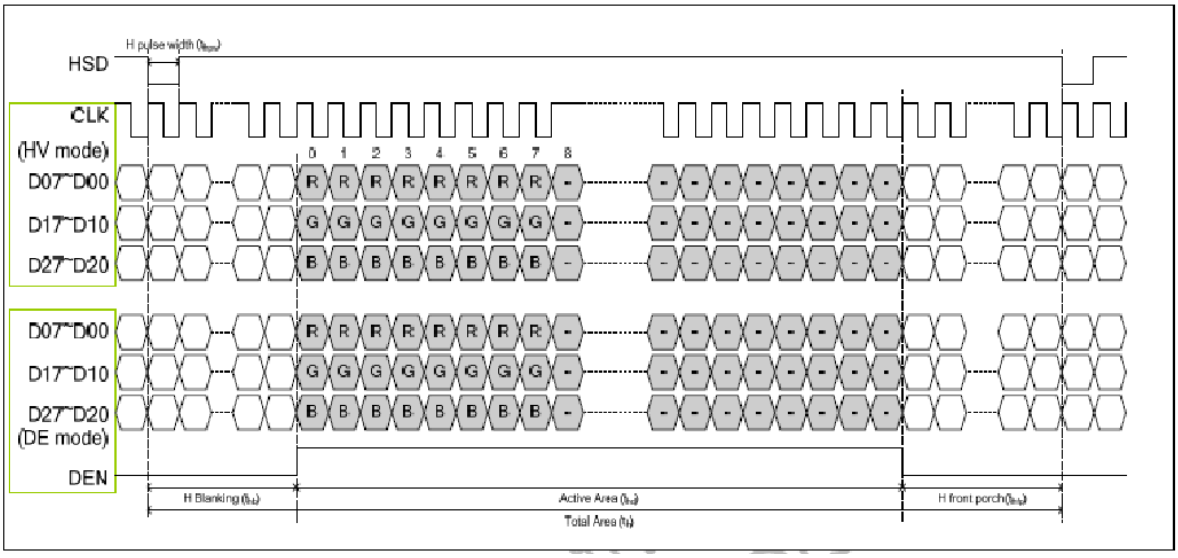
6.2.3 Timing Diagram of interface Signal

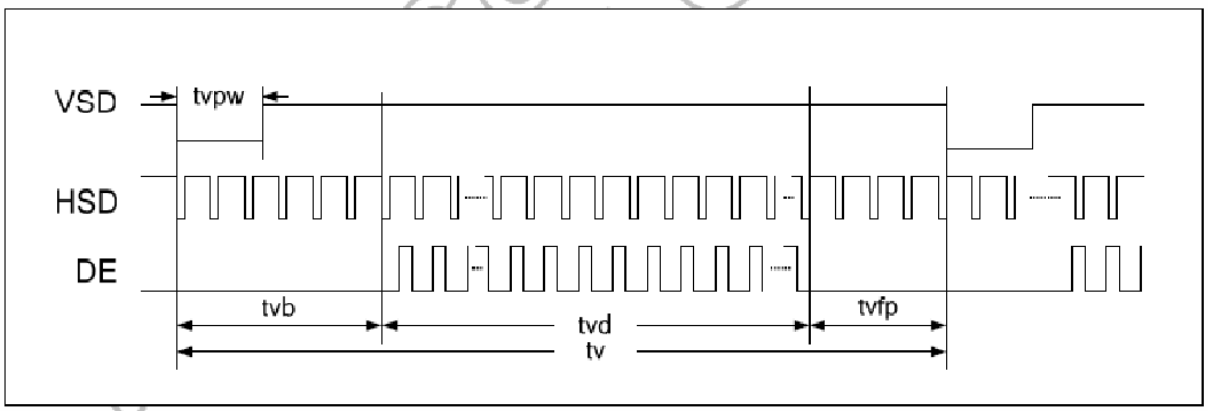


Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
HS setup time	T_{hst}	8	-	-	ns
HS hold time	T_{hhd}	8	-	-	ns
VS setup time	T_{vst}	8	-	-	ns
VS hold time	T_{vhd}	8	-	-	ns
Data setup time	T_{dsu}	8	-	-	ns
Data hold time	T_{dhd}	8	-	-	ns
DE setup time	T_{esu}	8	-	-	ns
DE hold time	T_{ehd}	8	-	-	ns
VDD Power On Slew rate	T_{POR}	-	-	20	ms
RSTB pulse width	T_{Rst}	10	-	-	us
CLKIN cycle time	T_{cph}	20	-	-	ns
CLKIN pulse duty	T_{cwh}	40	50	60	%
Output stable time	T_{sst}	-	-	6	us

- **Horizontal timing**



- **Vertical timing**



- **Horizontal timing**

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
Horizontal Display Area	thd		800		DCLK
DCLK frequency	fclk	-	30	50	MHz
One Horizontal Line	th	889	928	1143	DCLK
HS pulse width	thpw	1	48	255	DCLK
HS Back Porch (Blanking)	thb		88		DCLK
HS Front Porch	thfp	1	40	255	DCLK
DE mode Blanking	th-thd	85	128	512	DCLK

- **Vertical timing**

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
Vertical Display Area	tvd		480		T_H
VS period time	tv	513	525	767	T_H
VS pulse width	tvpw	3	3	255	T_H
VS Back Porch (Blanking)	tvb		32		T_H
VS Front Porch	tvfp	1	13	255	T_H
DE mode Blanking	tv-tvd	4	45	255	T_H