

	Register	Display Parameter	Description
Horizontal	REG_HCYCLE	T_H	Total length of line (visible and non-visible) (in PCLKs)
	REG_HSIZE	T_{HD}	Length of visible part of line (in PCLKs)
	REG_HOFFSET	$T_{HF} + T_{HP} + T_{HB}$	Length of non-visible part of line (in PCLK cycles)
	REG_HSYNC0	T_{HF}	Horizontal Front Porch (in PCLK cycles)
	REG_HSYNC1	$T_{HF} + T_{HP}$	Horizontal Front Porch plus Hsync Pulse width (in PCLK cycles)
Vertical	REG_VCYCLE	T_V	Total number of lines (visible and non-visible) (in lines)
	REG_VSIZE	T_{VD}	Number of visible lines (in lines)
	REG_VOFFSET	$T_{VF} + T_{VP} + T_{VB}$	Number of non-visible lines (in lines)
	REG_VSYNC0	T_{VF}	Vertical Front Porch (in lines)
	REG_VSYNC1	$T_{VF} + T_{VP}$	Vertical Front Porch plus Vsync Pulse width (in lines)

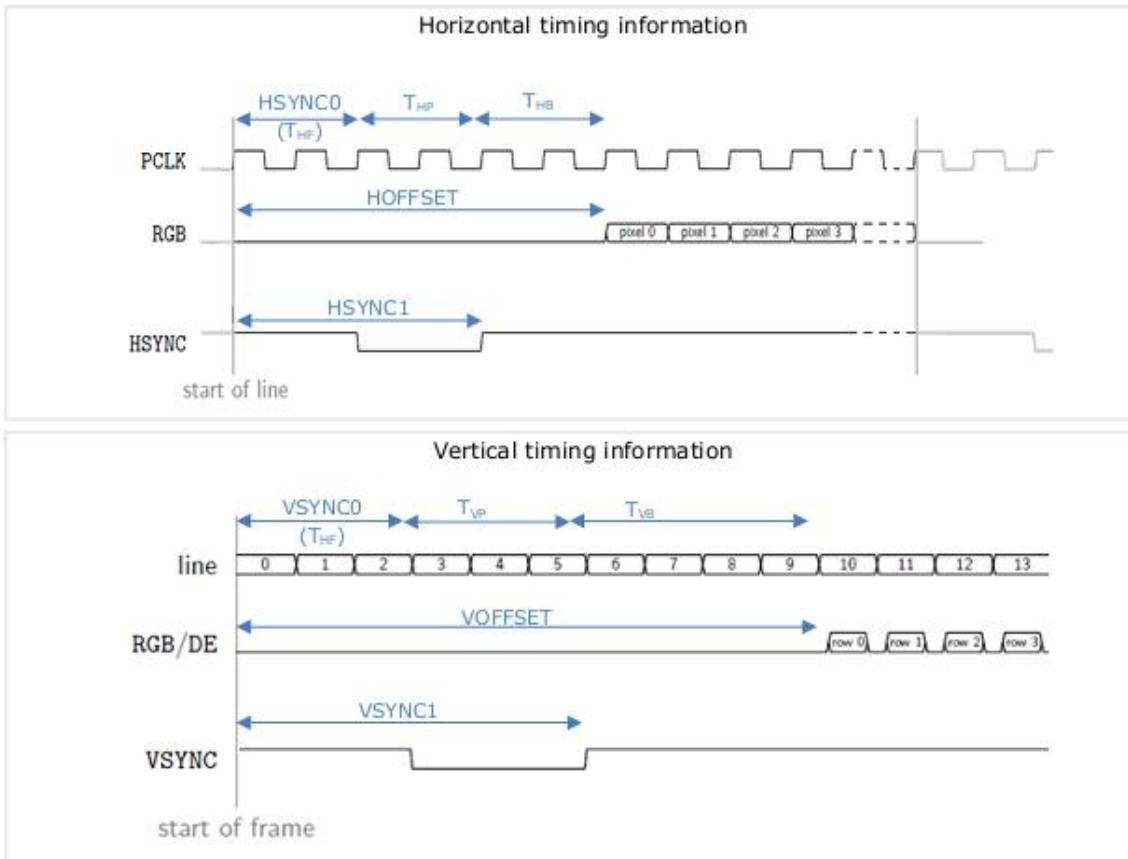


Figure 4-7 RGB timing waveforms

4.5 Miscellaneous Control