

2 Pin configuration

Figure 2. Pin connections (top view)

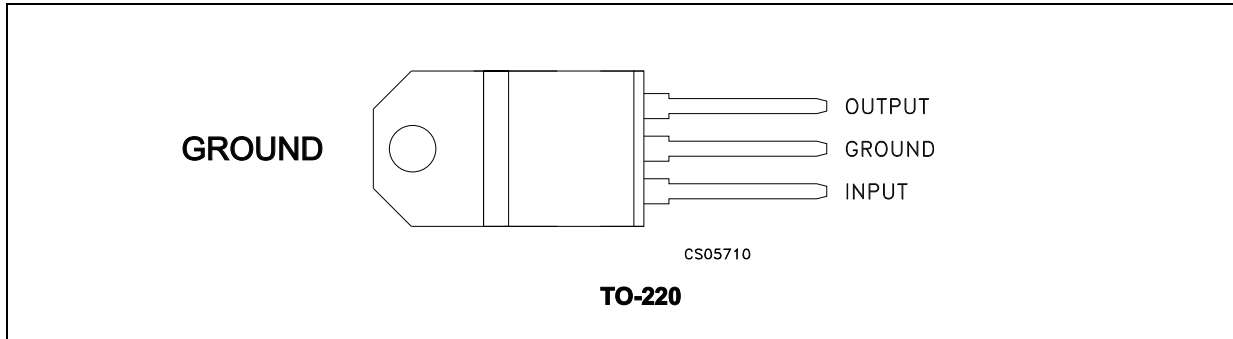


Figure 3. Schematic diagram

