EPROM's

23.1.85 dzr

27512	27256	27128	2764	2732A	2716			2716	2732A	2764	27128	27256	27512
A15	VPP					– (1)	(28) –			VCC			
	A12					– (2)	(27) –			$\overline{PGM}$		A14	
					A7	1 (3)	(26) 24	VCC		—	A13		
					A6	2 (4)	(25) 23	A8					
					A5	3 (5)	(24) 22	A9					
					A4	4 (6)	(23) 21	VPP	A11				
					A3	5 (7)	(22) 20	$\overline{OE}$	$\overline{OE}$/VPP	$\overline{OE}$			$\overline{OE}$/VPP
					A2	6 (8)	(21) 19	A10					
					A1	7 (9)	(20) 18	$\overline{CE}$/PGM	$\overline{CE}$ ($\overline{PGM}$)	$\overline{CE}$		$\overline{CE}$ ($\overline{PGM}$)	
					A0	8 (10)	(19) 17	D7					
					D0	9 (11)	(18) 16	D6					
					D1	10 (12)	(17) 15	D5					
					D2	11 (13)	(16) 14	D4					
					GND	12 (14)	(15) 13	D3					

"VARIABLE" PINS: SIGNALZUSTÄNDE

	TYP	STAND-BY						LESEN						PROGRAMMIEREN						PROGR. VERIFY					
	PIN-NR.	16	32	64	128	256	512	16	32	64	128	256	512	16	32	64	128	256	512	16	32	64	128	256	512
	(1)			+5V			A15			+5V			A15			+21V		+12,5V	A15			+21V		+12,5V	A15
18	(20)	1						Ø												Ø				1	Ø
20	(22)	X						Ø						1	+21V	1			+12,5V	Ø					
21	(23)	+5V	A11					+5V	A11					+25V	A11					+25V	A11				
24	(26)	+5V		—	A13			+5V		—	A13			+5V		—	A13			+5V		—	A13		
	(27)			X		A14				1		A14						A14				1		A14	
	(28)			+5V						+5V						+5V						+5V			
(mA)	Icc (TYP)	10	–	–	–	–	–	57	–	70	–	–	–	–	85		–	–	–						
	Ii (MAX)	25	35	40	25	25	25	100	125	100	100	100	100	100	125	100	100	100	150						
	IPP (MAX)	5		5	5	5		5		5	5	5		30	35	30	30	30	30						