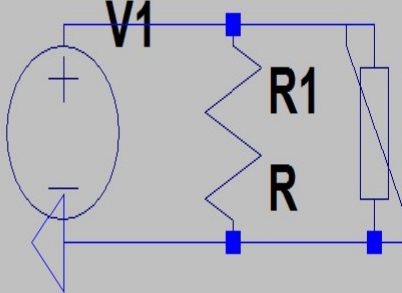


LTspice XVII - [Draft2]

File Edit Hierarchy View Simulate Tools Window Help

V660LA100B.lib



V1

R1

R

varistor
U1
V660LA100B

```
PULSE(0 4000 1m 1u 1u 20m 2m 1k)  
.tran 0 20m 0 1k
```

Draft2 Draft2