

AA51880

PRELIMINARY

SERVO MOTOR CONTROLLER

DESCRIPTION

The AA51880 is a BJT integrated circuit using in servo motor control applications. The built-in voltage regulator provides the AA51880 with extremely stable output voltage. It also incorporates a linear one-shot and pulse-width demodulator for improved positional accuracy.

FEATURES

- Built-in voltage regulator.
- Larger driving capability.
- Internal output NPN power transistor.
- Adjustable dead band range control.
- Incorporates a protection circuit for "H-Bridge" driving configuration.
- Small outline package.

■ APPLICATIONS

- · Servo motor control.
- Radio controlled car, boat, and airplane.
- IA products (such as air-condition).
- Remote positioning.

BLOCK DIAGRAM



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PIN DESCRIPTION



SSOP16L

SSOP16L(FD)

PIN NO.			FUNCTION		
SSOP16	DIP14				
1	14	RT	Timing Resistor		
2	1	EXP1	External PNP Transistor 1 output		
3	3	OUT1	Output Driver 1		
5	6	OUT2	Output Driver 2		
6	8	EXP2	External PNP Transistor 2 output		
7	7	CST	Stretcher capacitor		
8	12	NC	No Connection		
9	9	VREG	Regulated Voltage output		
10	10	VCC	Power supply pin		
11	5	RDB	Error pulse output		
4,12,13	4	GND	Ground pin		

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PIN NO.			FUNCTION		
SSOP16 DIP14					
14	2	VIN	Input signal		
15	11	POT	Servo position voltage		
16	13	СТ	Timing Capacitor		

ABSOLUTE MAXIMUM RATINGS

Ta=25℃

SYMBOL	PARAMETER	RATINGS	UNIT
Vcc	Supply voltage	4.8~7	V
Pd	Power dissipation	300	mW
Tstg	Storage temperature range	-25 ~ +125	°C

ELECTRICAL CHARACTERISTICS

				Ta=2	25°C, Vc	c=4.8V
SYMBOL		TEST CONDITIONS	LIMITS			
			MIN	TYP	MAX	
	Operating current	When output is OFF	-	5	8	mA
		When output is ON	-	25	28	mA
VOL	Output voltage "L"	Load resistor 20Ω	-	0.1	0.2	V
VOH	Output voltage "H"	Load resistor 240Ω	3.5	-	-	V
IPNP	External PNP transistor drive current	Load resistor 240Ω	30	-	-	mA
VREG	Regulator output voltage	Load capacitor 0.1uF	2.3	2.45	2.6	V
IREG	Regulator output current	Load resistor 500Ω	4	-	-	mA

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■ FUNCTIONAL DESCRIPTION

(1) Voltage Regulator

This circuit is composed of a band gap circuit, and outputs a 2.45-volt temperature compensated reference voltage. This reference voltage is stabilized when the supply voltage is variable.

(2) Saw tooth wave & One-Shot Generator

Connect a capacitor to CT pin will generate a triangular wave by constant current charging. A typical value is 0.1uF. The constant current is determined by a resistor connected to RT pin. A typical capacitor of approximately 0.1uF should be connected in parallel with the resistor to increase stability.

(3) Servo Position & Over-Time logic

This circuitry consists of a pulse width comparator, which compares the servo position detection signal (POT pin) from the one-shot timer (CT pin) whose period depends on the resistance of a potentiometer connected to the servo's drive shaft. This feedback is what provides the stability for the control circuitry.

(4) Pulse Stretcher

The difference between the servo control signal and the feedback signal is the error signal. This error signal is used to toggle the direction the current flows through the servo. The function of this pulse stretcher is to "stretch" the small error signal long enough and increases the duty cycle to the motor for it can maintain sufficient holding force. The circuit also implements a "dead band" function that prevents servo jitter and hunting. This is a range over which differences between the input and reference signals will not cause servo operation. When the signal differences exceed this "dead band" range, drive to the motor occurs. servo's drive shaft. The dead band will change according to the value of resistor connected to the RDB pin.

(5) Directional Logic Control

The comparator circuit compares pulses from the servo control input (VIN pin) and the voltage controlled pulse generator and provides either a positive or a negative output depending on whether the signal pulse width is larger or smaller than the position generator's output pulse (POT pin). Application of this error voltage to the motor driver circuit causes the motor to turn in a direction that will minimize the error until both pulse widths are the same.

(6) PWM Control

This signal generated by directional logic control block is used to control a flip-flop that toggles the direction the current flows through the motor. The outputs of the flip-flop drive an H-Bridge output driver block that handles the high current going through the motor. This blocks output will be turned on or off with each input pulse based on the status of the directional logic. The

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PWM drive techniques provide the benefits of reduced power dissipation, improved servo motor performance and positively affect system efficiency.

(7) Output driver

The output driver controls the servo current direction. For light load application, the servo can be connected with OUT1 and OUT2 pin only. For heavy load application, the EXP1 and EXP2 pin will be connected to the base of the external PNP transistor. It drives in an H-bridge configuration (The sink NPN drivers are built inside the chip).

TYPICAL APPLICATION EXAMPLE

A. Mini-Servo Application (No external drivers needed)



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B. Half H-Bridge application using two external PNP drivers



C. Full H-Bridge application using two PNPs and two NPNs



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D. Full H-Bridge application using two PMOSs and two NMOSs



Note: Pin no. is for AA51880C, please refer to "PIN DESCRIPTION" for other package type.

ORDERING INFORMATION

ORDER NO.	PACKAGE	PACKING	Q'TY	MARK CHART
AA51880C	SSOP 16L	TAPE & REEL	2,500ea	AA51880
AA51880F	SSOP 16L(FD)	TAPE & REEL	2,500ea	AA51880
AA51880P	DIP 14L	TUBE	25ea	AA51880

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PACKAGE DIMENSIONS





SYMPOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES			
SIMBOLS	MIN	NOM	MAX	MIN	NOM	MAX	
А	1.35	1.60	1.75	0.053	0.064	0.069	
A1	0.10		0.25	0.004		0.010	
A2		1.45			0.057		
b	0.20	0.25	0.30	0.008	0.010	0.012	
С	0.19		0.25	0.007		0.010	
D	4.80		5.00	0.189		0.197	
Е	5.80		6.20	0.228		0.244	
E1	3.80		4.00	0.150		0.157	
e		0.64			0.025		
L	0.40		1.27	0.016		0.050	
У			0.10			0.004	
theta	0°		8°	0°		8°	

NOTE

1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH PROTRUSIONS OR GATE BURRS

2. DIMENSION L IS MEASURED IN GAGE PLANE

3. TOLERANCE +/- 0.1 mm UNLESS OTHERWISE SPECIFIED

4. CONTROLLING DIMENSION IS MILLIMETER. CONVERTED INCH DIMENSIONS ARE

NOT NECESSARILY EXACT.

5. FOLLOWED FROM JEDEC MO-137

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SVMPOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES			
31 MIDOLS	MIN	NOM	MAX	MIN	NOM	MAX	
А	1.35	1.50	1.65	0.053	0.059	0.065	
Al	0.00		0.10	0.000		0.004	
A2		1.45			0.057		
b	0.20	0.25	0.30	0.008	0.010	0.012	
С	0.19		0.25	0.007		0.010	
D	4.80		5.00	0.189		0.197	
Е	5.80		6.20	0.228		0.244	
El	3.80		4.00	0.150		0.157	
e		0.64			0.025		
L	0.40		1.27	0.016		0.050	
У			0.10			0.004	
theta	0°		8°	0°		8°	

NOTE

1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH PROTRUSIONS OR GATE BURRS

2. DIMENSION L IS MEASURED IN GAGE PLANE

3. TOLERANCE 0.1 mm UNLESS OTHERWISE SPECIFIED

4. CONTROLLING DIMENSION IS MILLIMETER. CONVERTED INCH DIMENSIONS ARE

NOT NECESSARILY EXACT.

5. DIE PAD EXPOSURE SIZE IS ACCORDING TO LEAD FRAME DESIGN.

6. FOLLOWED FROM JEDEC MO-137

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14-pin DIP



SYMBOLS	MIN.	NOR.	MAX.	
А			0.210	
A1	0.015			
A2	0.125	0.130	0.135	
D	0.735	0.750	0.775	
Е	0.300 BSC.			
E1	0.245	0.250	0.255	
L	0.115	0.130	0.150	
e _B	0.335	0.355	0.375	
θ $$	0	7	15	

UNIT : INCH

NOTES:

1. JEDEC OUTLINE : MS-001 AA

0.060 typ.

- 2. "D", "E1" DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.010 INCH.
- 3. eB IS MEASURED AT THE LEAD TIPS WITH THE LEADS UNCONSTRAINED.
- 4. POINTED OR ROUNDED LEAD TIPS ARE PREFERRED TO EASE INSERTION.
- 5. DISTANCE BETWEEN LEADS INCLUDING DAM BAR PROTRUSIONS TO BE 0.005 INCH MININUM. DATUM PLANE "H" COINCIDENT WITH THE BOTTOM OF LEAD WHERE LEAD EXITS BODY.

NOTES ON USE

- The application circuit examples explain typical applications of the products, and do not guarantee the success of any specific mass-production design.
- Application circuit diagrams and circuit constants contained herein are shown as examples of standard use and operation. Please pay careful attention to the peripheral conditions when designing circuits and deciding upon circuit constants in the set.
- Take account of common impedance when designing the earth line on a printed wiring board.

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