



+5V_1	+5V_2	S	Active
LOW	LOW	LOW	None
LOW	HIGH	HIGH	Port 2
HIGH	LOW	LOW	Port 1
HIGH	HIGH	LOW	Port 1

	Variant:		ECL:
	Section:		
	Document:	untitled	
	Project:	>PROJECT	
	Verified:	9/7/2022 4:29 PM	>USR

