

(1) Crystal/ceramic oscillation (2) External clock input (3) High-speed Ring-OSC

High-speed Ring-OSC (typ 8MHz)

Processor Clock Control (PCC)

Low-speed Ring-OSC (typ 240kHz)

POC/LVI Power-on-clear/Low-voltage detection

Controls of Reset

Processor Clock Control (PCC)

Low-speed Ring-OSC (typ 240kHz)

POC/LVI Power-on-clear/Low-voltage detection

Controls of Reset

10-bit A/D converter (4 channels)

TM80 - 8-Bit Timer 80

WDT - Watchdog Timer

Package information

Standby Function

External Reset Output Function

CALLT Table Area and Vector Table Area

Address	Program area	Protect byte	Option byte
008xh	CALLT31	CALLT30	CALLT29
007xh	CALLT23	CALLT22	CALLT21
006xh	CALLT15	CALLT14	CALLT13
005xh	CALLT7	CALLT6	CALLT5
004xh	CALLT0	CALLT1	CALLT2
003xh	CALLT0	CALLT1	CALLT2
002xh	CALLT0	CALLT1	CALLT2
001xh	CALLT0	CALLT1	CALLT2
000xh	CALLT0	CALLT1	CALLT2

Package information

Standby Function

External Reset Output Function

Processor Registers

Memory Space

Interrupt Control

Interrupt Control

UART6 - Asynchronous serial I/F(ASI)

Reception

Transmission

TM00 - 16-bit timer/event counter 00

TM01 - 16-bit timer/event counter 01

TM02 - 16-bit timer/event counter 02

TM03 - 16-bit timer/event counter 03

TM04 - 16-bit timer/event counter 04

TM05 - 16-bit timer/event counter 05

TM06 - 16-bit timer/event counter 06

TM07 - 16-bit timer/event counter 07

TM08 - 16-bit timer/event counter 08

TM09 - 16-bit timer/event counter 09

TM10 - 16-bit timer/event counter 10

TM11 - 8-bit timer H1

Interrupt Control

UART6 - Asynchronous serial I/F(ASI)

Reception

Transmission

TM11 - 8-bit timer H1