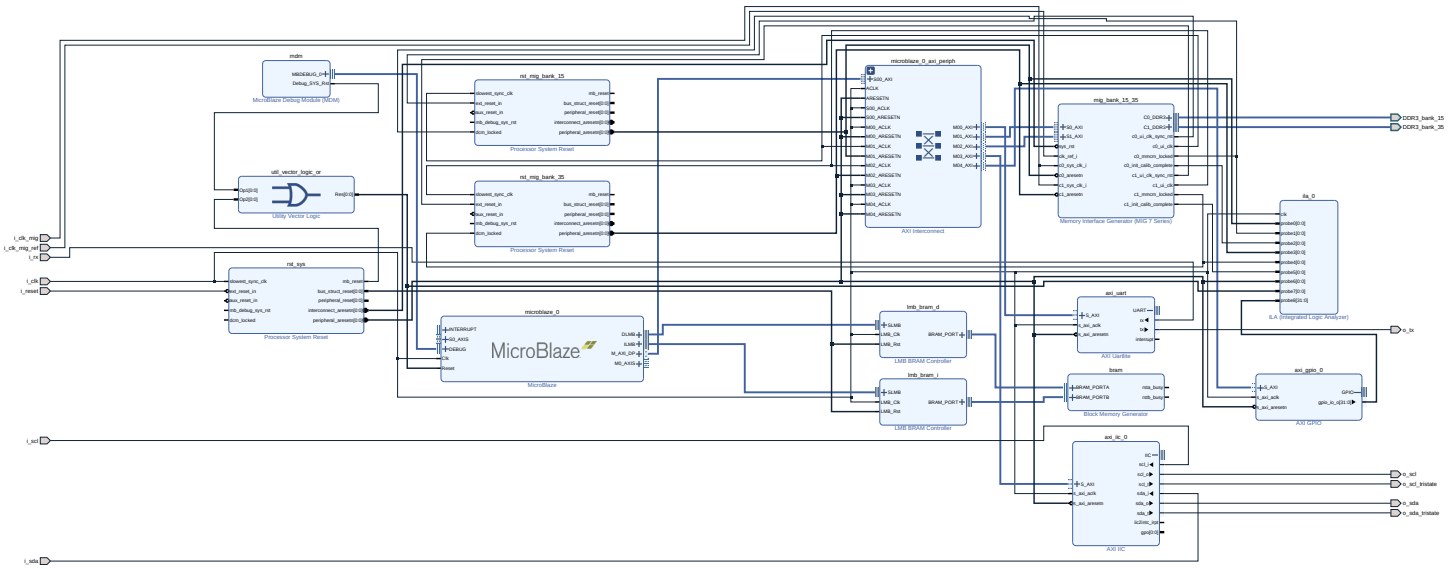
mdm
MicroBlaze Debug Module (MDM)
rst_mig_bank_15
Processor System Reset
rst_mig_bank_35
Processor System Reset
util_vector_logic_or
Utility Vector Logic
rst_sys
Processor System Reset
microblaze_0
MicroBlaze
microblaze_0_axi_periph
AXI Interconnect
mig_bank_15_35
Memory Interface Generator (MIG 7 Series)
lmb_bram_d
LMB BRAM Controller
lmb_bram_i
LMB BRAM Controller
axi_uart
AXI Uartlite
bram
Block Memory Generator
axi_gpio_0
AXI GPIO
axi_iic_0
AXI IIC
ila_0
ILA (Integrated Logic Analyzer)
i_clk_mig
i_clk_mig_ref
i_rst
i_clk
i_reset
i_sci
i_sda
o_tx
o_scl
o_scl_tristate
o_sda
o_sda_tristate
DDR3_bank_15
DDR3_bank_35