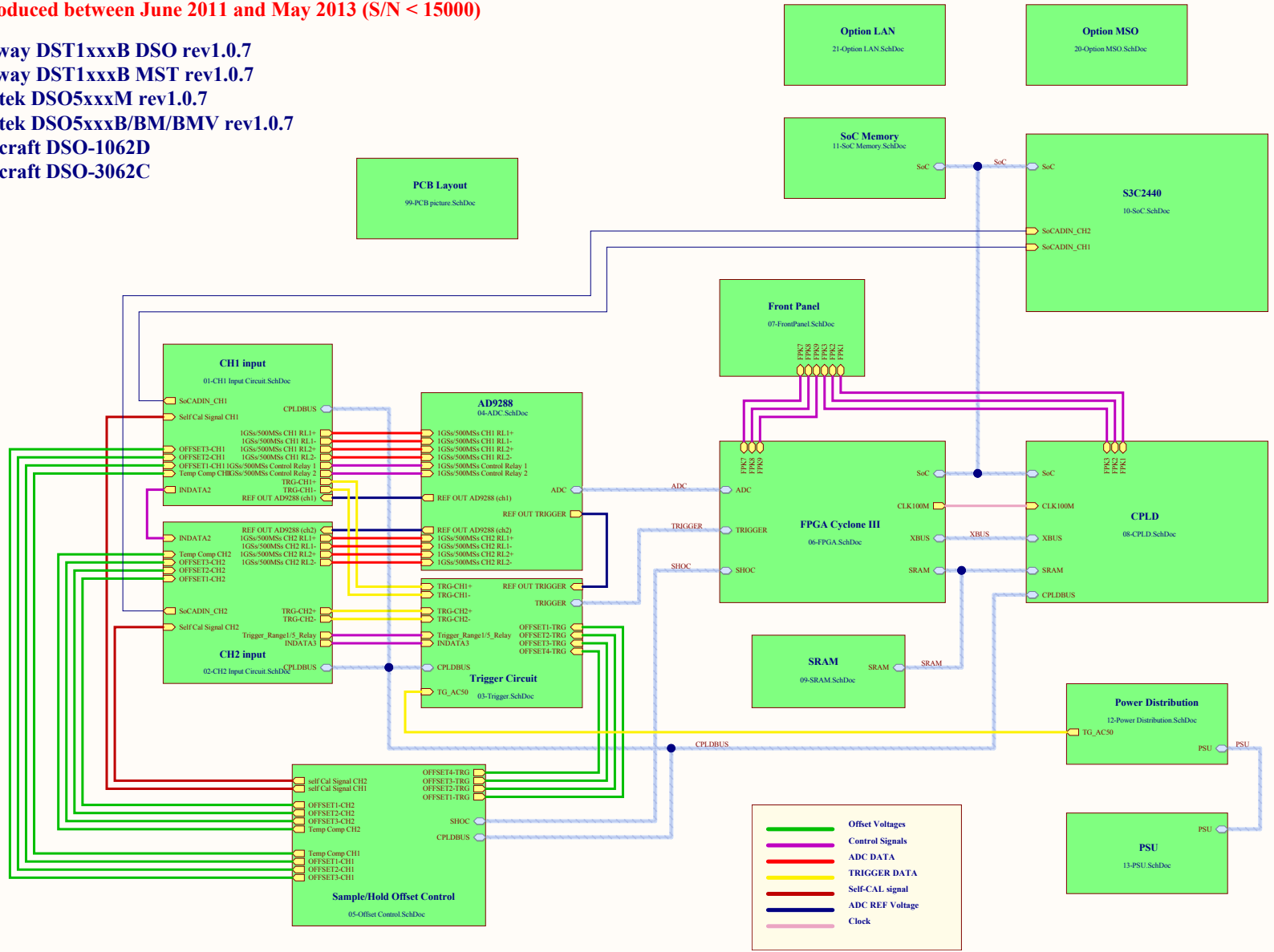
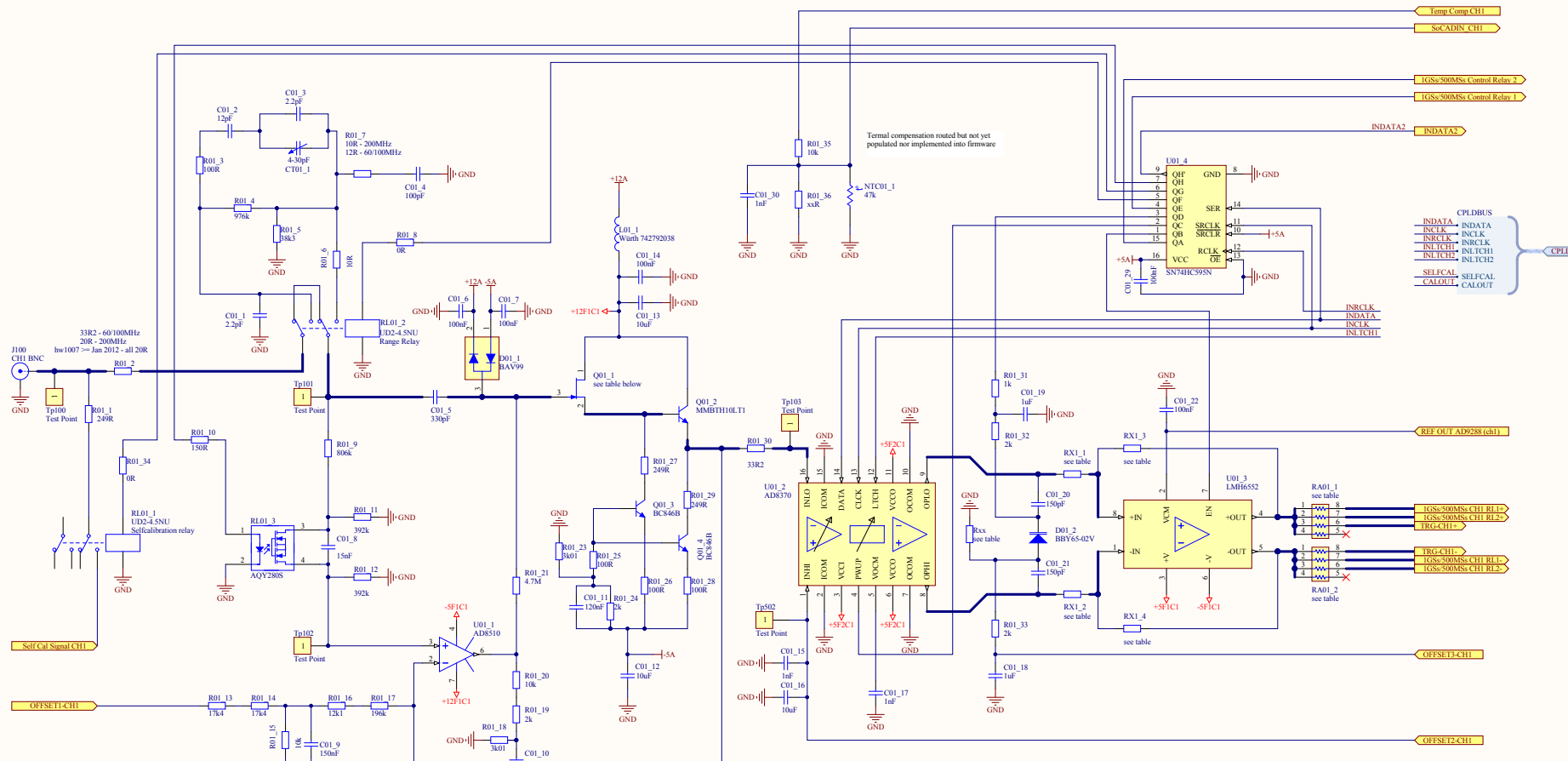


DSOs produced between June 2011 and May 2013 (S/N < 15000)

Tekway DST1xxxB DSO rev1.0.7
Tekway DST1xxxB MST rev1.0.7
Hantek DSO5xxxM rev1.0.7
Hantek DSO5xxxB/BM/BMV rev1.0.7
Volcraft DSO-1062D
Volcraft DSO-3062C



CH1 Input Circuit



Q01_1
 Hantek DSO5062B hw1007 - Jan 2012
 Voltcraft DSO3062C, DSO1062D

MMBF4393, SNT4393 - CCPBE; CCGBF (CC) marked
 PMBF4392 - W6k, p6k marked
 PMBF4393 - W6G, p6G marked

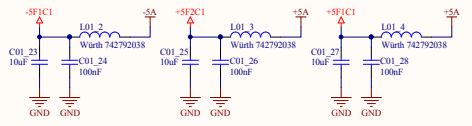
Tekway DST1062B hw1007 Sep 2011
 BRS5L11G - 60MHz

Tekway DST1202B hw0100/200MHz
 JSS1201 - PIKJF (PI) marked

Rigol E hw58
 PMBF4308 - 48 marked
 MMBF3991T1 - 6U marked

Rigol CA
 2SK508 - K51 marked

ATTEN CML
 2SK508 - K52 marked



RX1_1 - RX1_2 - RX1_3 - RX1_4
 Hantek / Tekway
 200MHz - 200R 0.1%
 100MHz - 301R 0.1%
 60MHz - 365R 0.1%

original circuit have no Rxx !

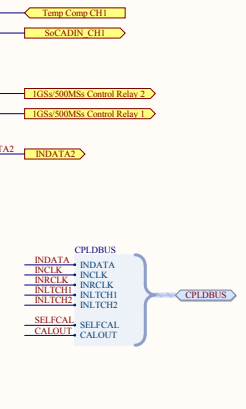
Tinhead@evblog.com version
 200MHz - 270R 0.1%
 add 2k2 as Rxx

AB hw1007 - Jan 2012
 200R 0.1%

RA01_1 - RA01_2
 Hantek / Tekway
 200MHz - 33R 1%
 100MHz - 301R 0.1%
 60MHz - 47R 1%

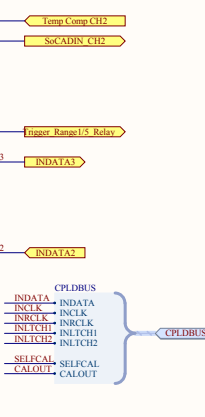
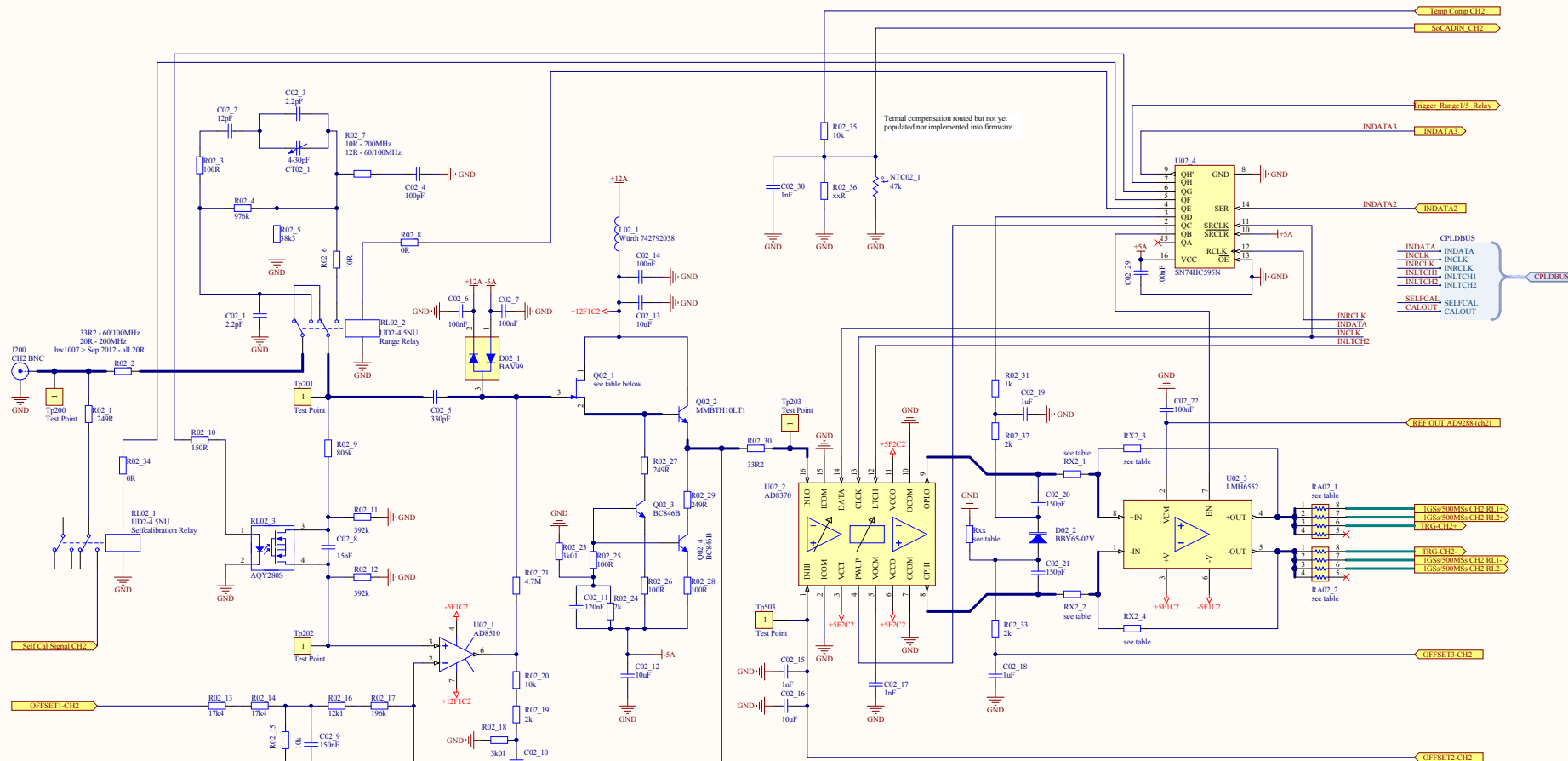
Tinhead@evblog.com version
 200MHz - 33R 1%

AB hw1007 - Jan 2012
 22R 1%

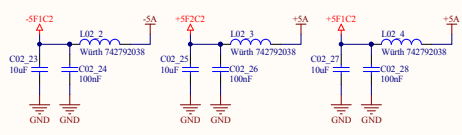


Termal compensation routed but not yet populated nor implemented into firmware

CH2 Input Circuit



Q02_1
 Hantek DSO58G2B hw1007 -- Jun 2012
 Voltcraft DSO3862C, DSO1062D
 MMBF4393 - SST4393 - CCPBE; CCGBF (CC) marked
 PMBF4392 - W6K, p6K marked
 PMBF4393 - W6G, p6G marked
 Tekway DST1062B hw1007 Sep 2011
 BSKS81.TIG - 60MHz
 Tekway DST1202B hw100 200MHz
 JSS1201 - PIKIF (PI) marked
 Rigol E hw58
 PMBF4398 - 48 marked
 MMBF4399.T1 - 61 marked
 Rigol CA
 2SK508 - K51 marked
 ATEN CML
 2SK508 - K52 marked



RX2_1 - RX2_2 - RX2_3 - RX2_4
 Hantek / Tekway
 200MHz - 280R 0.1%
 100MHz - 201R 0.1%
 60MHz - 365R 0.1%
 original circuit have no Rxx !
 Tinhead@evblog.com version
 200MHz - 270R 0.1%
 add 2k2 as Rxx
 All hw1007 -- Jun 2012
 280R 0.1%

RA02_1 - RA02_2
 Hantek / Tekway
 200MHz - 238R 1%
 100MHz - 398 1%
 60MHz - 47R 1%
 Tinhead@evblog.com version
 200MHz - 33R 1%
 All hw1007 -- Jun 2012
 22R 1%

A
B
C
D

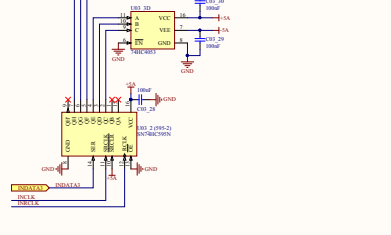
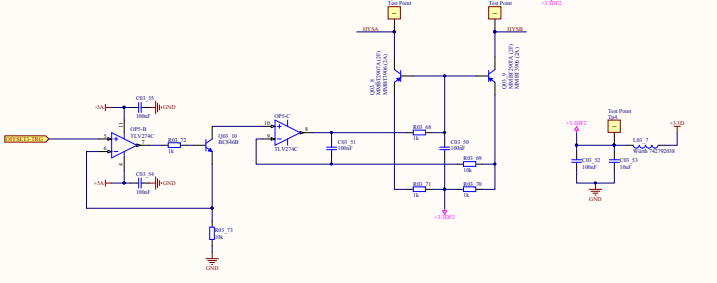
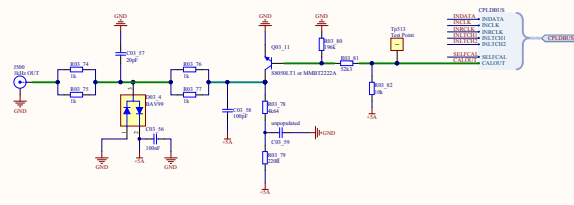
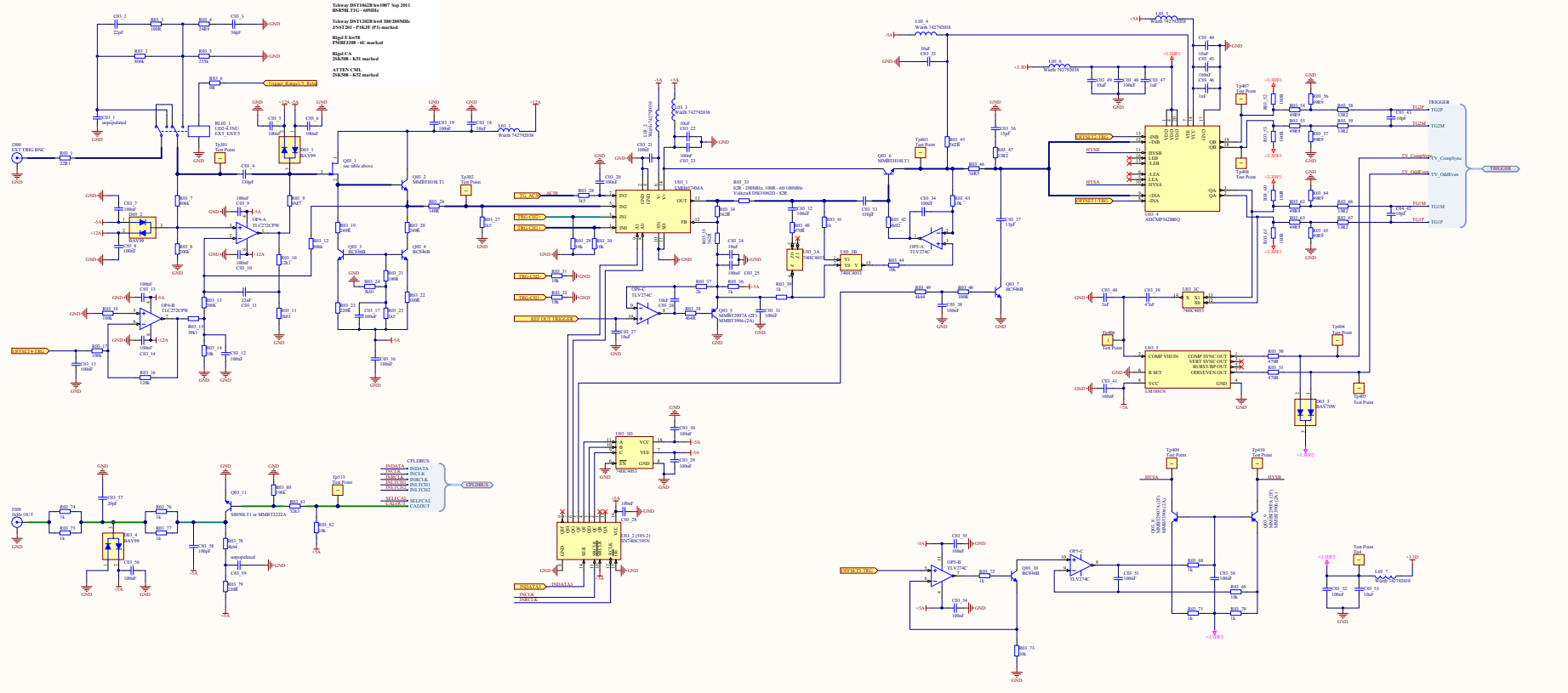
Trigger Circuit

Q0_1
 Board: 00050242 Rev 007 - Jul 2012
 VISA: 00 050242_0000620
 M90R203, S10240 - CCFBI; CCGB; CC2
 marked

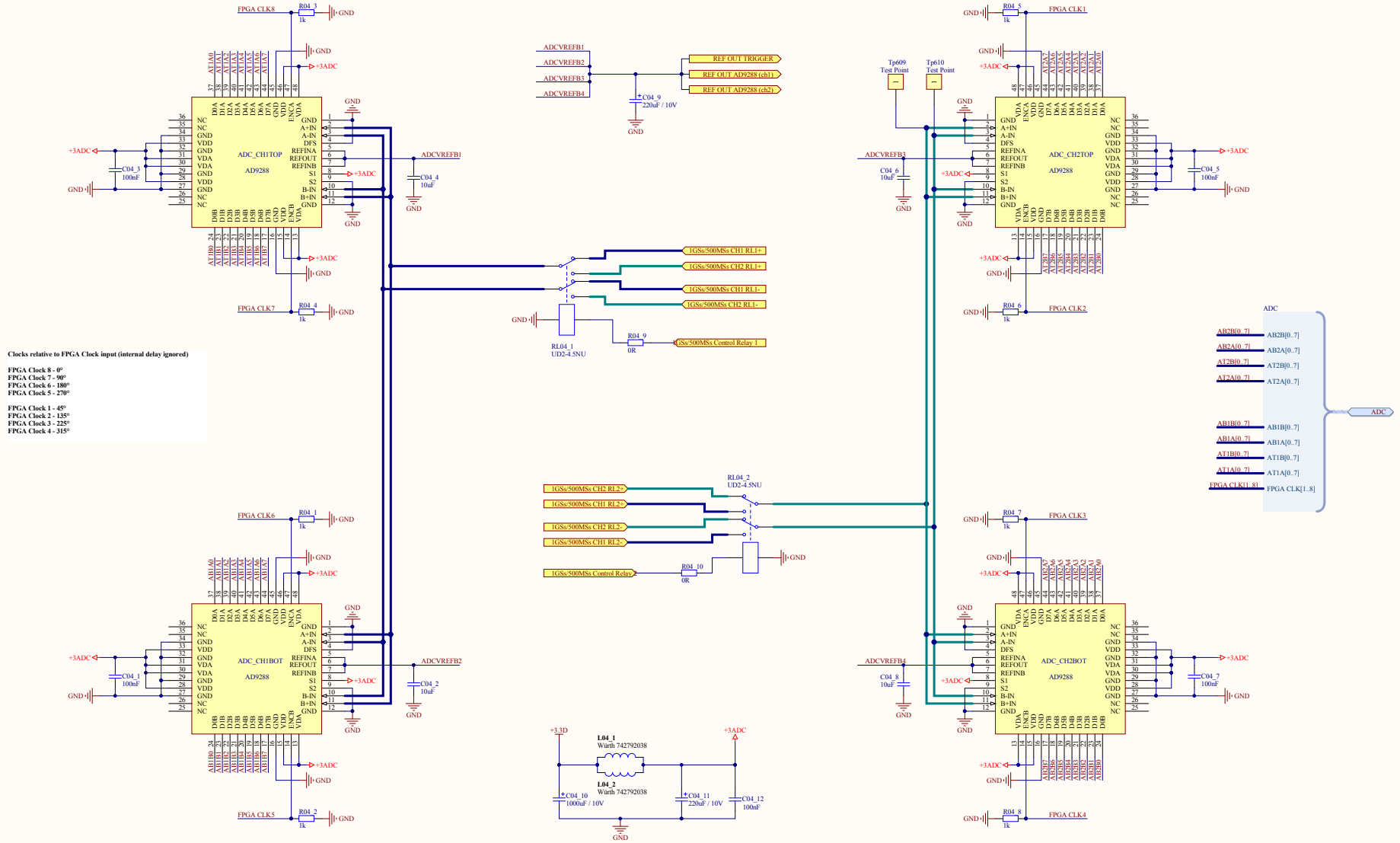
Tektron DUT1002 Rev 007 Sep 2011
 D8368L TIC - 606014

Tektron DUT1002 Rev 008 20090515
 EXT. EXT.5
 EXT.2 - P1&P2 (PI) marked

Rigid CA
 P1&P2 - 4C marked
 ATTN: CH1
 2&3&4 - 1&2 marked



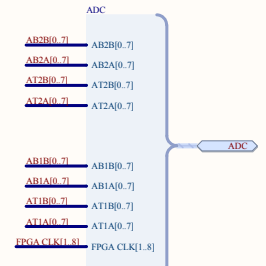
ADC Circuit



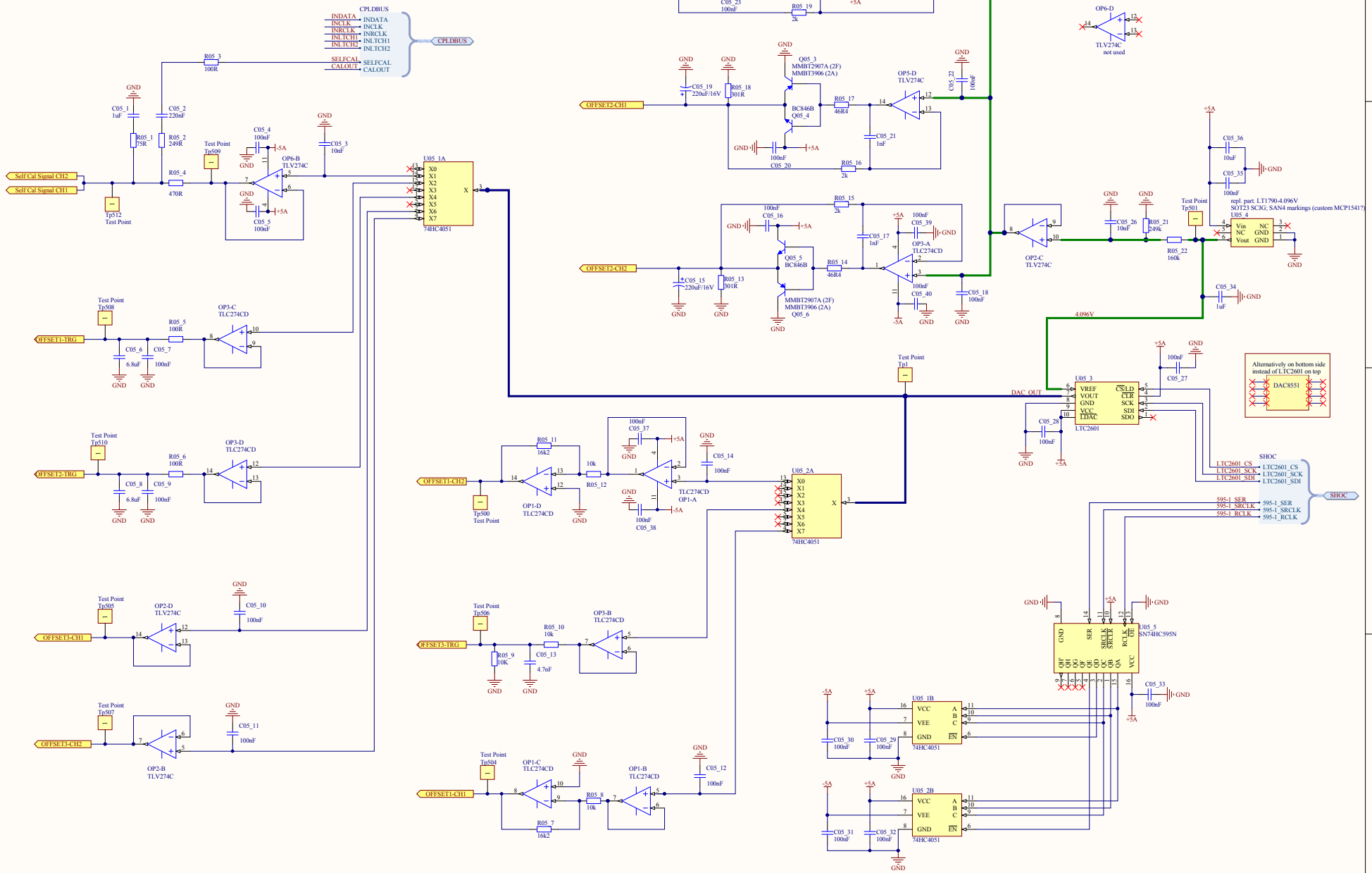
Clocks relative to FPGA Clock input (internal delay ignored)

- FPGA Clock 8 - 0°
- FPGA Clock 7 - 90°
- FPGA Clock 6 - 180°
- FPGA Clock 5 - 270°

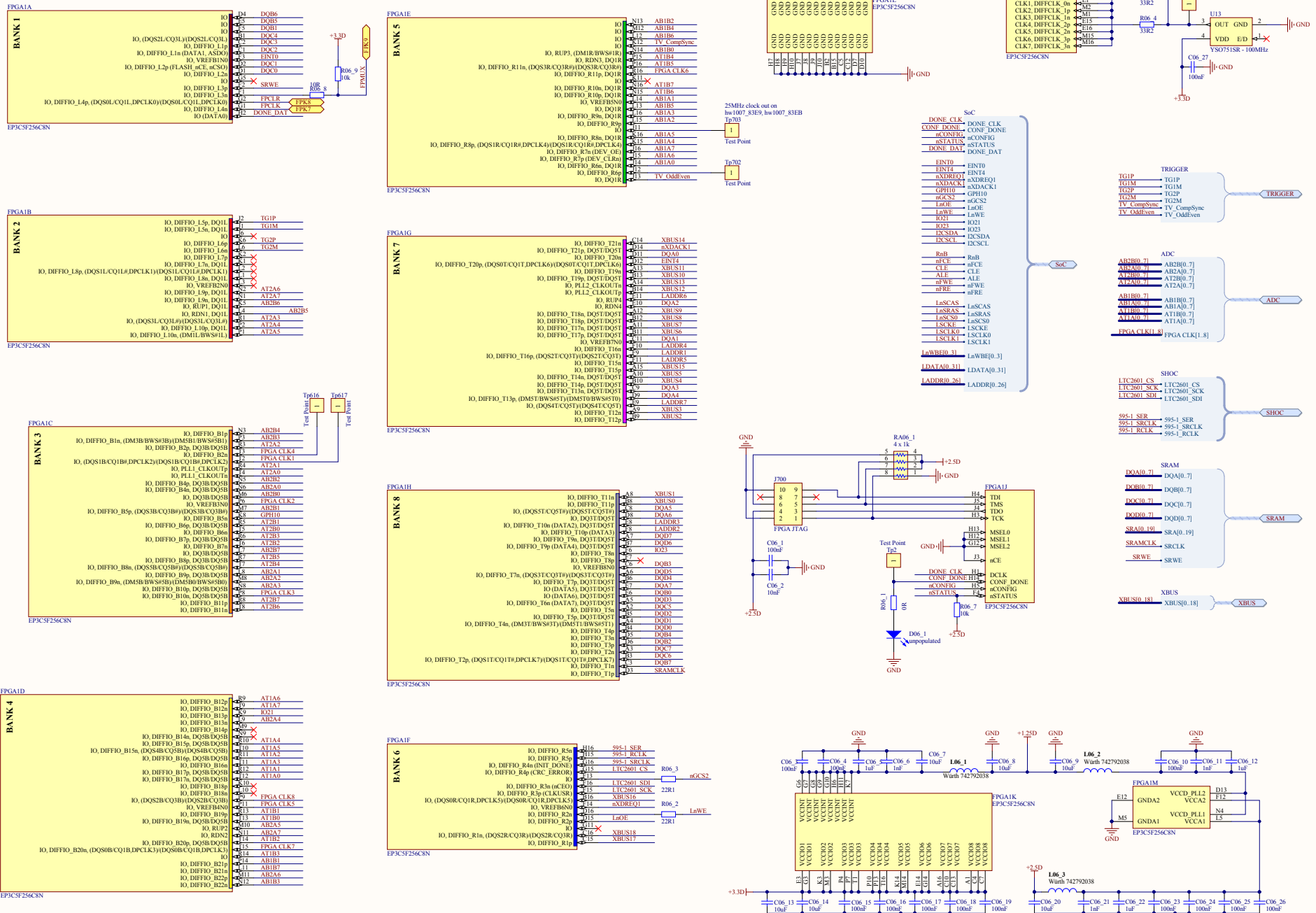
- FPGA Clock 1 - 45°
- FPGA Clock 2 - 135°
- FPGA Clock 3 - 225°
- FPGA Clock 4 - 315°



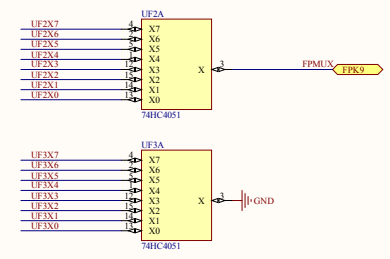
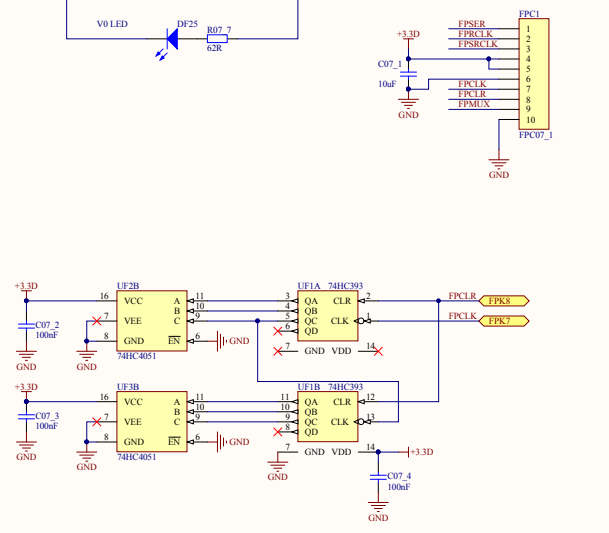
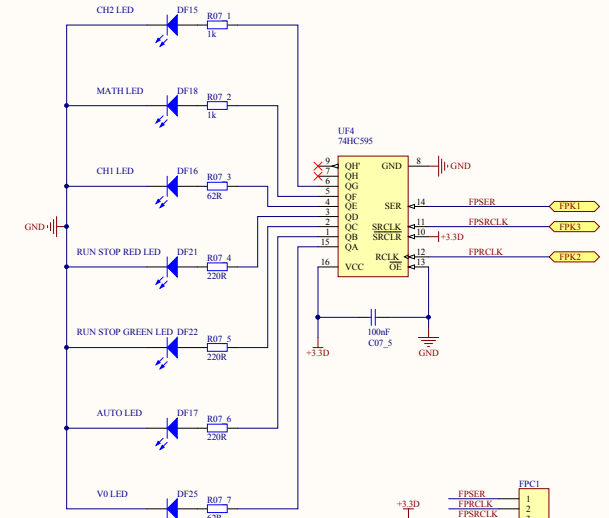
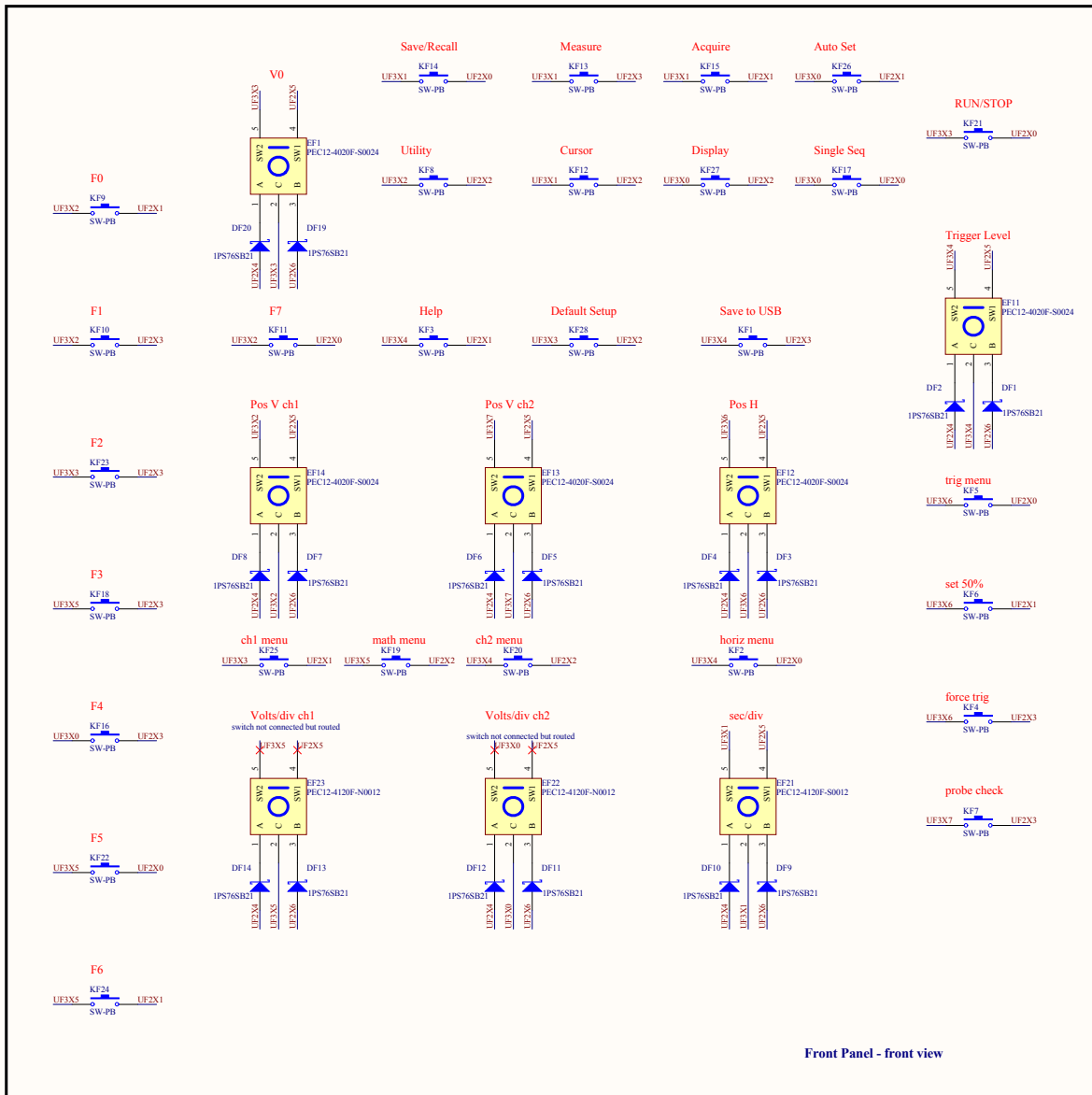
Sample/Hold Offset Control



FPGA Circuit



Front Panel Circuit



CPLD Circuit

A

B

C

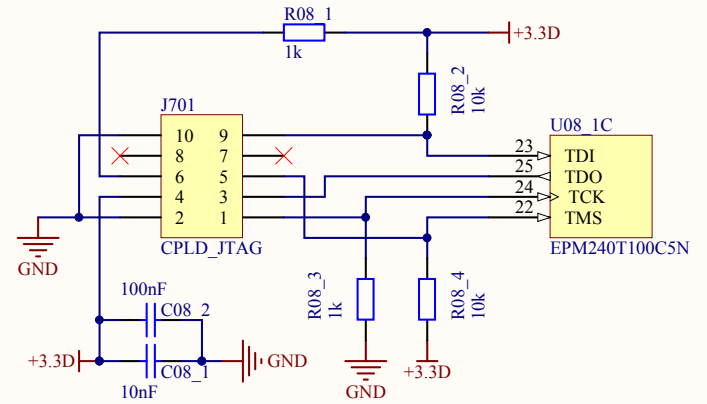
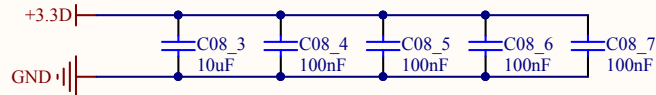
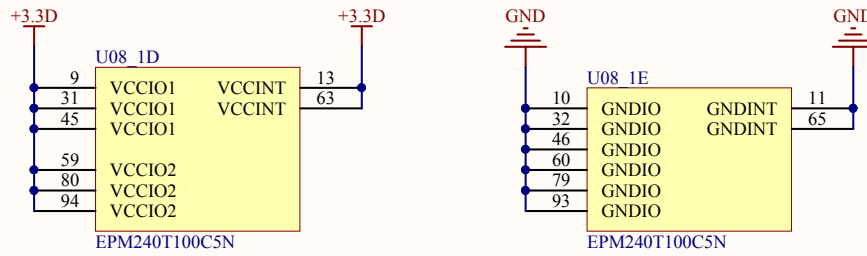
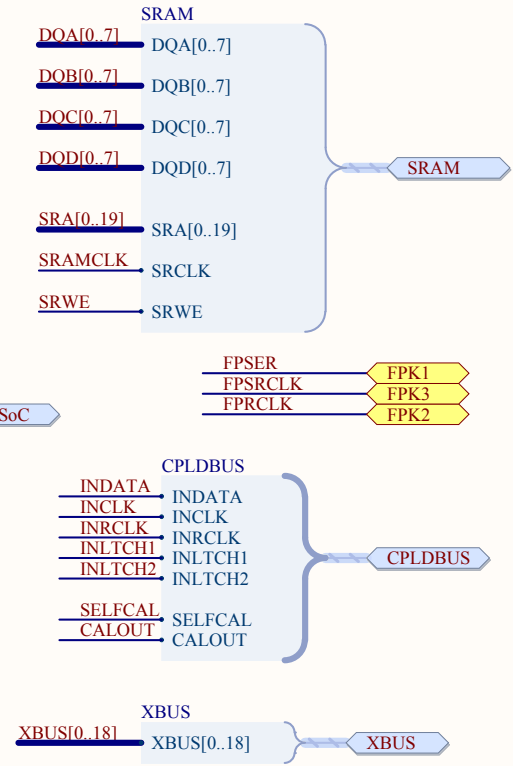
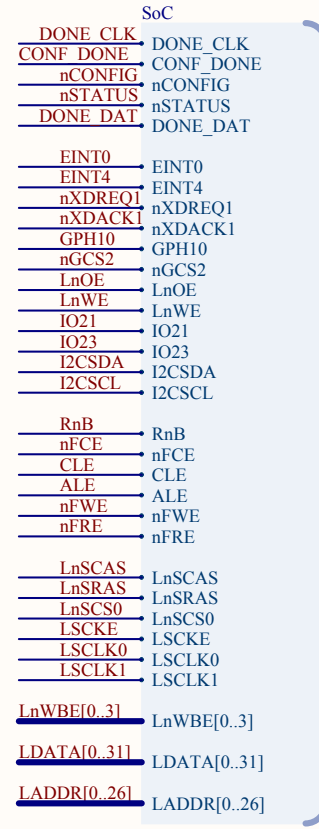
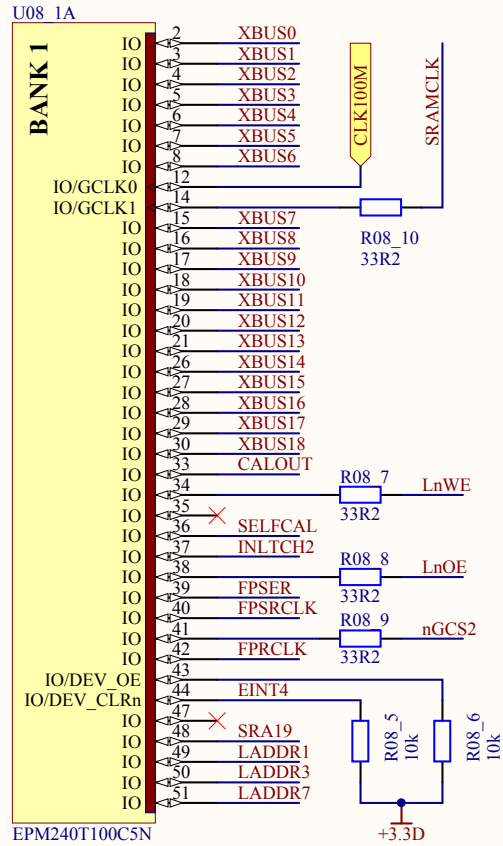
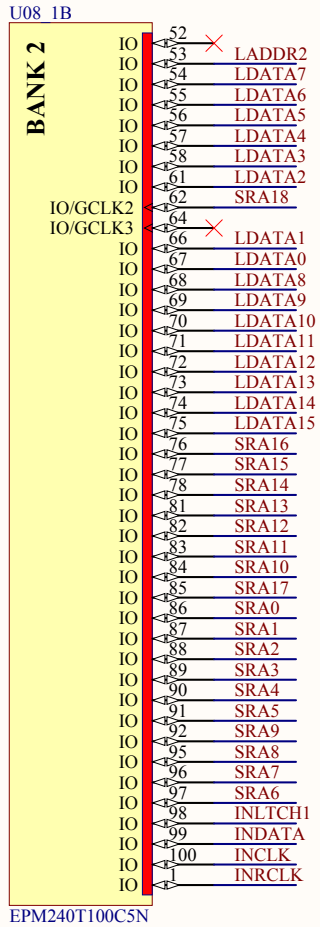
D

A

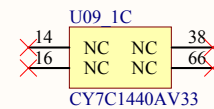
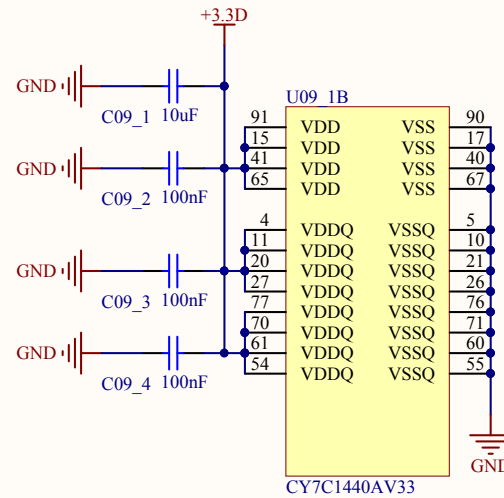
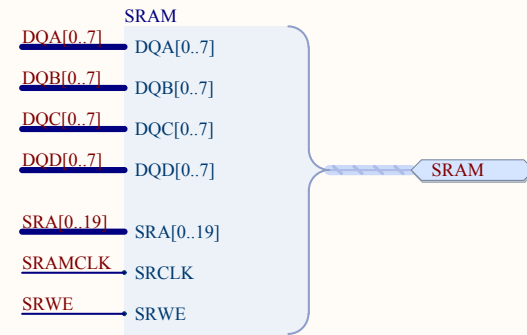
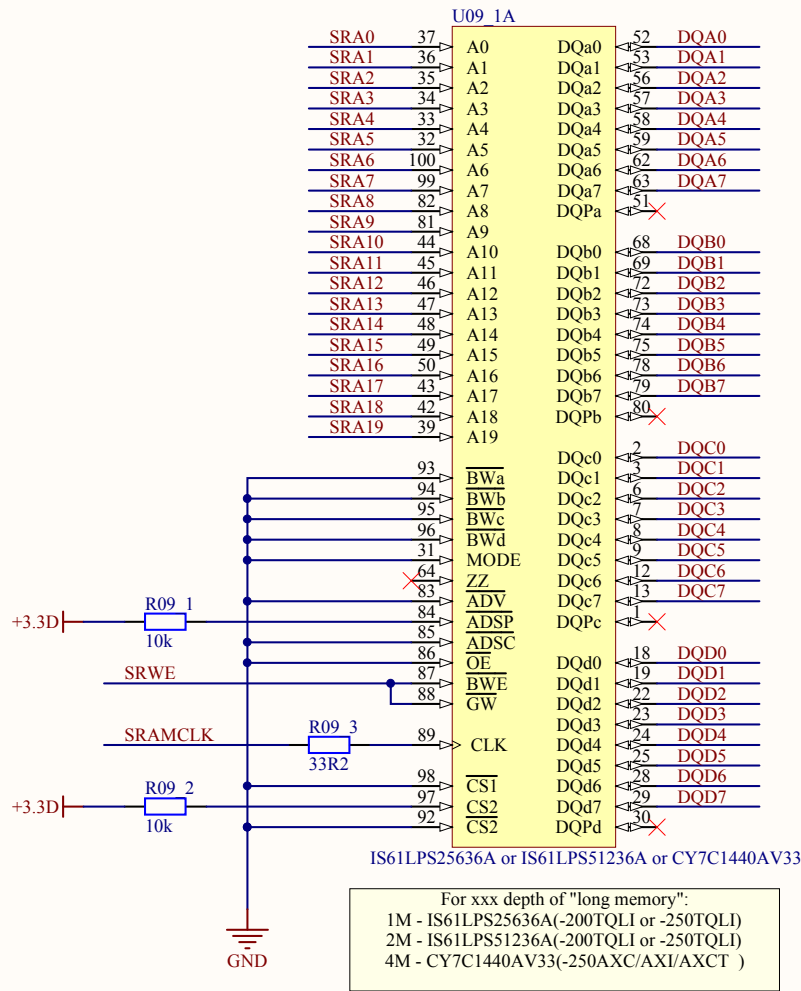
B

C

D



SRAM Circuit



SoC Memory Circuit

A

A

B

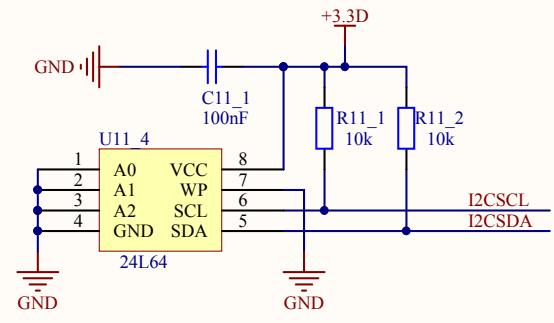
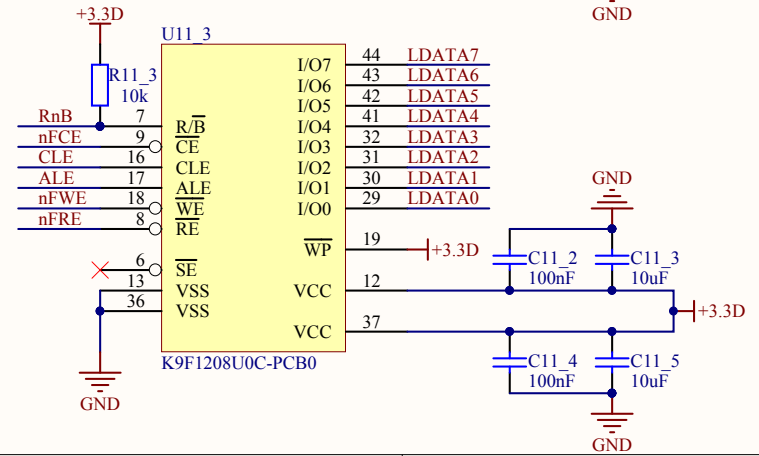
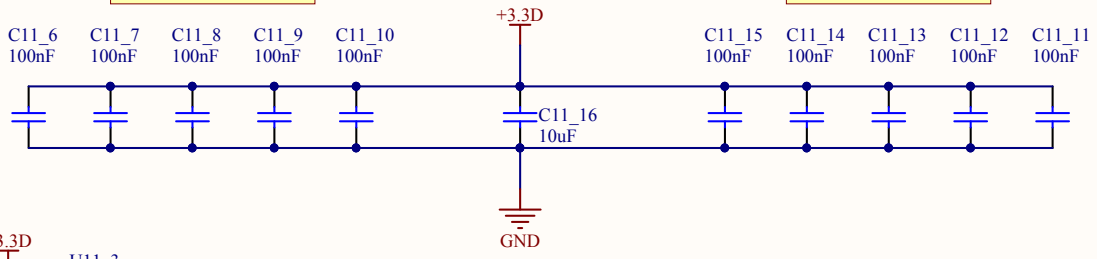
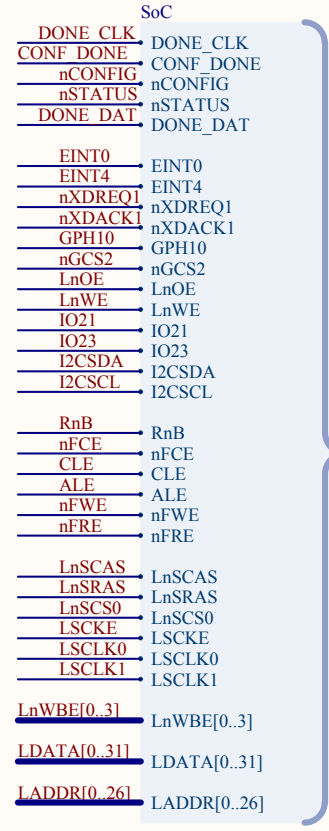
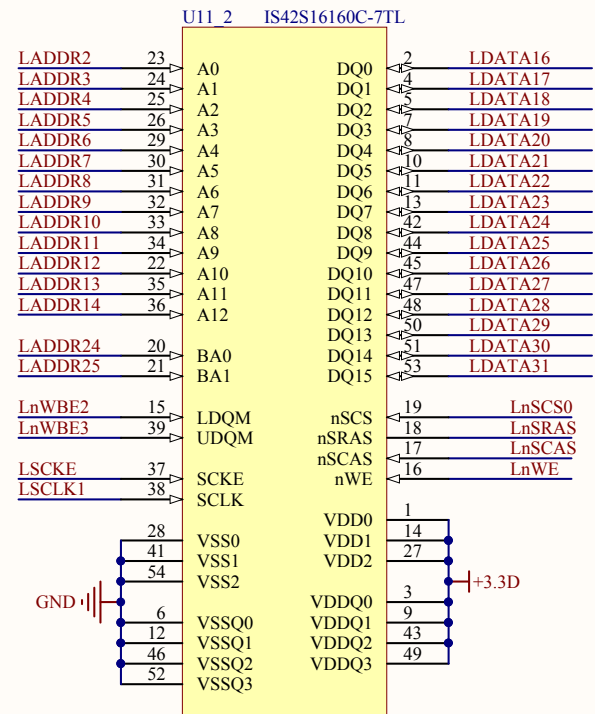
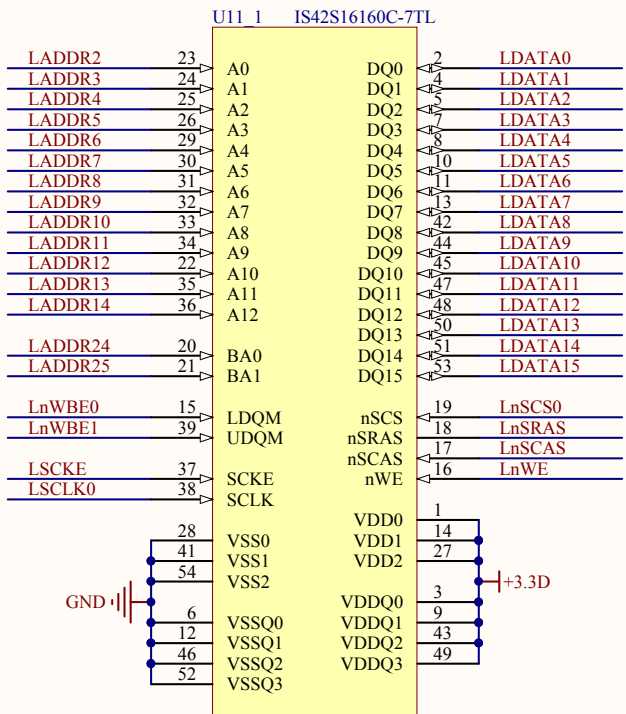
B

C

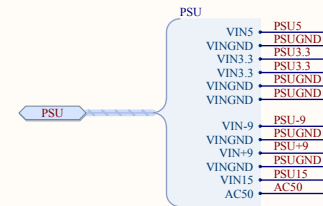
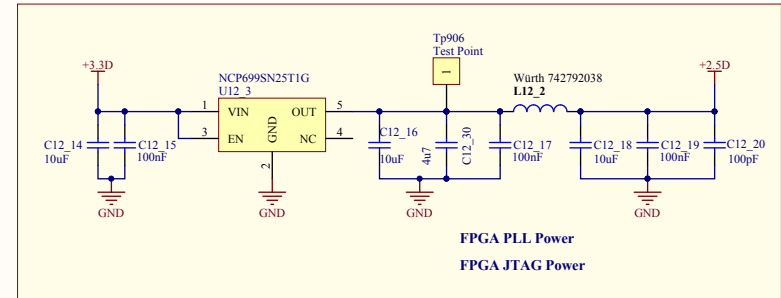
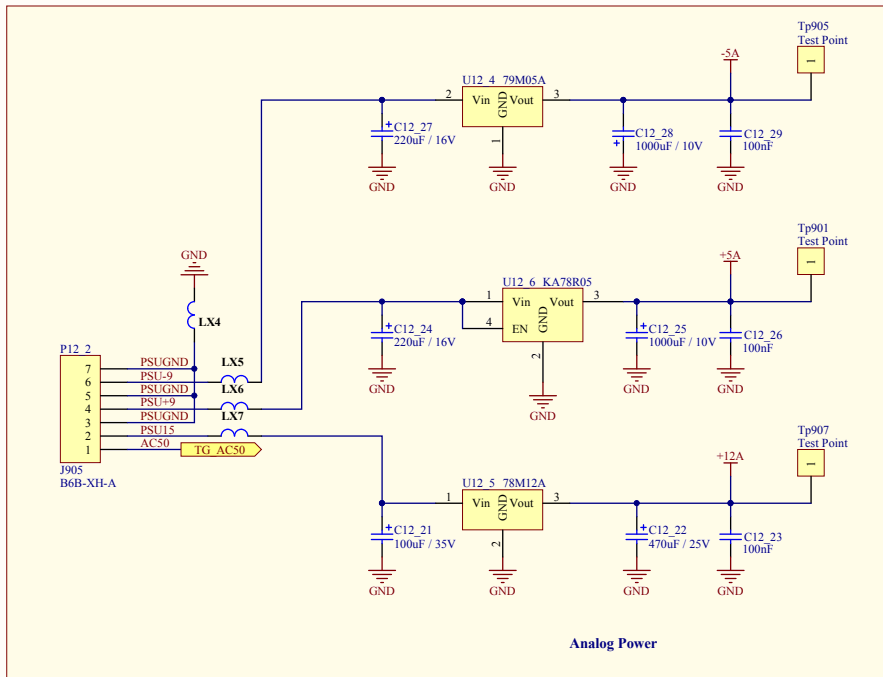
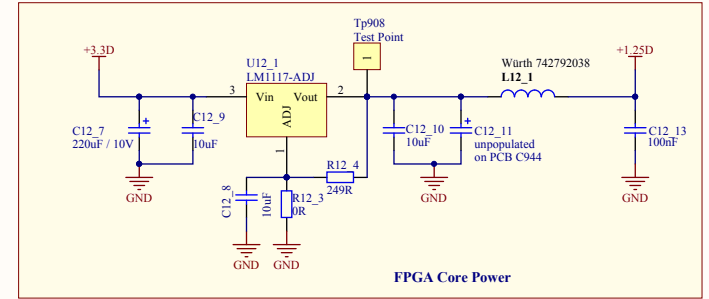
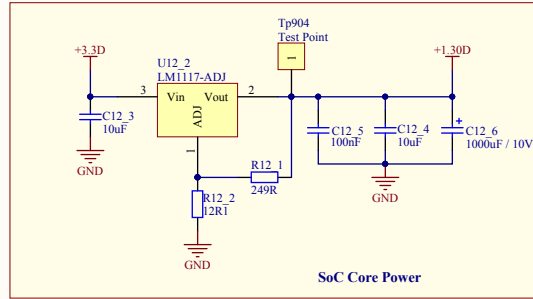
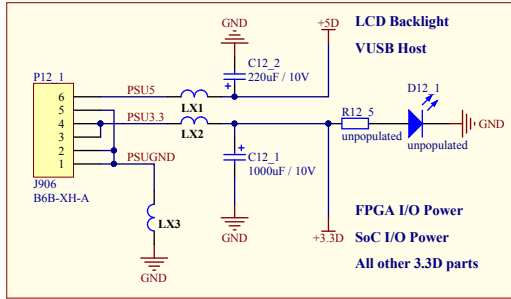
C

D

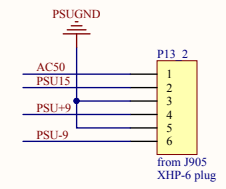
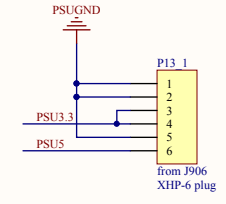
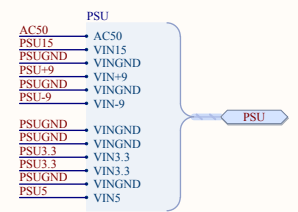
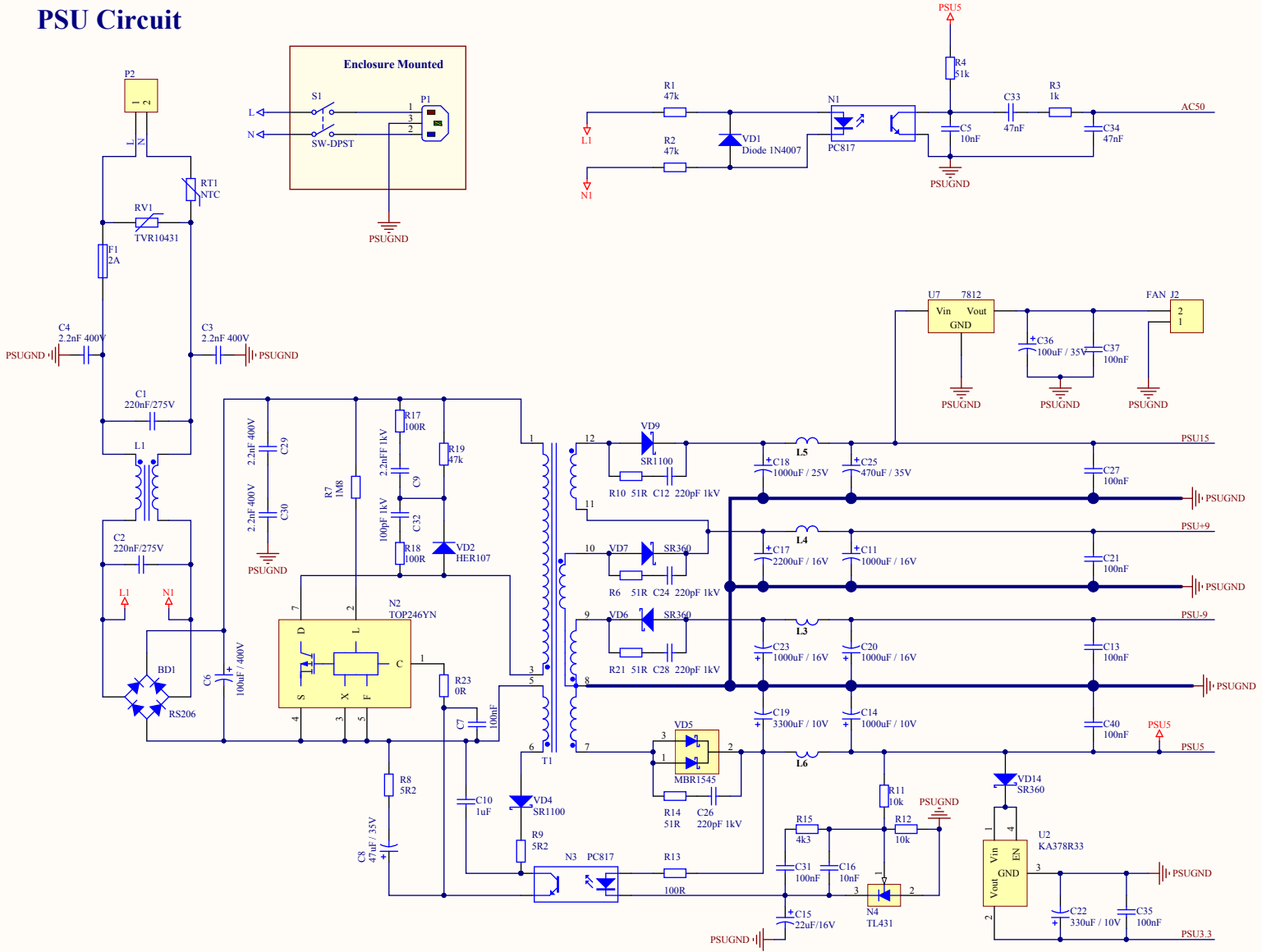
D



Power Distribution Circuit



PSU Circuit



Possible PSU Voltages

PSU5 = 4.9 -> 5.2V (4.98V, 0.5A DSO/MSO, +0.5A for USB)

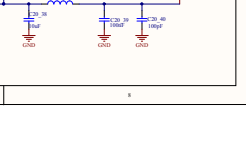
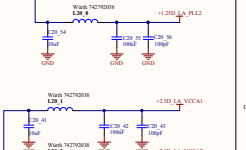
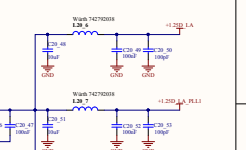
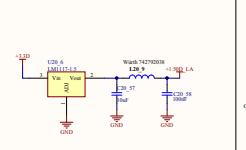
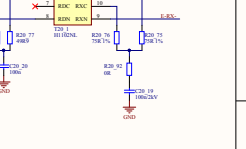
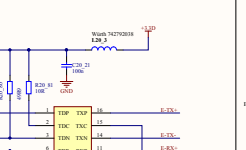
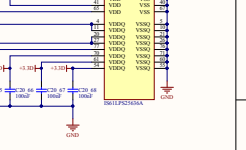
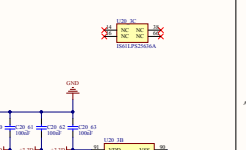
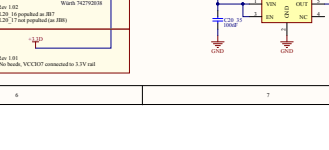
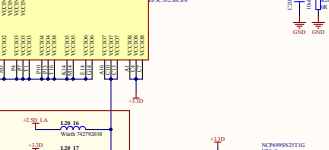
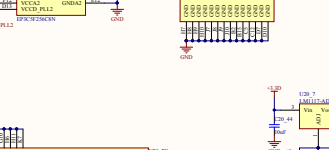
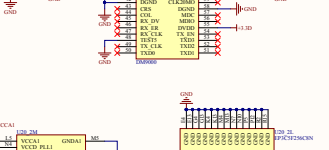
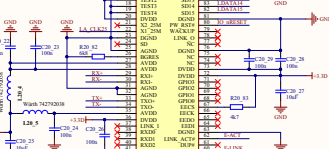
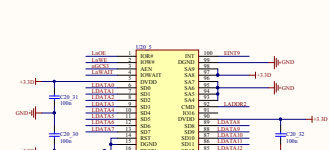
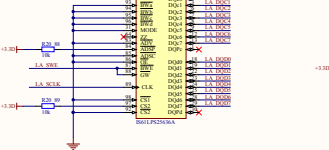
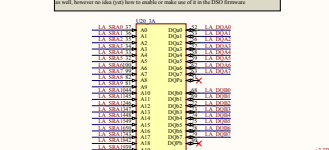
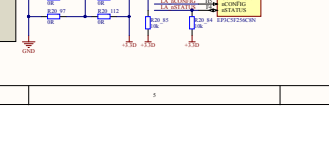
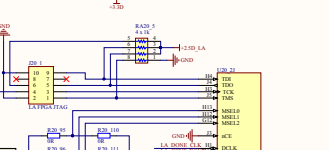
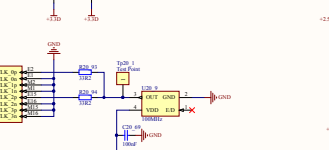
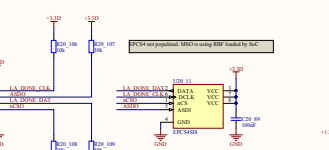
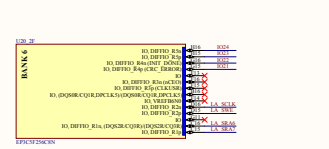
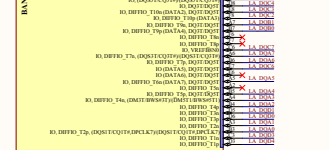
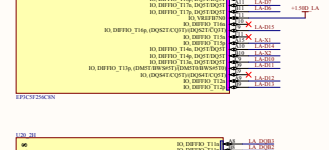
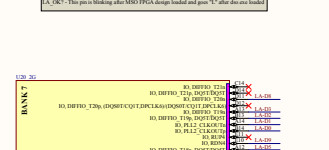
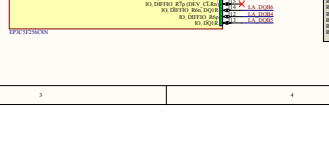
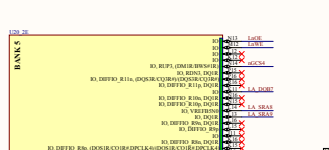
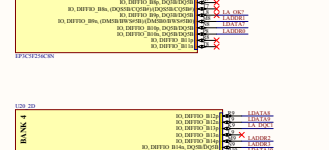
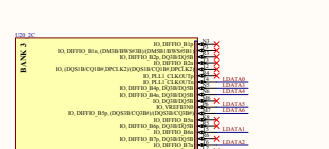
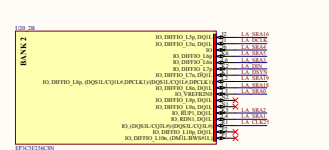
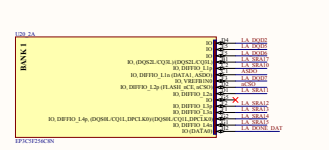
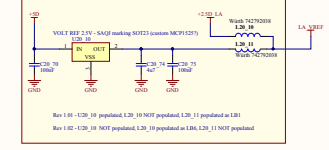
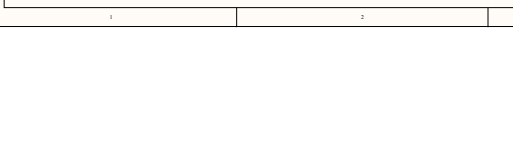
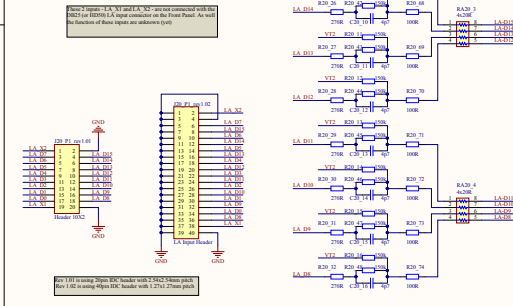
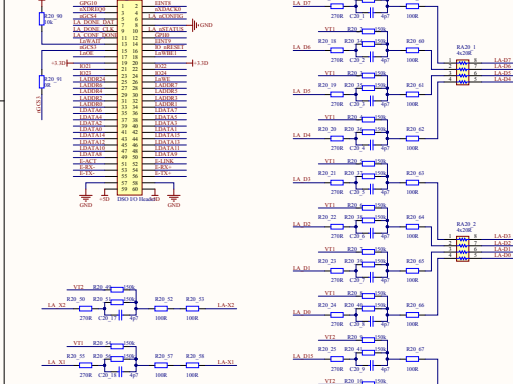
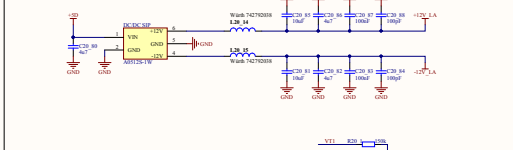
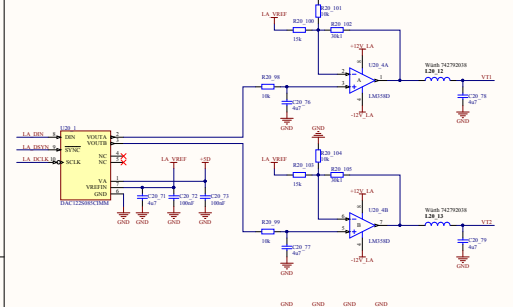
PSU3.3 = 3.2 -> 3.3V (3.15V, up to 1.4A (DSO) and 1.8A (MSO))

PSU+9 = 6.5 -> 9.0V (6.49V, 300mA, up to 450mA during boot !!!)

PSU-9 = -6.5V -> -9.0V (-9.37V, up to 200mA)

PSU15 = 14.0 -> 15.5V (15.95V, up to 100mA)

Option MSO (LAN/LA PCB)



Option LAN

